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Automation Portal	

CLP2_TESTING [CPU 1214C DC/DC/DC]

CLP2_TESTING			
General\Project inform	nation		
Name		Author	LAI-09
Comment	CLP2_TESTING	Slot	1
		SIOT	1
Rack	0		
General\Catalog inform		- -	
Short designation	CPU 1214C DC/DC/DC	Description	Work memory 100 KB; 24VDC power supply with DI14 x 24VDC SINK/ SOURCE, DQ10 x 24VDC and AI2 on board; 6 high-speed counters and 4 pulse outputs on board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 8 signal modules for I/O expansion; 0.04 ms/1000 instructions; PROFINET interface for programming, HMI and PLC to PLC communication
Article number	6ES7 214-1AG40-0XB0	Firmware version	V4.2
General\Identification	& Maintenance		
Plant designation		Location identifier	
Installation date	2023-05-04 14:13:24.297	Additional informa- tion	
General\Checksums			
Text lists	FA 70 E8 75 1D 5A 8E 29	Software	C3 C0 4A 97 11 DC B8 5E
PROFINET interface [X	1]\General		
Name	PROFINET interface_1	Author	LAI-09
Comment			-
	1]\General\Project information		
Name	DI 14/DQ 10_1	Comment	
Name	AI 2_1	Comment	
Name	AQ 1x12BIT_1	Comment	
	1]\General\Catalog information	Comment	
Short designation	AQ1 Signal board	Doscription	Signal board AQ1 x 12 bits; plug-in ter
Short designation	AQT Signal board	Description	minal blocks; output: +/-10V and 0 to 20 mA; configurable diagnostics; configurable substitute output value
Article number	6ES7 232-4HA30-0XB0	Firmware version	V1.0
PROFINET interface [X	1]\Ethernet addresses\Interface netw	orked with	
Subnet:	Not connected		
· ·	1]\Ethernet addresses\IP protocol		
IP configuration	Set IP address in the project	IP address:	150.162.14.24
Subnet mask:	255.255.0.0	Use router	False
·	1]\Ethernet addresses\PROFINET		
PROFINET device name is set directly at the device	False	Generate PROFINET device name automatically	True
	clp2_testing	Converted name:	clp2xbtestingab04
PROFINET device	crpz_testing		,
name:	, = 5		
name: Device number:	0		, s
name: Device number: PROFINET interface [X	0 1]\Time synchronization		
name: Device number: PROFINET interface [X Enable time synchro-	0 1]\Time synchronization Enable time synchronization via NTP		IP addresses
name: Device number: PROFINET interface [X Enable time synchro- nization via NTP serv-	0 1]\Time synchronization Enable time synchronization via NTP		
name: Device number: PROFINET interface [X Enable time synchro- nization via NTP serv- er	0 1]\Time synchronization Enable time synchronization via NTP server	Camina 2	IP addresses
name: Device number: PROFINET interface [X Enable time synchro- nization via NTP serv- er Server 1	0 1]\Time synchronization Enable time synchronization via NTP server 0.0.0.0	Server 2	IP addresses
name: Device number: PROFINET interface [X Enable time synchro- nization via NTP serv- er	0 1]\Time synchronization Enable time synchronization via NTP server	Server 2 Server 4 Empty	IP addresses

CPU synchronizes the modules of the de-	No synchronization		
/ice.			
PROFINET interface [X	1]\Digital inputs\Channel0		
Channel address	10.0	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel0\		
Enable rising edge de tection	-0	RidPrefixRisingEdgeE- vent	49152
Event name:	0	Hardware interrupt:	0
Rising edge0	Rising edge0	naraware interrupt.	<u>o</u>
	[1]\Digital inputs\Channel0\		
Enable falling edge	0	RidPrefixFallingEdg-	49280
detection		eEvent	
Event name:	0	Hardware interrupt:	0
Falling edge0	Falling edge0		
	1]\Digital inputs\Channel1	- -	
Channel address	0.1	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel1\	Did Due fiv Die in a Edua E	40152
Enable rising edge de tection	-0	RidPrefixRisingEdgeE- vent	49153
Event name:	0	Hardware interrupt:	0
Rising edge1	Rising edge1	1	
	1]\Digital inputs\Channel1\		
Enable falling edge	0	RidPrefixFallingEdg-	49281
detection		eEvent	
Event name:	0	Hardware interrupt:	0
Falling edge1	Falling edge1		
PROFINET interface (X Channel address	1]\Digital inputs\Channel2	lumint filtana	6.4 millisec
Enable pulse catch	0	Input filters	6.4 milisec
•	1]\Digital inputs\Channel2\		
Enable rising edge de		RidPrefixRisingEdgeE-	49154
tection		vent	
Event name:	0	Hardware interrupt:	0
Rising edge2	Rising edge2		
PROFINET interface [X	1]\Digital inputs\Channel2\		
Enable falling edge	0	RidPrefixFallingEdg-	49282
detection		eEvent	
Event name:		Hardware interrupt:	0
Falling edge2 PROFINET interface IX	Falling edge2 1]\Digital inputs\Channel3		
Channel address	10.3	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel3\		
Enable rising edge de		RidPrefixRisingEdgeE-	49155
tection		vent	
Event name:	0	Hardware interrupt:	0
Rising edge3	Rising edge3		
	1]\Digital inputs\Channel3\	Dialogative Him and	40202
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49283
Event name:	0	Hardware interrupt:	0
Falling edge3	Falling edge3	naramare micirupt.	1-
	1]\Digital inputs\Channel4		
	10.4	Input filters	6.4 millisec
Channel address	The state of the s		
Channel address Enable pulse catch	0		

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PROFINET interface [X	1]\Digital inputs\Channel4\		
Enable rising edge de- tection	0	RidPrefixRisingEdgeE- vent	49156
Event name:	0	Hardware interrupt:	0
Rising edge4	Rising edge4		-
	1]\Digital inputs\Channel4\		
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49284
Event name:	0	Hardware interrupt:	0
	Falling edge4	maraware interrupt.	U
	1]\Digital inputs\Channel5		
	10.5	Input filters	6.4 millisec
Enable pulse catch	0	input inters	0.4 minisec
	1]\Digital inputs\Channel5\		
Enable rising edge de-		RidPrefixRisingEdgeE-	49157
tection		vent	
Event name:	0	Hardware interrupt:	0
	Rising edge5		
	1]\Digital inputs\Channel5\	pidow C = U = 1	40205
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49285
Event name:	0	Hardware interrupt:	0
	Falling edge5		
PROFINET interface [X	1]\Digital inputs\Channel6		
Channel address	10.6	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel6\		
Enable rising edge de- tection	0	RidPrefixRisingEdgeE- vent	49158
Event name:	0	Hardware interrupt:	0
Rising edge6	Rising edge6		
PROFINET interface [X	1]\Digital inputs\Channel6\		
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49286
Event name:	0	Hardware interrupt:	0
Falling edge6	Falling edge6		-
	1]\Digital inputs\Channel7		
Channel address	10.7	Input filters	6.4 millisec
Enable pulse catch	0	-	
<u> </u>	1]\Digital inputs\Channel7\		
Enable rising edge de- tection		RidPrefixRisingEdgeE- vent	49159
Event name:	0	Hardware interrupt:	0
Rising edge7	Rising edge7	iniawaic intellupt.	
	1]\Digital inputs\Channel7\		
Enable falling edge	0	RidPrefixFallingEdg-	49287
detection	0	eEvent	0
Event name:	0 Falling adge7	Hardware interrupt:	0
	Falling edge7		
	1]\Digital inputs\Channel8 1.0	Input filtors	6.4 millisec
	0	Input filters	0.4 IIIIIISEC
Enable pulse catch	U 1]\Digital inputs\Channel8\		
Enable rising edge de-		RidPrefixRisingEdgeE-	19160
Enable rising edge de- tection		vent	100 EF
		ACIIC	
Event name:	0	Hardware interrupt:	0

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	1]\Digital inputs\Channel8\	D' 1D (' E II' E I	10200
Enable falling edge detection	0	RidPrefixFallingEdg-	49288
	0	eEvent	0
Event name:	0	Hardware interrupt:	0
	Falling edge8		
	1]\Digital inputs\Channel9	- -	
	11.1	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel9\		
Enable rising edge de-	0	RidPrefixRisingEdgeE-	49161
tection	-	vent	
Event name:	0	Hardware interrupt:	0
	Rising edge9		
	1]\Digital inputs\Channel9\		
Enable falling edge	0	RidPrefixFallingEdg-	49289
detection		eEvent	
Event name:	0	Hardware interrupt:	0
	Falling edge9		
	1]\Digital inputs\Channel10		
Channel address	11.2	Input filters	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X	1]\Digital inputs\Channel10\		
Enable rising edge de-		RidPrefixRisingEdgeE-	49162
tection		vent	
Event name:	0	Hardware interrupt:	0
Rising edge10	Rising edge10		
	1]\Digital inputs\Channel10\		
	0	RidPrefixFallingEdg-	49290
detection		eEvent	
Event name:	0	Hardware interrupt:	0
Falling edge10	Falling edge10		
	1]\Digital inputs\Channel11		
	11.3	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel11\		
Enable rising edge de-		RidPrefixRisingEdgeE-	49163
tection		vent	13103
Event name:	0	Hardware interrupt:	0
Rising edge11	Rising edge11	naraware interrupt.	
	1]\Digital inputs\Channel11\		
Enable falling edge	0	RidPrefixFallingEdg-	49291
detection		eEvent	15251
Event name:	0	Hardware interrupt:	0
Falling edge11	Falling edge11	aramare interrupt.	<u> </u>
	1]\Digital inputs\Channel12		
	11.4	Input filters	6.4 millisec
	0	mput miers	U.T IIIIIISEC
Enable nulse satab	(C)		
Enable pulse catch	<u> </u>		
PROFINET interface [X	1]\Digital inputs\Channel13		C 4'II'.
PROFINET interface [X Channel address	1]\Digital inputs\Channel13 1.5	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch	1]\Digital inputs\Channel13 1.5 0	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X	1]\Digital inputs\Channel13 1.5 0 1]\Analog inputs\Noise reduction	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time	1]\Digital inputs\Channel13 I1.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms)	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0		
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time	1]\Digital inputs\Channel13 I1.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms)	Input filters Measurement type	6.4 millisec Voltage
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0	Measurement type Smoothing	Voltage Weak (4 cycles)
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X Channel address	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0 IW64	Measurement type	Voltage Weak (4 cycles)
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X Channel address Voltage range	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0 IW64	Measurement type Smoothing	Voltage Weak (4 cycles)
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X ntegration time PROFINET interface [X Channel address Voltage range Empty	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0 IW64	Measurement type Smoothing Enable overflow diag-	Voltage Weak (4 cycles)

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			<u> </u>
/oltage range	010 V	Smoothing	Weak (4 cycles)
Empty		Enable overflow diag- nostics	1
PROFINET interface			
	P Use substitute value		
PROFINET INTERFACE Channel address	[X1]\Digital outputs\Channel0 O0.0	Substitute a value of	0
Criainiei address	Q0.0	1 on a change from RUN to STOP.	
PROFINET interface	[X1]\Digital outputs\Channel1		
Channel address	Q0.1	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel2		
Channel address	Q0.2	Substitute a value of 1 on a change from RUN to STOP.	0
PROFINET interface	[X1]\Digital outputs\Channel3		
Channel address	Q0.3	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel4		
Channel address	Q0.4	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel5		
Channel address	Q0.5	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel6		
Channel address	Q0.6	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel7		
Channel address	Q0.7	Substitute a value of 1 on a change from RUN to STOP.	0
PROFINET interface	[X1]\Digital outputs\Channel8		
Channel address	Q1.0	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel9		
Channel address	Q1.1	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Operating mode		
IO controller	True	IO system	
Device number	0	IO device	False
	[X1]\Analog outputs		
	P Use substitute value [X1]\Analog outputs\Channel0		
Channel address	QW80	Analog output type	Voltage
Voltage range	+/- 10 V	Substitute value for channel on a change from RUN to STOP	0.000V
Empty		Enable short circuit diagnostics	1
Enable overflow diag	g- 1	Enable underflow diagnostics	1
	[X1]\I/O addresses\Input addresse		
Start address	0.0	End address	1.7

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Organization block	0	Process image	0
	(1]\I/O addresses\Input addresses		
Start address	64	End address	67
Organization block	0	Process image	0
PROFINET interface [X	(1]\I/O addresses\Output addresses		
Start address	0.0	End address	1.7
Organization block	0	Process image	0
ROFINET interface [X	(1]\I/O addresses\Output addresses		
Start address	80	End address	81
Organization block	0	Process image	0
ROFINET interface [X	(1]\Advanced options\Interface optio	ons	
Support device re- placement without exchangeable medi-	True	Permit overwriting of device names of all assigned IO devices	False
um Use IEC V2.2 LLDP	False	Keep-Alive connec-	30s
mode	(1]\Advanced options\Real time setti	tion monitoring:	
Send clock:	1.000ms	ngsho communication	
	1.000ms (1]\Advanced options\Real time setti	ngs\Pool time ontions	
Calculated bandwidth for cyclic IO data:		Calculated bandwidth for cyclic IO data:	0.000%
	[[1]\Advanced options\Port [X1 P1]\Ge		
Name	Port 1	Author	LAI-09
Comment			<u> </u>
	 {1]\Advanced options\Port [X1 P1]\Po	ort interconnection\l ocal	nort:
Local port:	CLP2_TESTING\PROFINET interface_1	Medium:	Copper
Cable name:	[X1]\Port_1 [X1 P1 R]	Wicdiani.	СОРРСІ
11			
	504.		
PROFINET interface [X	(1]\Advanced options\Port [X1 P1]\Po		· ·
PROFINET interface [X	Monitoring of partner port is not poss		er port: Any partner
	Monitoring of partner port is not possible	si- Partner port:	· ·
PROFINET interface [X	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	si- Partner port:	· ·
PROFINET interface [X Activate this port for	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	si- Partner port:	· ·
PROFINET interface [X Activate this port for use	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	ort options\Activate	· ·
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate /	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	ort options\Activate	· ·
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia-	Monitoring of partner port is not possible [1]\Advanced options\Port [X1 P1]\Po True [1]\Advanced options\Port [X1 P1]\Po	ort options\Activate ort options\Connection	Any partner
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True	ort options\Activate ort options\Connection Monitor	Any partner
PROFINET interface [X Activate this port for use PROFINET interface [X Iransmission rate / duplex: Enable autonegotia- tion PROFINET interface [X	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices End of the sync do- main	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False False	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices End of the sync do-	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False False	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices End of the sync do- main	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False False (1]\Web server access	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner False

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High speed counters (HSC)\HSC1\General\Enable		
Enable this high	0	Enable this high	0
speed counter		speed counter	
Enable this high	0	Enable this high	0
speed counter		speed counter	
Enable this high	0	Enable this high	0
speed counter		speed counter	
	HSC)\HSC1\General\Project information	TI TO THE PARTY OF	
Name	HSC_1	Comment	
Name	HSC_2	Comment	
Name	HSC_3	Comment	
Name	HSC_4	Comment	
Name	HSC_5	Comment	
Name	HSC_6	Comment	
	HSC)\HSC1\I/O addresses\Input addre	1	
Start address	1000.0	End address	1003.7
Start address	1004.0	End address	1007.7
Organization block	0	Start address	1008.0
End address	1011.7	Organization block	0
Process image	0	Start address	1012.0
End address	1015.7	Organization block	0
Process image	0	Start address	1016.0
End address	1019.7	Organization block	0
Process image	0	Start address	1020.0
End address	1023.7	Organization block	0
Process image	0	Organization block	0
Process image	0	Process image	0
	/PWM)\PTO1/PWM1\General\Enable		
Enable this pulse gen-	0	Enable this pulse gen-	0
erator		erator	
Pulse generators (PTO	/PWM)\PTO1/PWM1\General\Project i	nformation	
Name	Pulse_1	Comment	
		Comment	
Name	Pulse_2		
Pulse generators (PTO	/PWM)\PTO1/PWM1\I/O addresses\Ou	•	
Pulse generators (PTO Start address	/PWM)\PTO1/PWM1\I/O addresses\Ou ⁻ 1000.0	End address	1001.7
Pulse generators (PTO Start address Start address	/PWM)\PTO1/PWM1\I/O addresses\Ou	End address End address	1003.7
Pulse generators (PTO Start address Start address Organization block	/PWM)\PTO1/PWM1\I/O addresses\Ou ⁻ 1000.0	End address End address Organization block	
Pulse generators (PTO Start address Start address	/PWM)\PTO1/PWM1\I/O addresses\Ou [*] 1000.0 1002.0	End address End address	1003.7
Pulse generators (PTO Start address Start address Organization block Process image Startup	/PWM)\PTO1/PWM1\I/O addresses\Ou [*] 1000.0 1002.0 0	End address End address Organization block Process image	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER	End address End address Organization block Process image Comparison preset to	1003.7
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF	End address End address Organization block Process image Comparison preset to actual configuration	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER	End address End address Organization block Process image Comparison preset to actual configuration OBs should be inter-	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF	End address End address Organization block Process image Comparison preset to actual configuration	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be inter-	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle	/PWM)\PTO1/PWM1\I/O addresses\Our 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy-	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be inter-	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy- cle time for cyclic OBs	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy- cle time for cyclic OBs Communication load Cycle load due to	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy- cle time for cyclic OBs Communication load Cycle load due to communication	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy- cle time for cyclic OBs Communication load Cycle load due to communication System and clock men	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of sys-	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx)	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of sys-	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status	1003.7 0 0 Startup CPU even if mismatch 1 1ms
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup After POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte First cycle	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status changed	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte First cycle	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0 20% nory\System memory bits 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte First cycle	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status changed	1003.7 0 0 Startup CPU even if mismatch 1 1ms

Automation Porta	l I					
10 Hz clock			5 Hz clock			
2.5 Hz clock						
			2 Hz clock			
1.25 Hz clock			1 Hz clock			
0.625 Hz clock			0.5 Hz clock			
Web server\General			11			
Activate Web server on all modules of thi			Permit access only with HTTPS	True		
device	3		WIGHTHES			
Web server\Automat	ric undate					
Enable automatic up			Update interval	Os		
date Web server\User inte	erface languages					
Assign project langu			User interface langu	ages		
English (United States			German			
English (United States	·		English			
English (United States	·		French			
English (United States			Spanish			
English (United States	<u> </u>		Italian			
English (United States			Chinese (simplified)			
-			crimese (simplified)			
Web server\User ma	nagement					
User name			User rights			
Everybody						
Web server\User-def Application name		Default HTML page	Files with dynamic	Wah DP number	Fragment DB num	
Application hame	HTML Source path	Default HTML page	content	Web DB Humber	ber	
		index.htm	.htm;.html	333	334	
Web server\Overviev	v of interfaces					
Device		Interface		Enabled web serve	er access	
CLP2 TESTING		PROFINET interface_	1	False		
User interface langu	anes	_				
Assign project langu			User interface langu	1200		
			_	lages		
English (United States			German			
F I'-l- /I I -'+ -l C+ +	. 1		English			
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English (United States	5)		French			
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Totally Integrated Automation Portal						
Ductaction C. Coccusit AC	accusitor account				L	
events in case of high	True		Length of a	n interval	20	
message volume Unit	seconds					
Protection & Security\E						
Disable copying from internal load memory to external load mem-						
ory	- 4					
	Configuration control for	central conf	riguration			
Allow to reconfigure the device via the	0					
user program						
Connection resources\						
	Station resources - Re- served - Maximum	Station reso served - Co		Station res namic - Co		Module resources - CLP2_TESTING [CPU 1214C DC/DC/DC] - Con- figured
Maximum number of re-	-	62		6		68
sources:	Maximum	Configured		Configured		Configured
PG communication: HMI communication:	12	0		0		0
S7 communication:	8	0		0		0
Open user communication:	8	0		0		0
Web communication:	30	-		-		-
Other communication:	-	-		0		0
Total resources used:		0		0		0
Available resources:		62		6		68
Overview of addresses	Overview of addresses\	Overview of	addresses			
•	True		Outputs		True	
Address gaps	False		Slot		True	

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Totally Integrat	ed
Automation Por	tal

Туре	Addr.	Addr. to	Module	PIP	Device	Device	Size	Master /	Rack	Slot
1960	from	, tudi. to			name	number		IO system	Nuck	
	0	1	DI 14/DQ 10_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
Ο	0	1	DI 14/DQ 10_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	_	2 Bytes	-	0	1 1
l	64	67	AI 2_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 21
O	1000	1001	Pulse_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 32
O	1002	1003	Pulse_2	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 33
Ο	1004	1005	Pulse_3	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 34
O	1006	1007	Pulse_4	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 35
Ο	80	81	AQ 1x12BIT_1		CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 3
I	8	8	DI 8/DQ 8x24VDC_ 1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	1 Bytes	-	0	2

Totall Autor	y Integrate nation Port	ed cal								
pe	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
	8	8	DI 8/DQ 8x24VDC_ 1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	1 Bytes	-	0	2

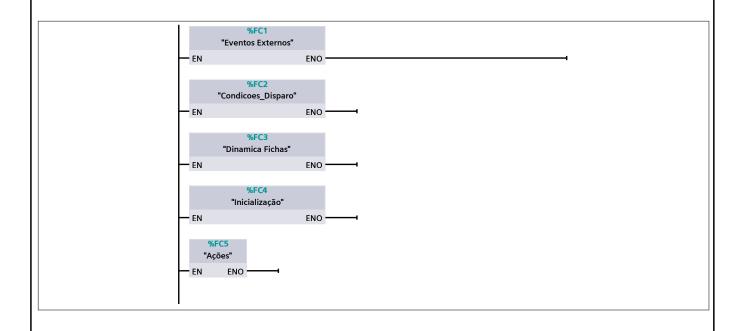
CLP2_TESTING [CPU 1214C DC/DC/DC] / Program blocks

Main [OB1]

Main Propert	ies			
General				
Name	Main	Number	1	Type OB
Language	LAD	Numbering	Automatic	
Information				
Title	"Main Program Sweep (Cycle)"	Author		Comment
Family		Version	0.1	User-defined ID

Name	Data type	Default value	Comment
▼ Input			
Initial_Call	Bool		Initial call of this OB
Remanence	Bool		=True, if remanent data are available
Temp			
Constant			

Network 1:



Automatio	egrated In Portal							
			4C D	C/DC	[/DC] / Pr	ogram	blocks	
Eventos E								
Eventos Exter General	rnos Properti	es						
Name	Eventos Ext	ernos	Numbe	er	1		Туре	FC
Language	LAD		Numbe	ering	Automatic			
Information Title			Author				Comment	
Family			Versio		0.1		User-defined ID	
Name	<u>'</u>	Data ty	/pe	Defaul	t value	Comm	ent	
Input								
Output								
InOut								
Temp								
Constant								
▼ Return								
Evento	s Externos	Void						

```
Totally Integrated
Automation Portal
                                            %I0.0
"PART_AV"
                                                                                                                                        %M0.0
"Chega_Peça"
                                               -|₽|-
                                                                                                                                             -( )-
                                             %M57.0 "void"
                                                                                                                                          %M0.1
"Subida_
Liberada"
                                              %I0.2
"B4"
                                                                                                                                           %M0.3
                                              %I0.4
"1B1"
                                                                                                                                       "Elevador_Em_
Cima"
                                              \dashvP\vdash
                                                                                                                                             \leftarrow
                                             %M57.1 "void2"
                                                                                                                                            %M0.4
                                              %I0.5 "1B2"
                                                                                                                                          "Elevador_
Embaixo"
                                                                                                                                             4  
                                              %I0.6 "2B1"
                                                                                                                                           %M0.5
                                                                                                                                       "Pistão_Recuou"
                                               -|₽|-
                                                                                                                                            \leftarrow
                                             %M58.1
                                              "Tag_1"
                                              %I0.6 "2B1"
                                                                                                                                           %M58.2
                                                                                                                                      "Pistão_Recuado"
                                               4 F
                                                                                                                                             %I0.3 "B5"
                                                                                                                                           %M0.6
                                                                                                                                        "Altura_Certa"
                                                                                                                                             -( )-
                                               -| |-
                                                                                                                                           %M0.7
                                              %I0.7
"IP_FI"
                                                                                                                                       "Prox.Bancada_
Liberou"
                                               <del>1</del>/}
                                                                                                                                             ()
                                           %I8.0
"Bot_Start"
                                                                                                                                           %M1.0 "Start"
                                               -|₽|-
                                                                                                                                             ( )-
                                             %M57.5 "void6"
                                              %18.3
                                                                                                                                            %M1.1
                                           "Bot_Reset"
                                                                                                                                            "Reset"
                                               \dashvP\vdash
                                             %M57.6
                                              "void7"
                                                                                                                                           %M1.2 "Stop"
                                              %18.1
                                            "Bot_Stop"
                                              H۱۲
                                                                                                                                             -( )-
                                             %M57.7
                                              "void8"
```

CLP2_TE			4C D	C/D(C/DC] / P	rogram	blocks		
Condicoe									
Condicoes_Di General	isparo Proper	ties							
Name	Condicoes_	Disparo	Numbe		2		Туре	FC	
Language	LAD		Numbe	ering	Automatic				
Information Title			Author	r			Comment		
Family			Versio		0.1		User-defined ID		
Name		Data ty	ype	Defaul	t value	Comm	ent		
Input									
Output									
InOut									
Temp									
Constant									
Return									
Condico	oes_Disparo	Void							

Totally Integrated Automation Portal Network 1: (1.1 / 3.1) %M2.6 %M0.0 %M8.1 "Elevador_Livre" "Chega_Peça" "T0" 4 + 4 F () %M0.1 "Subida_ Liberada" **%M4.0** "T1" %M2.7 "Tem_Peça" **H** F **H** F () %DB1 "IEC_Timer_0_ DB_2" %M2.0 TON "Elevador_ Subindo" %M4.1 Time "T2" **H** F Q IN T#5S — PT ET — T#0ms %M0.7 "Prox.Bancada_ **%M6.2** "T10" Liberou" ()-%M3.2 **%M4.3** "T4" %M2.1 %M3.0 "Prox_Bancada_ %M0.6 "Testando_Altura" "Altura_Certa" "Rampa_Desl" Livre" **H** F 1 F ()-%M0.6 %M4.2 "Altura_Certa" |/} ()-%M2.2 "Elevador_ Descendo_c_ %M0.4 "Elevador_ Embaixo" %M4.6 Peça" "T7" 4 F %M2.5 "Descartando_ Peça" %M0.5 %M6.0 "Pistão_Recuou" +%M2.3 "Empurrando_ %M0.5 %M4.4 Peça" "Pistão_Recuou" "T5" 4 6 () %M2.4 "Elevador_ Descendo_ Vazio" %M0.4 "Elevador_ Embaixo" %M4.5 "T6" \dashv \vdash %DB2 "IEC_Timer_0_DB" TON **%M6.1** "T9" %M3.1 Time "Rampa_Lig" | | | () IN T#3000MS — ET — T#0ms PT %M1.0 %M3.5 %M6.3 "Pronto" "Start" "T11"

2.1 (Page4 - 3)

Totally Integrated **Automation Portal** Network 1: (2.1 / 3.1) 1.1 (Page4 - 2) îř **%M1.2** "Stop" **%M6.4** "T12" %M2.7 "Tem_Peça" 4 F ()-%M2.0 "Elevador_ Subindo" **%M6.5** "T13" **%M2.1**"Testando_Altura" **%M6.6** "T14" | | | ()-%M2.2 "Elevador_ Descendo_c_ Peça" **%M6.7** "T15" 4 F **(**)-%M2.3 "Empurrando_ Peça" **%M7.0** "T16" ()-**%M7.1** "T17" %M3.1 "Rampa_Lig" **()** %M3.4 "Resetando_ Elevador" **%M7.2** "T18" +()-%M2.4 "Elevador_ Descendo_ Vazio" **%M7.3** "T19" () %M2.5 "Descartando_ Peça" **%M7.4** "T20" **(**)-**%M7.7** "T23" %M3.5 "Pronto" **H** F () **%M8.0** "T24" %M2.6 "Elevador_Livre" () %M3.3 %M8.3 "Parado" "T25" ()-%M3.6 "Resetando_ %M8.5 Pistão" "T27" %M3.3 %M1.1 %M7.5 3.1 (Page4 - 4)

Totally Integrated Automation Portal					
Network 1: (3.1 / 3.1)		2.1 (Page4	- 3)		
"Parado"	"Reset"		"T21"	~~~	
%M3.4 "Resetando_ Elevador"	%M0.4 "Elevador_ Embaixo"		%M8.4 "T26"		
%M3.6 "Resetando_ Pistão"	%M58.2 "Pistão_Recuado"		%M7.6 "T22"		

Totally Inte								
CLP2_TE			4C D	OC/DO	C/DC] / F	rogram	blocks	
Dinamica	Fichas [F	-C3]						
Dinamica Fich General	has Propertie	s						
Name	Dinamica Fi	chas	Numb		3		Туре	FC
Language Information	LAD		Numb	ering	Automatic			
Title			Autho	r			Comment	
Family			Versio		0.1		User-defined ID	
Name		Data t	ype	Defaul	t value	Comm	ent	
Input								
Output								
InOut								
Temp								
Constant ▼ Return								
	ca Fichas	Void						
Dinami	Ca Fichas	voiu						

Totally Integrated Automation Portal		
Network 1: (1.1 / 4.1)		
%M8.1 "TO"	%M2.7 "Tem_Peça" ———{ S }———•	
	%M2.6 "Elevador_Livre" ————————————————————————————————————	
%M4.0 "T1"	%M2.0 "Elevador_ Subindo"	
	%M2.7 "Tem_Peça" ————————————————————————————————————	
%M4.1 "T2"	%M2.1 "Testando_Altura" { S }	
	%M2.0 "Elevador_ Subindo" ————(R)	
%M4.2	%M2.2 "Elevador_ Descendo_c_ Peca"	
"T3"	(S)	
%M4.3	R } R } %M2.3	
"T4"	Peça" (S) %M3.1 "Rampa_Lig"	
	%M3.2 "Prox Bancada	
	Livre" (R) "M2.1	
	"Testando_Altura" (R) %M2.4	
%M4.4 "T5"	"Elevador_ Descendo_ Vazio" ————————————————————————————————————	
	%M2.3 "Empurrando_ Peca"	
	2.1 (Page5 - 3)	

Totally Integrated **Automation Portal** Network 1: (2.1 / 4.1) 1.1 (Page5 - 2) -(R)-**%M4.5** "T6" **%M2.6**"Elevador_Livre" (s)-%M2.4 "Elevador_ Descendo_ Vazio" -(R)-**%M2.5**"Descartando_ Peça" **%M4.6** "T7" **H** F -(s)-%DB3
"IEC_Counter_
0_DB_1" CTU Int · CU Q· false — R **cv** — 0 %MW4 "Contador_ Descarte" — PV **%M2.2**"Elevador_
Descendo_c_
Peça" (R) **%M6.0** "T8" %M2.6 "Elevador_Livre" 4 F (s)— %M2.5 "Descartando_ -(R)-**%M6.1** "T9" %M3.0 "Rampa_Desl" 4 F (s)-**%M3.1** "Rampa_Lig" -(R)-%M3.2 **%M6.2** "T10" "Prox_Bancada_ Livre" (s)-**%M6.3** "T11" **%M2.6**"Elevador_Livre" 4 F (s)-%M3.5 "Pronto" -(R)-3.1 (Page5 - 4)

vork 1: (3.1 / 4.1)		<u> </u>
	2.1 (Page5 - 3)	
%M6.4	%M2.7	~~~~~
"T12"	"Tem_Peça" 	
	· <i>,</i>	
%M6.5	%M2.0 "Elevador_	
"T13" 	Subindo" 	
%M6.6 "T14"	<mark>%M2.1</mark> "Testando_Altura"	
	(R)	
	%M2.2	
%M6.7	"Elevador_ Descendo_c_	
"T15"	Peça"	
<u> </u>	(R)	
%M7.0	%M2.3 "Empurrando_	
"T16"	Peça"	
<u> </u>	(R }	
<mark>%M7.1</mark> "T17"	%M3.1 "Rampa_Lig"	
i′′	(R)	
	WARD 4	
%M7.2 "T18"	%M3.4 "Resetando_ Elevador"	
	(R }	
	%M2.4 "Elevador_	
%M7.3 "T19"	Descendo_ Vazio"	
	(R)	
	%M2.5	
%M7.4 "T20"	"Descartando_ Peça"	
	(R)	
%M7.5	%M3.3	
"T21" 	"Parado" ───── { R } ───	
	%M3.4 "Resetando_	
	Elevador* (S)	
%M7.6	%M3.6 "Resetando_	
"T22"	Pistão"	
	%M3.5 "Pronto"	
	(s }	
%M7.7 "T7.2"	%M3.5	
"T23"	"Pronto"	

Totally Integrated Automation Portal		
Network 1: (4.1 / 4.1)	2.1 (Daniel T. 1)	
	3.1 (Page5 - 4)	
%M8.0 "T24"	%M2.6 "Elevador_Livre"	
%M8.3		
"T25"	"Parado" "Parado" (S)	
%M8.4 "T26"	%M3.4 "Resetando_ Elevador" ————————————————————————————————————	
	%M3.6 "Resetando_ Pistão" ——{	
%M8.5 "T27"	%M3.6 "Resetando_ Pistão" ————————————————————————————————————	
	. ,	

Totally Into	egrated on Portal							
CLP2_TE	ESTING [CPU 121	4C [OC/DO	C/DC] / P	rogram	blocks	
Inicializa	ção [FC4]							
Inicialização General	Properties							
Name	Inicialização)	Numb		4		Туре	FC
Language Information	LAD		Numb	ering	Automatic			
Title			Autho	r			Comment	
Family			Versio	n	0.1		User-defined ID	
Name		Data ty	ype	Defaul	t value	Comm	ent	
Input								
Output								
InOut								
Temp								
Constant ▼ Return								
Inicializ	~ .	Void						

Totally Integrated Automation Portal		
Network 1: (1.1 / 2.1)		
%M58.0 "Memo_ Inicialização"	%M58.0 "Memo_ Inicialização" ——(S)	
	%M3.3 "Parado" ——{ S }——	
	%M3.0 "Rampa_Desl" (S) %M2.7	
	"Tem_Peça" ———————————————————————————————————	
	"Elevador_ Subindo" ——(R)——1	
	"Testando_Altura" (R) %M2.3 "Empurrando_	
	Peça" 【 R }	
	"Elevador_ Descendo_c_ Peça" R}	
	%M2.5 "Descartando_ Peça" (R)	
	%M2.4 "Elevador_ Descendo_ Vazio" R }	
	%M2.6 "Elevador_Livre" ———————————————————————————————————	
	%M3.2 "Prox_Bancada_ Livre" ————————————————————————————————————	
	%M3.1 "Rampa_Lig" ————————————————————————————————————	
	%M3.4 "C	
	2.1 (Page6 - 3)	

Totally Integrated Automation Portal			
etwork 1: (2.1 / 2.1)			
	1.1 (Page6 - 2)		
	"Resetando_ Elevador"	~~~~	
	Elevador"		
	()		
	%M3.5 "Pronto"		
	(R)		
	%M3.6 "Resetando_ Pistão"		
	Pistāo" (R)		
	MOVE		
	O IN		
	"Contador_ Descarte"		
<u>, </u>			
		1	

|--|

CLP2_TESTING [CPU 1214C DC/DC/DC] / Program blocks

Ações [FC5]

Ações Prope	rties				
General					
Name	Ações	Number	5	Туре	FC
Language	LAD	Numbering	Automatic		
Information					
Title		Author		Comment	
Family		Version	0.1	User-defined	
				ID	

Name	Data type	Default value	Comment	
Input				
Output				
InOut				
Temp				
Constant				
▼ Return				
Ações	Void			

Network 1:

```
*M2.2
"Elevador_
Descendo_c_
Peça"

*M2.4
"Elevador_
Descendo_
Vazio"

*M3.4
"Resetando_
Elevador"
```

Network 2:

```
%M2.0
"Elevador__
Subindo"
"Sobe_Elevador"

%M2.3
"Empurrando__
Peça"
```

```
Totally Integrated
  Automation Portal
Network 3:
                                                                          %DB4
                                                                      "IEC_Timer_0_
DB_1"
                                      %M2.5
                                   "Descartando_
                                                                           TP
                                                                                                              %Q0.2
                                      Peça"
                                                                          Time
                                                                                                          "Empurra_Peça"
                                       Q
                                                        T#1S_500MS — PT
                                                                                ET — T#0ms
                                      %M2.3
                                  "Empurrando_
Peça"
                                       H F
                                      %M3.6
                                   "Resetando_
Pistão"
                                       H F
Network 4:
                                      %M3.1
                                                                                                              %Q0.3
                                    "Rampa_Lig"
                                                                                                             "Rampa"
                                       Network 5:
                                                                                                            %Q0.7
"IP_N_FO"
                                      %M2.7
                                    "Tem_Peça"
                                      \dashv \vdash
                                                                                                              -( s )-
                                                                                                            %Q0.7
"IP_N_FO"
                                      %M2.6
                                  "Elevador_Livre"
                                       H F
                                                                                                              –( R )–
Network 6:
                                      %M3.4
                                    "Resetando_
Elevador"
                                                                                                            %Q8.2
"LED_Q1"
                                                                                                              -( s )-
                                                                                                            %Q8.2
"LED_Q1"
                                      %M3.5
                                     "Pronto"
                                                                                                             —( R )—
                                      %M1.2 "Stop"
                                       H F
```

```
Totally Integrated
   Automation Portal
Network 7:
                                         %M2.6
"Elevador_Livre"
                                                                                                                                    %Q8.3
"LED_Q2"
                                                                                                                                      -( s )-
                                               %M3.3 "Parado"
                                                                                                                                   %Q8.3
"LED_Q2"
                                               <del>|</del> | |
                                                                                                                                      -( R )-
                                             %M1.2 "Stop"
                                               Network 8:
                                             %M3.3 "Parado"
                                                                                                                                  %Q8.1
"LED_Reset"
                                                                                                                                      -( s )-
                                              %M3.4
                                           "Resetando_
Elevador"
                                                                                                                                   %Q8.1
"LED_Reset"
                                                                                                                                      _( R )_
                                               Network 9:
                                             %M3.5
"Pronto"
                                                                                                                                   %Q8.0
"LED_Start"
                                               \dashv \vdash
                                                                                                                                     _( s }__
                                                                                                                                   %Q8.0
"LED_Start"
                                              %M2.6
                                         "Elevador_Livre"
                                               +
                                                                                                                                     _( R )_
                                              %M1.2 "Stop"
                                               -| |-
```

Internation
Author Version 1.0 Comment User-defined ID Set- Visible EC_TMR ID Start Value Retain Accessible tain HMI point Vision From HMI/O PC UA m HM I/O PC UA M I/O PC
Static PT Time T#0ms False True False ITrue False ITrue False Q Bool false False True False ITrue
Static PT Time T#0ms False True True False ET Time T#0ms False True False IN Bool false False True False Q Bool false False True False True False Sible from HMI/O PC UA hold engi-neer-ing neer-ing True False ET True False
PT Time T#0ms False True True False ET Time T#0ms False True False True False IN Bool false False True True False Q Bool false False True False True False
ET Time T#0ms False True False e IN Bool false False True False e Q Bool false False True False True False
IN Bool false False True True False Q Bool false False True Fals True False
Q Bool false False True False

General Name Language Information Sitle Samily	IEC_Count DB	ter_O_DB_´		Number Numbering Author Version	3 Autor Simat				Comme User-de	ent	DB CNTR
Name		Data type	Start	value		Accessible from HMI/O PC UA	ta- ble fro	ing	Set-		Comment
▼ Static											
CU			false		True	True	e	True	False		
CD			false		True	True	е	True	False		
R		Bool	false		True	True	Tru e	True	False		
LD		Bool	false		True	True	Tru e	True	False		
QU		Bool	false		True	True	Tru e	True	False		
QD		Bool	false		True	True		True	False		
PV		Int	0		True	True		True	False		
CV		Int	0		True	True	Tru	True	False		
CV		Int	0		True	True	Tru e	True	False		

sible ta- in HMI point vision from ble engi- HMI/O fro neer- PC UA m ing HM I/O PC UA	EC_Timer_0_I General											
Author Version 1.0 Comment User-defined IEC_TMR ID User-defined ID User-define			r_0_DB_1							Type		DB
Static PT Time T#0ms False True False Image		DB			Numbering	Autor	natic					
Data type Start value Retain Sible from HMI/O PC UA PC UA Static PT Time T#0ms False True True False ET True False IN Bool false False True False Palse Q Bool false False True False True False PT False True False PT True False PT False False True False PT False False True False PT False False False False True False PT False False False True False PT False False False False False False False PT False False False True False False							ic					
Sible from HMI/O PC UA Static PT Time T#0ms False True Tru e False ET Time T#0ms False True False IN Bool false False True Fals True False Q Bool false False True Fals True False False True False True False	amily	IEC			Version	1.0					efined	IEC_TMR
PT Time T#0ms False True Tru True False ET Time T#0ms False True Fals True False IN Bool false False True Tru True False Q Bool false False True Fals True False			Data type	Start	value	Retain	sible from HMI/O	ta- ble fro m HM I/O PC	in HMI engi- neer- ing		Super- vision	Comment
ET Time T#0ms False True False False			Time	T#0m	nc .	Ealso	True	Tru	Truo	Ealco		
IN Bool false False True True False Q Bool false False True False True False False								e				
Q Bool false False True False True False			Time	T#0m	1S	False	True		True	False		
	ET							_				
			Bool	false		False	True	Tru	True	False		
	IN							Tru e Fals				

EC_Timer_0_ ieneral lame	IEC_Time			Number	1				Type		DB
anguage	DB	I_U_DB_Z		Numbering	Autor	natic			Туре		ПВ
nformation itle				Author	Simat	ic			Comme	ent	
amily	IEC			Version	1.0				User-de ID		IEC_TMR
lame		Data type	Start	value		sible from HMI/O	ta- ble fro	Visible in HMI engi- neer- ing			Comment
▼ Static PT		Time	T#0m	ıs	False	True	Tru	True	False		
ET		Time	T#0m	is	False	True	e Fals	True	False		
IN		Bool	false		False	True	e Tru	True	False		
Q		Bool	false		False	True	e Fals	True	False		

Totally Integrated Automation Portal		
CLP2_TESTING [CPU 1214C DC/DC/DC]	
Technology objec	ts	
This folder is empty.		

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ally Integrated	
Automation Portal	

CLP2_TESTING [CPU 1214C DC/DC/DC] / PLC tags / Default tag table [116]

PLC tags

N	ame	Data type	Address	Retain	sible from HMI/O	ble	ble in HMI engi-	Supervision	Comment
11	PART_AV	Bool	%10.0	False	True	True	True		
EI	B2	Bool	%IO.1	False	True	True	True		
EI	B4	Bool	%10.2	False	True	True	True		
EI	B5	Bool	%10.3	False	True	True	True		
EI	1B1	Bool	%10.4	False	True	True	True		
El	1B2	Bool	%10.5	False	True	True	True		
EI	2B1	Bool	%10.6	False	True	True	True		
EI	IP_FI	Bool	%10.7	False	True	True	True		
EI	Bot_Start	Bool	%18.0	False	True	True	True		
EI	Bot_Stop	Bool	%18.1	False	True	True	True		
EI	Bot_Reset	Bool	%18.3	False	True	True	True		
CI	Desce_Elevador	Bool	%Q0.0	False	True	True	True		
EI	Sobe_Elevador	Bool	%Q0.1	False	True	True	True		
EI	Empurra_Peça	Bool	%Q0.2	False	True	True	True		
EI	Rampa	Bool	%Q0.3	False	True	True	True		
EI	IP_N_FO	Bool	%Q0.7	False	True	True	True		
EI	LED_Start	Bool	%Q8.0	False	True	True	True		
EI	LED_Reset	Bool	%Q8.1	False	True	True	True		
EI	LED_Q1	Bool	%Q8.2	False	True	True	True		
EI	LED_Q2	Bool	%Q8.3	False	True	True	True		
EI	void	Bool	%M57.0	False	True	True	True		
EI	Chega_Peça	Bool	%M0.0	False	True	True	True		
EI	Subida_Liberada	Bool	%M0.1	False	True	True	True		
EI	void2	Bool	%M57.1	False	True	True	True		
EI	Elevador_Em_Cima	Bool	%M0.3	False	True	True	True		
EI	void3	Bool	%M57.2	False	True	True	True		
EI	Elevador_Embaixo	Bool	%M0.4	False	True	True	True		
EI	void4	Bool	%M57.3	False	True	True	True		
EI	Pistão_Recuou	Bool	%M0.5	False	True	True	True		
EI	void5	Bool	%M57.4	False	True	True	True		
EI	Altura_Certa	Bool	%M0.6	False	True	True	True		
[1]	Prox.Bancada_Liber- ou	Bool	%M0.7	False	True	True	True		
EI	void6	Bool	%M57.5	False	True	True	True		
EI	void7	Bool	%M57.6	False	True	True	True		
EI	void8	Bool	%M57.7	False	True	True	True		
EI	Start	Bool	%M1.0	False	True	True	True		

S	Reset Stop Elevador_Subindo Testando_Altura Elevador_Descen- do_c_Peça Empurrando_Peça Elevador_Descen- do_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Bool Bool Bool Bool Bool Bool	%M1.1 %M1.2 %M2.0 %M2.1 %M2.2 %M2.3 %M2.4 %M2.5 %M2.6 %MW4 %M3.0 %M3.1 %M3.2	False	from HMI/O PC UA True True True True True True True Tru	ble from HMI/O PC UA True True True True True True True True	ble in HMI engi-	Supervision	Comment
S	Elevador_Subindo Festando_Altura Elevador_Descendo_c_Peça Empurrando_Peça Elevador_Descendo_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Bool Bool Bool Bool Bool Bool	%M1.2 %M2.0 %M2.1 %M2.2 %M2.3 %M2.4 %M2.5 %M2.6 %MW4 %M3.0 %M3.1	False	True True True True True True True True	True True True True True True True True	True True True True True True True True		
E E D D E E D D E E E E E E E E E E E E	Elevador_Subindo Festando_Altura Elevador_Descendo_c_Peça Empurrando_Peça Elevador_Descendo_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Bool Bool Bool Bool Bool Bool	%M2.0 %M2.1 %M2.2 %M2.3 %M2.4 %M2.5 %M2.6 %MW4 %M3.0 %M3.1	False	True True True True True True True True	True True True True True True True True	True True True True True True True True		
T E d d d E E d d D D E E E E E E E E E E	Testando_Altura Elevador_Descen- do_c_Peça Empurrando_Peça Elevador_Descen- do_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Bool Bool Bool Bool Bool Bool	%M2.1 %M2.2 %M2.3 %M2.4 %M2.5 %M2.6 %MW4 %M3.0 %M3.1	False	True True True True True True True True	True True True True True True True True	True True True True True True		
E d d d E E d d D D E E E E E E E E E E	Elevador_Descendo_c_Peça Empurrando_Peça Elevador_Descendo_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Bool Int Bool Bool Bool Bool	%M2.2 %M2.3 %M2.4 %M2.5 %M2.6 %MW4 %M3.0 %M3.1	False False False False False False False False False	True True True True True True True True	True True True True True True True	True True True True True		
d d E d d d d D D D D D D D D D D D D D	do_c_Peça Empurrando_Peça Elevador_Descendo_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Bool Int Bool Bool Bool	%M2.3 %M2.4 %M2.5 %M2.6 %MW4 %M3.0 %M3.1	False False False False False False False False	True True True True True True True	True True True True True	True True True True		
E d d d D D E G D D D D D D D D D D D D D D D D	Elevador_Descendo_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Int Bool Bool Bool Bool	%M2.4 %M2.5 %M2.6 %MW4 %M3.0 %M3.1	False False False False False False	True True True True True	True True True True	True True True		
d D D C C C C C C C C C C C C C C C C C	do_Vazio Descartando_Peça Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool Bool Bool	%M2.5 %M2.6 %MW4 %M3.0 %M3.1	False False False False False	True True True True	True True True	True True		
E C C C C C C C C C C C C C C C C C C C	Elevador_Livre Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Int Bool Bool Bool	%M2.6 %MW4 %M3.0 %M3.1	False False False False	True True True	True True	True		
C R R R R R R R R R R R R R R R R R R R	Contador_Descarte Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Int Bool Bool Bool	%MW4 %M3.0 %M3.1	False False	True True	True			
R R R R R R R R R R R R R R R R R R R	Rampa_Desl Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool	%M3.0 %M3.1	False False	True		True		1
R P P R R T T T T T T T T T T T T T T T	Rampa_Lig Prox_Bancada_Livre Parado Resetando_Elevador	Bool Bool	%M3.1	False		Truc			
P P R R T T T T T T T T T T T T T T T T	Prox_Bancada_Livre Parado Resetando_Elevador	Bool			True		True		
P R R T T T T T T T T T T T T T T T T T	Parado Resetando_Elevador		%M3.2	F-		True	True		
R P T T T T T T T T T T T T T T T T T T	Resetando_Elevador	Bool		False		True	True		
P T T T T T T T T T T T T T T T T T T T			%M3.3	False	True	True	True		
		Bool	%M3.4	False	True	True	True		
	Pronto	Bool	%M3.5	False	True	True	True		
T T T T T T T T T T T T T T T T T T T	Гет_Реçа	Bool	%M2.7	False		True	True		
	Γ1	Bool	%M4.0	False	True	True	True		
1 T T T T T T T T T T T T T T T T T T T	Γ2	Bool	%M4.1	False	True	True	True		
	Γ3	Bool	%M4.2	False	True	True	True		
T T T T T T T T T T T T T T T T T T T	Γ4	Bool	%M4.3	False	True	True	True		
11 T T T T T T	Γ5	Bool	%M4.4	False	True		True		
11 T	Γ6	Bool	%M4.5	False	True	True	True		
10 T	Γ7	Bool	%M4.6	False	True	True	True		
10 T	Γ8	Bool	%M6.0	False	True		True		
T T	Г9	Bool	%M6.1	False	True	True	True		
T T	Γ10	Bool	%M6.2	False	True	True	True		
	Γ11	Bool	%M6.3	False		True	True		
	Γ12	Bool	%M6.4	False	True	True	True		
T T	Γ13	Bool	%M6.5	False	True	True	True		
	Γ14	Bool	%M6.6	False	True		True		
	Г15	Bool	%M6.7	False		True	True		
	Г16	Bool	%M7.0	False	True		True		
	Γ17	Bool	%M7.1	False	True	True	True		
		Bool	%M7.2	False			True		
	Г18	Bool	%M7.3	False	True		True		
ti T	Г18	Bool	%M7.4	False	True	True	True		
T T			%M7.5	False	True	True	True		
ti T	Г19	Bool Bool	%M7.6	False	True	True	True		

Name T24 T0 Fim_Reset T25 Memo_Inicialização T26 Resetando_Pistão T27 Tag_1 Pistão_Recuado	Bool Bool Bool Bool Bool Bool Bool Bool	%M8.0 %M8.1 %M8.2 %M8.3 %M58.0 %M8.4 %M3.6	False False False False False False False	from HMI/O PC UA True	Writa- ble from HMI/O PC UA True True	ble in HMI engi- neer- ing True	Supervision	Comment
TO Fim_Reset T25 Memo_Inicialização T26 Resetando_Pistão T27 Tag_1	Bool Bool Bool Bool Bool Bool Bool	%M8.1 %M8.2 %M8.3 %M58.0 %M8.4 %M3.6	False False False False	True True	True	True True		
TO Fim_Reset T25 Memo_Inicialização T26 Resetando_Pistão T27 Tag_1	Bool Bool Bool Bool Bool Bool Bool	%M8.1 %M8.2 %M8.3 %M58.0 %M8.4 %M3.6	False False False False	True True	True	True		
Fim_Reset T25 Memo_Inicialização T26 Resetando_Pistão T27 Tag_1	Bool Bool Bool Bool Bool Bool	%M8.2 %M8.3 %M58.0 %M8.4 %M3.6	False False False	True	True			
T25 Memo_Inicialização T26 Resetando_Pistão T27 Tag_1	Bool Bool Bool Bool Bool	%M8.3 %M58.0 %M8.4 %M3.6	False False			True		
Memo_Inicialização T26 Resetando_Pistão T27 Tag_1	Bool Bool Bool	%M8.4 %M3.6			True	True		
T26 Resetando_Pistão T27 Tag_1	Bool Bool Bool	%M3.6	False	True	True	True		
Resetando_Pistão T27 Tag_1	Bool Bool			True	True	True		
T27 Tag_1	Bool	0/ 1/40 5	False	True	True	True		
Tag_1		%M8.5	False	True	True	True		
		%M58.1	False	True	True	True		
_	200.	%M58.2	False		True	True		

Totally Integrated Automation Portal				
CLP2_TESTING [CPU 1214C DC/DC	/DC] / PLC tags	s / Default	tag table [116]
User constants				
User constants	Data tura	Walter	Camara	
Name	Data type	Value	Comme	ent

Totally Integrated Automation Portal				
CLP2_TESTING [CPU 1214C DC/DC/DC] / PLC data types				
System data types	5			
This folder is empty.				

Totally Integrated Automation Portal				
CLP2_TESTING [[CPU 1214C D	C/DC/DC] / Wat	ch and force t	tables
Force table				
Name A	ddress	Display format	Force value	Comment
	1			
	1			

Totally Integrated Automation Portal		
CLP2_TESTING [CPU 1214C DC/DC/DC]	
Traces		
Name		

Totally Integrated Automation Portal		
CLP2_TESTING [[CPU 1214C DC/DC/DC] / Traces	
Measurements		
This folder is empty.		

Totally Integrated Automation Portal				
CLP2_TESTING [CPU 1214C DC/DC/DC] / Traces				
Combined measurements				
Name				

Totally Integrated Automation Portal				
CLP2_TESTING [CPU 1214C DC/DC/DC]				
PLC alarm text list	s			
This folder is empty.				

|--|

CLP2_TESTING [CPU 1214C DC/DC/DC] / Local modules

CLP2_TESTING [CPU 1214C DC/DC/DC]

_	<u>-</u>		
CLP2_TESTING			
General\Project inform	nation		
Name	CLP2_TESTING	Author	LAI-09
Comment		Slot	1
Rack	0		
General\Catalog infor	mation		
Short designation	CPU 1214C DC/DC/DC	Description	Work memory 100 KB; 24VDC power supply with DI14 x 24VDC SINK/SOURCE, DQ10 x 24VDC and AI2 on board; 6 high-speed counters and 4 pulse outputs on board; signal board expands on-board I/O; up to 3 communication modules for serial communication; up to 8 signal modules for I/O expansion; 0.04 ms/1000 instructions; PROFINET interface for programming, HMI and PLC to PLC communication
Article number	6ES7 214-1AG40-0XB0	Firmware version	V4.2
General\Identification	a & Maintenance		
Plant designation		Location identifier	
Installation date	2023-05-04 14:13:24.297	Additional informa- tion	
General\Checksums		"	
Text lists	FA 70 E8 75 1D 5A 8E 29	Software	C3 C0 4A 97 11 DC B8 5E
PROFINET interface [>	(1]\General		
Name	PROFINET interface_1	Author	LAI-09
Comment	_		
PROFINET interface [>	(1]\General\Project information		
Name	DI 14/DQ 10_1	Comment	
Name	AI 2_1	Comment	
Name	AQ 1x12BIT_1	Comment	
PROFINET interface [>	(1]\General\Catalog information	11	
Short designation	AQ1 Signal board	Description	Signal board AQ1 x 12 bits; plug-in terminal blocks; output: +/-10V and 0 to 20 mA; configurable diagnostics; configurable substitute output value
Article number	6ES7 232-4HA30-0XB0	Firmware version	V1.0
PROFINET interface [>	(1]\Ethernet addresses\Interface netw	orked with	
Subnet:	Not connected		
PROFINET interface [>	(1]\Ethernet addresses\IP protocol		
IP configuration	Set IP address in the project	IP address:	150.162.14.24
Subnet mask:	255.255.0.0	Use router	False
PROFINET interface [>	(1]\Ethernet addresses\PROFINET		
PROFINET device name is set directly a the device	False t	Generate PROFINET device name auto- matically	True
PROFINET device	clp2_testing	Converted name:	clp2xbtestingab04
Device number:	0		
PROFINET interface [>	(1]\Time synchronization		
	Enable time synchronization via NTP		IP addresses
Server 1	0.0.0.0	Server 2	0.0.0.0
Server 3	0.0.0.0	Server 2 Server 4	
server s	0.0.0.0	Server 4	0.0.0.0

Totally Integrated Automation Portal			
Automation Fortal			
Jpdate interval	10sec	Empty	
CPU synchronizes the	No synchronization		
modules of the de-			
vice.			
	I]\Digital inputs\Channel0		
	10.0	Input filters	6.4 millisec
manie panee caren	0		
	I]\Digital inputs\Channel0\		
Enable rising edge de-	0	RidPrefixRisingEdgeE-	49152
ection		vent	
	0	Hardware interrupt:	0
	Rising edge0		
	l]\Digital inputs\Channel0\		
	0	RidPrefixFallingEdg-	49280
detection	0	eEvent	
		Hardware interrupt:	0
	Falling edge0		
	I]\Digital inputs\Channel1	Innut file	6 4 millions
-	0.1	Input filters	6.4 millisec
	<u> </u>		
]\Digital inputs\Channel1\		1.24-2
Enable rising edge de-	0	RidPrefixRisingEdgeE-	49153
tection	0	vent	0
	0	Hardware interrupt:	0
	Rising edge1		
	I]\Digital inputs\Channel1\	D' 10 (' E 11' E 1	40204
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49281
	0	Hardware interrupt:	0
	Falling edge1	naruware interrupt.	O
	I]\Digital inputs\Channel2		
	1).Digital inputstenanneiz 10.2	Input filters	6.4 millisec
		input inters	0.4 minisec
	I]\Digital inputs\Channel2\		
Enable rising edge de-		Did Drofiy Dising Edge E	40154
tection	0	RidPrefixRisingEdgeE- vent	49154
	0	Hardware interrupt:	0
	Rising edge2	Halaware Interrupt.	O
	I]\Digital inputs\Channel2\		
	0	RidPrefixFallingEdg-	49282
detection	<u> </u>	eEvent	17202
	0	Hardware interrupt:	0
	Falling edge2	naraware interrupt.	<u> </u>
	I]\Digital inputs\Channel3		
	10.3	Input filters	6.4 millisec
	0	mpat micis	O. I IIIIIIGC
•	I]\Digital inputs\Channel3\		
Enable rising edge de-		RidPrefixRisingEdgeE-	49155
tection	<u> </u>	vent	
	0	Hardware interrupt:	0
	Rising edge3	naraware interrupt.	<u> </u>
	I]\Digital inputs\Channel3\		
	0	RidPrefixFallingEdg-	49283
detection	<u> </u>	eEvent	1,7203
	0	Hardware interrupt:	0
	Falling edge3	araware miterrupt.	
	I]\Digital inputs\Channel4		
K DEIME I INIONALA		Input filters	6.4 millisec
		THE PART OF THE PA	D. C C. L.
Channel address	0.4	parimon	of Finnisee
Channel address	0		or riminate

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Automation Portal			
PROFINET interface [X	1]\Digital inputs\Channel4\		
Enable rising edge de- tection	0	RidPrefixRisingEdgeE- vent	49156
Event name:	0	Hardware interrupt:	0
Rising edge4	Rising edge4		-
	1]\Digital inputs\Channel4\		
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49284
Event name:	0	Hardware interrupt:	0
	Falling edge4	maraware interrupt.	U
	1]\Digital inputs\Channel5		
	10.5	Input filters	6.4 millisec
Enable pulse catch	0	input inters	0.4 minisec
	1]\Digital inputs\Channel5\		
Enable rising edge de-		RidPrefixRisingEdgeE-	49157
tection		vent	
Event name:	0	Hardware interrupt:	0
	Rising edge5		
	1]\Digital inputs\Channel5\	pidow C = U = 1	40205
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49285
Event name:	0	Hardware interrupt:	0
	Falling edge5		
PROFINET interface [X	1]\Digital inputs\Channel6		
Channel address	10.6	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel6\		
Enable rising edge de- tection	0	RidPrefixRisingEdgeE- vent	49158
Event name:	0	Hardware interrupt:	0
Rising edge6	Rising edge6		
PROFINET interface [X	1]\Digital inputs\Channel6\		
Enable falling edge detection	0	RidPrefixFallingEdg- eEvent	49286
Event name:	0	Hardware interrupt:	0
Falling edge6	Falling edge6		-
	1]\Digital inputs\Channel7		
Channel address	10.7	Input filters	6.4 millisec
Enable pulse catch	0	-	
<u> </u>	1]\Digital inputs\Channel7\		
Enable rising edge de- tection		RidPrefixRisingEdgeE- vent	49159
Event name:	0	Hardware interrupt:	0
Rising edge7	Rising edge7	iniawaic intellupt.	
	1]\Digital inputs\Channel7\		
Enable falling edge	0	RidPrefixFallingEdg-	49287
detection	0	eEvent	0
Event name:	0 Falling adge7	Hardware interrupt:	0
	Falling edge7		
	1]\Digital inputs\Channel8 1.0	Input filtors	6.4 millisec
	0	Input filters	0.4 IIIIIISEC
Enable pulse catch	U 1]\Digital inputs\Channel8\		
Enable rising edge de-		RidPrefixRisingEdgeE-	19160
Enable rising edge de- tection		vent	100 EF
		ACIIC	
Event name:	0	Hardware interrupt:	0

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Automation Portal			
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DOCUMET' (DV			
	1]\Digital inputs\Channel8\	D' 1D (' E II' E I	10200
Enable falling edge detection	0	RidPrefixFallingEdg-	49288
	0	eEvent	0
Event name:	0	Hardware interrupt:	0
	Falling edge8		
	1]\Digital inputs\Channel9	- -	
	11.1	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel9\		
Enable rising edge de-	0	RidPrefixRisingEdgeE-	49161
tection	-	vent	
Event name:	0	Hardware interrupt:	0
	Rising edge9		
	1]\Digital inputs\Channel9\		
Enable falling edge	0	RidPrefixFallingEdg-	49289
detection		eEvent	
Event name:	0	Hardware interrupt:	0
	Falling edge9		
	1]\Digital inputs\Channel10		
Channel address	11.2	Input filters	6.4 millisec
Enable pulse catch	0		
PROFINET interface [X	1]\Digital inputs\Channel10\		
Enable rising edge de-		RidPrefixRisingEdgeE-	49162
tection		vent	
Event name:	0	Hardware interrupt:	0
Rising edge10	Rising edge10		
	1]\Digital inputs\Channel10\		
	0	RidPrefixFallingEdg-	49290
detection		eEvent	
Event name:	0	Hardware interrupt:	0
Falling edge10	Falling edge10		
	1]\Digital inputs\Channel11		
	11.3	Input filters	6.4 millisec
Enable pulse catch	0		
	1]\Digital inputs\Channel11\		
Enable rising edge de-		RidPrefixRisingEdgeE-	49163
tection		vent	13103
Event name:	0	Hardware interrupt:	0
Rising edge11	Rising edge11	naraware interrupt.	
	1]\Digital inputs\Channel11\		
Enable falling edge	0	RidPrefixFallingEdg-	49291
detection		eEvent	15251
Event name:	0	Hardware interrupt:	0
Falling edge11	Falling edge11	aramare interrupt.	<u> </u>
	1]\Digital inputs\Channel12		
	11.4	Input filters	6.4 millisec
	0	mput miers	U.T IIIIIISEC
Enable nulse satab	(C)		
Enable pulse catch			
PROFINET interface [X	1]\Digital inputs\Channel13		C 4'II'.
PROFINET interface [X Channel address	1]\Digital inputs\Channel13 1.5	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch	1]\Digital inputs\Channel13 1.5 0	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X	1]\Digital inputs\Channel13 1.5 0 1]\Analog inputs\Noise reduction	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time	1]\Digital inputs\Channel13 I1.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms)	Input filters	6.4 millisec
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0		
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time	1]\Digital inputs\Channel13 I1.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms)	Input filters Measurement type	6.4 millisec Voltage
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0	Measurement type Smoothing	Voltage Weak (4 cycles)
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X Channel address	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0 IW64	Measurement type	Voltage Weak (4 cycles)
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X Integration time PROFINET interface [X Channel address Voltage range	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0 IW64	Measurement type Smoothing	Voltage Weak (4 cycles)
PROFINET interface [X Channel address Enable pulse catch PROFINET interface [X ntegration time PROFINET interface [X Channel address Voltage range Empty	1]\Digital inputs\Channel13 11.5 0 1]\Analog inputs\Noise reduction 50 Hz (20 ms) 1]\Analog inputs\Channel0 IW64	Measurement type Smoothing Enable overflow diag-	Voltage Weak (4 cycles)

Totally Integrated Automation Porta			
			<u> </u>
/oltage range	010 V	Smoothing	Weak (4 cycles)
Empty		Enable overflow diag- nostics	1
PROFINET interface			
	P Use substitute value		
PROFINET INTERFACE Channel address	[X1]\Digital outputs\Channel0 O0.0	Substitute a value of	0
Criainiei address	Q0.0	1 on a change from RUN to STOP.	
PROFINET interface	[X1]\Digital outputs\Channel1		
Channel address	Q0.1	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel2		
Channel address	Q0.2	Substitute a value of 1 on a change from RUN to STOP.	0
PROFINET interface	[X1]\Digital outputs\Channel3		
Channel address	Q0.3	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel4		
Channel address	Q0.4	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel5		
Channel address	Q0.5	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel6		
Channel address	Q0.6	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel7		
Channel address	Q0.7	Substitute a value of 1 on a change from RUN to STOP.	0
PROFINET interface	[X1]\Digital outputs\Channel8		
Channel address	Q1.0	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Digital outputs\Channel9		
Channel address	Q1.1	Substitute a value of 1 on a change from RUN to STOP.	0
	[X1]\Operating mode		
IO controller	True	IO system	
Device number	0	IO device	False
	[X1]\Analog outputs		
	P Use substitute value [X1]\Analog outputs\Channel0		
Channel address	QW80	Analog output type	Voltage
Voltage range	+/- 10 V	Substitute value for channel on a change from RUN to STOP	0.000V
Empty		Enable short circuit diagnostics	1
Enable overflow diag	g- 1	Enable underflow diagnostics	1
	[X1]\I/O addresses\Input addresse		
Start address	0.0	End address	1.7

Automation Portal			
Organization block	0	Process image	0
	(1]\I/O addresses\Input addresses		
Start address	64	End address	67
Organization block	0	Process image	0
PROFINET interface [X	(1]\I/O addresses\Output addresses		
Start address	0.0	End address	1.7
Organization block	0	Process image	0
ROFINET interface [X	(1]\I/O addresses\Output addresses		
Start address	80	End address	81
Organization block	0	Process image	0
ROFINET interface [X	(1]\Advanced options\Interface optio	ons	
Support device re- placement without exchangeable medi-	True	Permit overwriting of device names of all assigned IO devices	False
um Use IEC V2.2 LLDP	False	Keep-Alive connec-	30s
mode	(1]\Advanced options\Real time setti	tion monitoring:	
Send clock:	1.000ms	ngsho communication	
	1.000ms (1]\Advanced options\Real time setti	ngs\Pool time ontions	
Calculated bandwidth for cyclic IO data:		Calculated bandwidth for cyclic IO data:	0.000%
	[[1]\Advanced options\Port [X1 P1]\Ge		
Name	Port 1	Author	LAI-09
Comment			<u> </u>
	 {1]\Advanced options\Port [X1 P1]\Po	ort interconnection\l ocal	nort:
Local port:	CLP2_TESTING\PROFINET interface_1	Medium:	Copper
Cable name:	[X1]\Port_1 [X1 P1 R]	Wicdiani.	СОРРСІ
11			
	504.		
PROFINET interface [X	(1]\Advanced options\Port [X1 P1]\Po		· ·
PROFINET interface [X	Monitoring of partner port is not poss		er port: Any partner
	Monitoring of partner port is not possible	si- Partner port:	· ·
PROFINET interface [X	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	si- Partner port:	· ·
PROFINET interface [X Activate this port for	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	si- Partner port:	· ·
PROFINET interface [X Activate this port for use	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	ort options\Activate	· ·
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate /	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po	ort options\Activate	· ·
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia-	Monitoring of partner port is not possible [1]\Advanced options\Port [X1 P1]\Po True [1]\Advanced options\Port [X1 P1]\Po	ort options\Activate ort options\Connection	Any partner
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True	ort options\Activate ort options\Connection Monitor	Any partner
PROFINET interface [X Activate this port for use PROFINET interface [X Iransmission rate / duplex: Enable autonegotia- tion PROFINET interface [X	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices End of the sync do- main	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False False	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices End of the sync do-	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False False	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner False
PROFINET interface [X Activate this port for use PROFINET interface [X Transmission rate / duplex: Enable autonegotia- tion PROFINET interface [X End of detection of accessible devices End of the sync do- main	Monitoring of partner port is not possible (1]\Advanced options\Port [X1 P1]\Po True (1]\Advanced options\Port [X1 P1]\Po Automatic True (1]\Advanced options\Port [X1 P1]\Po False False (1]\Web server access	Partner port: ort options\Activate ort options\Connection Monitor ort options\Boundaries End of topology dis-	Any partner False

Totally Integrated			
Automation Portal			
High speed counters (HSC)\HSC1\General\Enable		
Enable this high	0	Enable this high	0
speed counter		speed counter	
Enable this high	0	Enable this high	0
speed counter		speed counter	
Enable this high	0	Enable this high	0
speed counter		speed counter	
	HSC)\HSC1\General\Project information	TI TO THE PARTY OF	
Name	HSC_1	Comment	
Name	HSC_2	Comment	
Name	HSC_3	Comment	
Name	HSC_4	Comment	
Name	HSC_5	Comment	
Name	HSC_6	Comment	
	HSC)\HSC1\I/O addresses\Input addre		
Start address	1000.0	End address	1003.7
Start address	1004.0	End address	1007.7
Organization block	0	Start address	1008.0
End address	1011.7	Organization block	0
Process image	0	Start address	1012.0
End address	1015.7	Organization block	0
Process image	0	Start address	1016.0
End address	1019.7	Organization block	0
Process image	0	Start address	1020.0
End address	1023.7	Organization block	0
Process image	0	Organization block	0
Process image	0	Process image	0
	/PWM)\PTO1/PWM1\General\Enable		
Enable this pulse gen-	0	Enable this pulse gen-	0
erator		erator	
Pulse generators (PTO	/PWM)\PTO1/PWM1\General\Project i	nformation	
Name	Pulse_1	Comment	
		Comment	
Name	Pulse_2		
Pulse generators (PTO	/PWM)\PTO1/PWM1\I/O addresses\Ou	•	
Pulse generators (PTO Start address	/PWM)\PTO1/PWM1\I/O addresses\Ou ⁻ 1000.0	End address	1001.7
Pulse generators (PTO Start address Start address	/PWM)\PTO1/PWM1\I/O addresses\Ou	End address End address	1003.7
Pulse generators (PTO Start address Start address Organization block	/PWM)\PTO1/PWM1\I/O addresses\Ou ⁻ 1000.0	End address End address Organization block	
Pulse generators (PTO Start address Start address	/PWM)\PTO1/PWM1\I/O addresses\Ou [*] 1000.0 1002.0	End address End address	1003.7
Pulse generators (PTO Start address Start address Organization block Process image Startup	/PWM)\PTO1/PWM1\I/O addresses\Ou [*] 1000.0 1002.0 0	End address End address Organization block Process image	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER	End address End address Organization block Process image Comparison preset to	1003.7
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF	End address End address Organization block Process image Comparison preset to actual configuration	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER	End address End address Organization block Process image Comparison preset to actual configuration OBs should be inter-	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF	End address End address Organization block Process image Comparison preset to actual configuration	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be inter-	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle	/PWM)\PTO1/PWM1\I/O addresses\Our 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy-	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be inter-	1003.7 0 0
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy- cle time for cyclic OBs Communication load Cycle load due to	/PWM)\PTO1/PWM1\I/O addresses\Out 1000.0 1002.0 0 Warm restart - mode before POWER OFF 60000ms	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy- cle time for cyclic OBs Communication load Cycle load due to communication	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cy- cle time for cyclic OBs Communication load Cycle load due to communication System and clock men	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of sys-	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time	1003.7 0 0 Startup CPU even if mismatch
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx)	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of sys-	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status	1003.7 0 0 Startup CPU even if mismatch 1 1ms
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup After POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte First cycle	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status changed	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte First cycle Always 1 (high)	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0 20% nory\System memory bits 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status	1003.7 0 0 Startup CPU even if mismatch 1
Pulse generators (PTO Start address Start address Organization block Process image Startup Startup Startup after POWER ON Configuration time Cycle Cycle monitoring time Enable minimum cycle time for cyclic OBs Communication load Cycle load due to communication System and clock men Enable the use of system memory byte First cycle Always 1 (high)	/PWM)\PTO1/PWM1\I/O addresses\Ou' 1000.0 1002.0 0 0 Warm restart - mode before POWER OFF 60000ms 150ms 0	End address End address Organization block Process image Comparison preset to actual configuration OBs should be interruptible Minimum cycle time Address of system memory byte (MBx) Diagnostic status changed	1003.7 0 0 Startup CPU even if mismatch 1 1ms

Automation Porta	l I				
10 Hz clock			5 Hz clock		
2.5 Hz clock					
			2 Hz clock		
1.25 Hz clock			1 Hz clock		
0.625 Hz clock			0.5 Hz clock		
Web server\General			11		
Activate Web server on all modules of thi			Permit access only with HTTPS	True	
device	3		WIGHTHES		
Web server\Automat	ric undate				
Enable automatic up			Update interval	Os	
date Web server\User inte	erface languages				
Assign project langu			User interface langu	ages	
English (United States			German		
English (United States	·		English		
English (United States	·		French		
English (United States			Spanish		
English (United States	<u> </u>		Italian		
English (United States			Chinese (simplified)		
-			crimese (simplified)		
Web server\User ma	nagement				
User name			User rights		
Everybody					
Web server\User-def Application name		Default HTML page	Files with dynamic	Wah DP number	Fragment DB num
Application hame	HTML Source path	Default HTML page	content	Web DB Humber	ber
		index.htm	.htm;.html	333	334
Web server\Overviev	v of interfaces				
Device		Interface		Enabled web serve	er access
CLP2 TESTING		PROFINET interface_	1	False	
User interface langu	anes	_			
Assign project langu			User interface langu	1200	
			_	lages	
English (United States			German		
F I'-l- /I I -'+ -l C+ +	. 1		English		
			I		
English (United States	5)		French		
English (United States English (United States English (United States	5)		Spanish		
English (United State English (United State	5)				
English (United State English (United State English (United State	s) s)		Spanish		
English (United State English (United State English (United State English (United State	2) 2) 2) 2)		Spanish Italian		
English (United States English (United States English (United States English (United States Time of day\Local tir	s) s) s) ne	lin, Bern, Brussels,	Spanish Italian		
English (United States English (United States English (United States English (United States Time of day\Local tir	s) s) s) ne		Spanish Italian		
English (United States English (United States English (United States English (United States Time of day\Local tin Time zone	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm		Spanish Italian		
English (United States English (United States English (United States English (United States Time of day\Local tin Time zone	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time		Spanish Italian	60mins	
English (United States English (United States English (United States English (United States Time of day\Local tin Time zone Time of day\Daylight Activate daylight say	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time		Spanish Italian Chinese (simplified)		
English (United States English (United States English (United States English (United States Time of day\Local tin Time zone Time of day\Daylight Activate daylight say	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time		Spanish Italian Chinese (simplified) Difference between		
English (United States English (United States English (United States English (United States Time of day\Local tir	s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time y- 1	, Vienna	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time		
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English (United States Time of day\Local tir Time zone Time of day\Daylight Activate daylight saving time Time of day\Daylight Starting week of the	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time	, Vienna	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time	ht	
English (United States Time of day\Local tir Time zone Time of day\Daylight Activate daylight sav ing time Time of day\Daylight Starting week of the month:	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time	, Vienna	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time	ht	
English (United States Time of day\Local tir Time zone Time of day\Daylight Activate daylight saving time Time of day\Daylight Starting week of the month: of	(UTC +01:00) Ber Rome, Stockholm t saving time /- 1	, Vienna of daylight saving tir	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time	Sunday	
English (United States Time of day\Local tir Time zone Time of day\Daylight Activate daylight saving time Time of day\Daylight Starting week of the month: of	(UTC +01:00) Ber Rome, Stockholm t saving time Last March t saving time\Start of	, Vienna of daylight saving tir	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time	Sunday 01:00 a.m.	
English (United States Time of day\Local tir Time zone Time of day\Daylight Activate daylight saving time Time of day\Daylight Starting week of the month: of Time of day\Daylight	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time	, Vienna of daylight saving tir	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time ne at	Sunday 01:00 a.m. Sunday	
English (United States Time of day\Local tir Time zone Time of day\Daylight Activate daylight saving time Time of day\Daylight Starting week of the month: of Time of day\Daylight	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time	, Vienna of daylight saving tir	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time	Sunday 01:00 a.m.	
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English (United States Time of day\Local tin Time of day\Local tin Activate daylight saving time Time of day\Daylight Starting week of the month: of Time of day\Daylight of Protection & Security Level of protection	s) s) s) ne (UTC +01:00) Ber Rome, Stockholm t saving time /- 1 Last March t saving time\Start o Last October // No protection	, Vienna of daylight saving tir of standard time	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time ne at	Sunday 01:00 a.m. Sunday	
English (United States Time of day\Local tir Time of day\Local tir Time of day\Daylight Activate daylight saving time Time of day\Daylight Starting week of the month: of Time of day\Daylight of Protection & Security Level of protection Protection & Security	(UTC +01:00) Ber Rome, Stockholm t saving time Last March t saving time\Start o Lost October No protection NCOnnection mech	, Vienna of daylight saving tir of standard time	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time ne at	Sunday 01:00 a.m. Sunday	
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English (United States Time of day\Local tir Time of day\Local tir Time of day\Daylight Activate daylight saving time Time of day\Daylight Starting week of the month: of Time of day\Daylight of Protection & Security Level of protection Protection & Security Permit access with PUT/GET communication from remote	(UTC +01:00) Ber Rome, Stockholm tsaving time Last March tsaving time\Start o Last October No protection (/Connection mech	, Vienna of daylight saving tir of standard time	Spanish Italian Chinese (simplified) Difference between standard and daylig saving time ne at	Sunday 01:00 a.m. Sunday	
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Totally Integrated Automation Portal						
Ductaction C. Coccusit dC	accusitor account				L	
events in case of high	True		Length of a	n interval	20	
message volume Unit	seconds					
Protection & Security\E						
Disable copying from internal load memory to external load mem-						
ory	- 4					
	Configuration control for	central conf	riguration			
Allow to reconfigure the device via the	0					
user program						
Connection resources\						
	Station resources - Re- served - Maximum	Station reso served - Co		Station res namic - Co		Module resources - CLP2_TESTING [CPU 1214C DC/DC/DC] - Con- figured
Maximum number of re-	-	62		6		68
sources:	Maximum	Configured		Configured		Configured
PG communication: HMI communication:	12	0		0		0
S7 communication:	8	0		0		0
Open user communication:	8	0		0		0
Web communication:	30	-		-		-
Other communication:	-	-		0		0
Total resources used:		0		0		0
Available resources:		62		6		68
Overview of addresses	Overview of addresses\	Overview of	addresses			
•	True		Outputs		True	
Address gaps	False		Slot		True	

Totally Integrat	ed
Automation Por	tal

Туре	Addr.	Addr. to	Module	PIP	Device	Device	Size	Master /	Rack	Slot
1960	from	, tudi. to			name	number		IO system	Nuck	
	0	1	DI 14/DQ 10_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 1
Ο	0	1	DI 14/DQ 10_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	_	2 Bytes	-	0	1 1
l	64	67	AI 2_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 2
I	1000	1003	HSC_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 16
I	1004	1007	HSC_2	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 17
I	1008	1011	HSC_3	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 18
I	1012	1015	HSC_4	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 19
I	1016	1019	HSC_5	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 20
I	1020	1023	HSC_6	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	4 Bytes	-	0	1 21
O	1000	1001	Pulse_1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 32
O	1002	1003	Pulse_2	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 33
Ο	1004	1005	Pulse_3	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 34
O	1006	1007	Pulse_4	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 35
Ο	80	81	AQ 1x12BIT_1		CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	2 Bytes	-	0	1 3
I	8	8	DI 8/DQ 8x24VDC_ 1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	1 Bytes	-	0	2

Totall Autor	y Integrate nation Port	ed cal								
pe	Addr. from	Addr. to	Module	PIP	Device name	Device number	Size	Master / IO system	Rack	Slot
	8	8	DI 8/DQ 8x24VDC_ 1	Automatic update	CLP2_TEST ING [CPU 1214C DC/DC/DC]	-	1 Bytes	-	0	2

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Automation Portal	

CLP2_TESTING [CPU 1214C DC/DC/DC] / Local modules

DI 8/DQ 8x24VDC_1

DI 8/DQ 8x24VDC_1			
General\Project info			
Name	DI 8/DQ 8x24VDC_1	Author	LAI-09
Comment		Slot	2
General\Catalog info			
Short designation	SM 1223 DI8/DQ8 x 24VDC	Description	Digital input/output module DI8 x 24VDC SINK/SOURCE and DQ8 x 24VDC; configurable input delay; plug in terminal blocks
Article number	6ES7 223-1BH30-0XB0	Firmware version	V1.0
DI 8/DQ 8\Project inf	formation		
Name	DI 8/DQ 8x24VDC_1	Comment	
DI 8/DQ 8\Digital inp	outs\Input filters		
18.0 - 18.3	6.40ms	I8.4 - I8.7	6.40ms
DI 8/DQ 8\Digital inp	outs\Channel0		
Channel address	18.0		
DI 8/DQ 8\Digital inp	outs\Channel1		
Channel address	18.1		
DI 8/DQ 8\Digital inp	outs\Channel2		
Channel address	18.2		
DI 8/DQ 8\Digital inp	outs\Channel3		
Channel address	18.3		
DI 8/DQ 8\Digital inp	outs\Channel4		
Channel address	18.4		
DI 8/DQ 8\Digital inp	outs\Channel5		
Channel address	18.5		
DI 8/DQ 8\Digital inp	outs\Channel6		
Channel address	18.6		
DI 8/DQ 8\Digital inp	outs\Channel7		
Channel address	18.7		
DI 8/DQ 8\Digital ou	tputs		
	OP Use substitute value		
DI 8/DQ 8\Digital ou	tputs\Channel0		
Channel address	Q8.0	Substitute a value of 1 on a change from RUN to STOP.	0
DI 8/DQ 8\Digital ou	tputs\Channel1		
Channel address	Q8.1	Substitute a value of 1 on a change from RUN to STOP.	0
DI 8/DQ 8\Digital out	tputs\Channel2		
Channel address	Q8.2	Substitute a value of 1 on a change from RUN to STOP.	0
DI 8/DQ 8\Digital ou	tputs\Channel3	"	
Channel address	Q8.3	Substitute a value of 1 on a change from RUN to STOP.	0
DI 8/DQ 8\Digital out	tputs\Channel4		
Channel address	Q8.4	Substitute a value of 1 on a change from RUN to STOP.	0

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Automation Portal			
DI 8/DQ 8\Digital outp	outs\Channel5		
Channel address	Q8.5	Substitute a value of 1 on a change from RUN to STOP.	0
DI 8/DQ 8\Digital outp			
Channel address	Q8.6	Substitute a value of 1 on a change from RUN to STOP.	0
DI 8/DQ 8\Digital outp	outs\Channel7	"	
Channel address	Q8.7	Substitute a value of 1 on a change from RUN to STOP.	0
DI 8/DQ 8\I/O addresse	es\Input addresses		
Start address	8.0	End address	8.7
Organization block	0	Process image	0
DI 8/DQ 8\I/O addresse		"	
Start address	8.0	End address	8.7
Organization block	0	Process image	0
J	1		1