



1. Description

1.1. Project

| | |
|-----------------|---------------------|
| Project Name | instrumento_virtual |
| Board Name | NUCLEO-F334R8 |
| Generated with: | STM32CubeMX 6.8.1 |
| Date | 11/03/2023 |

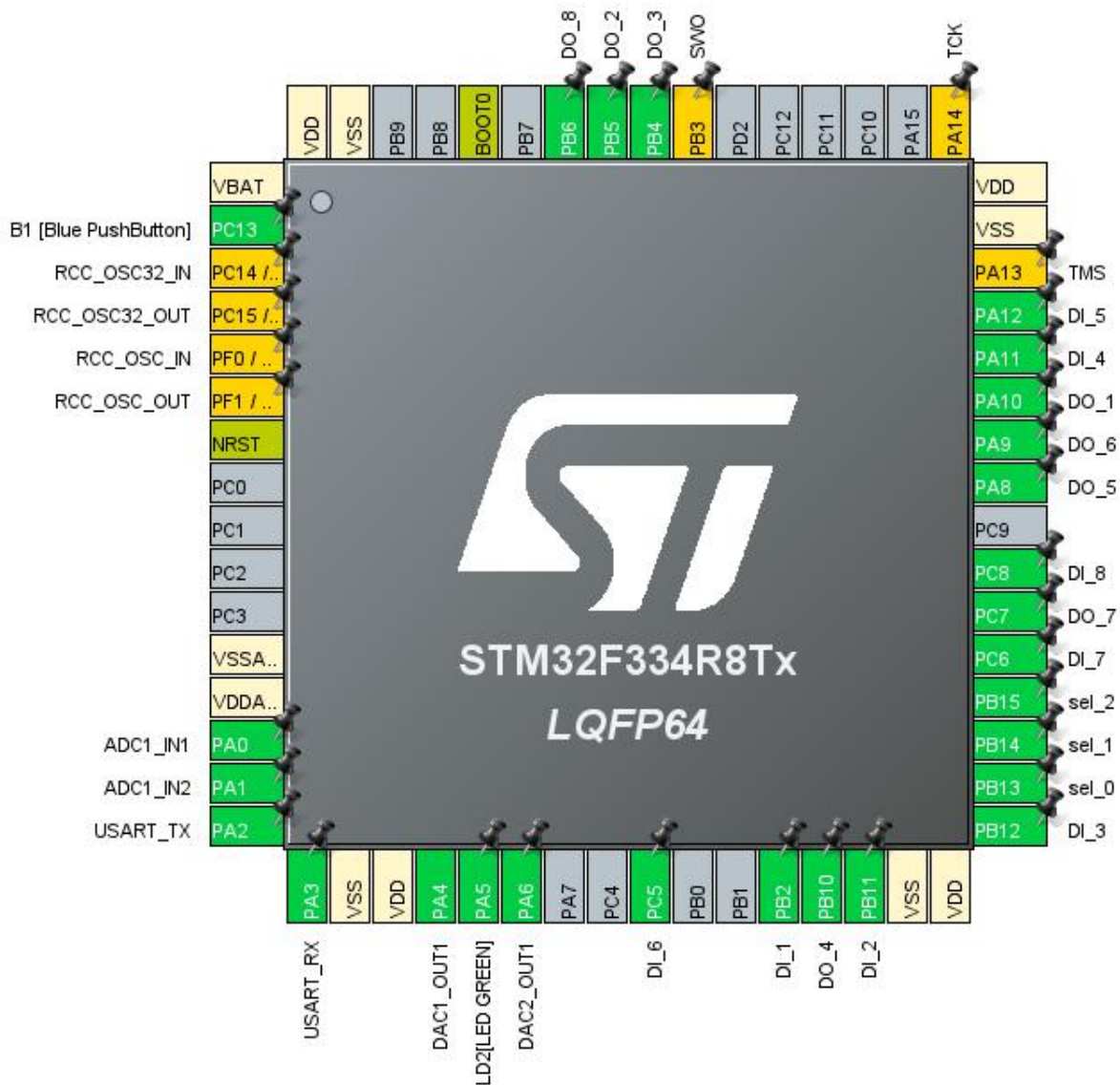
1.2. MCU

| | |
|----------------|---------------|
| MCU Series | STM32F3 |
| MCU Line | STM32F334 |
| MCU name | STM32F334R8Tx |
| MCU Package | LQFP64 |
| MCU Pin number | 64 |

1.3. Core(s) information

| | |
|---------|---------------|
| Core(s) | Arm Cortex-M4 |
|---------|---------------|

2. Pinout Configuration



3. Pins Configuration

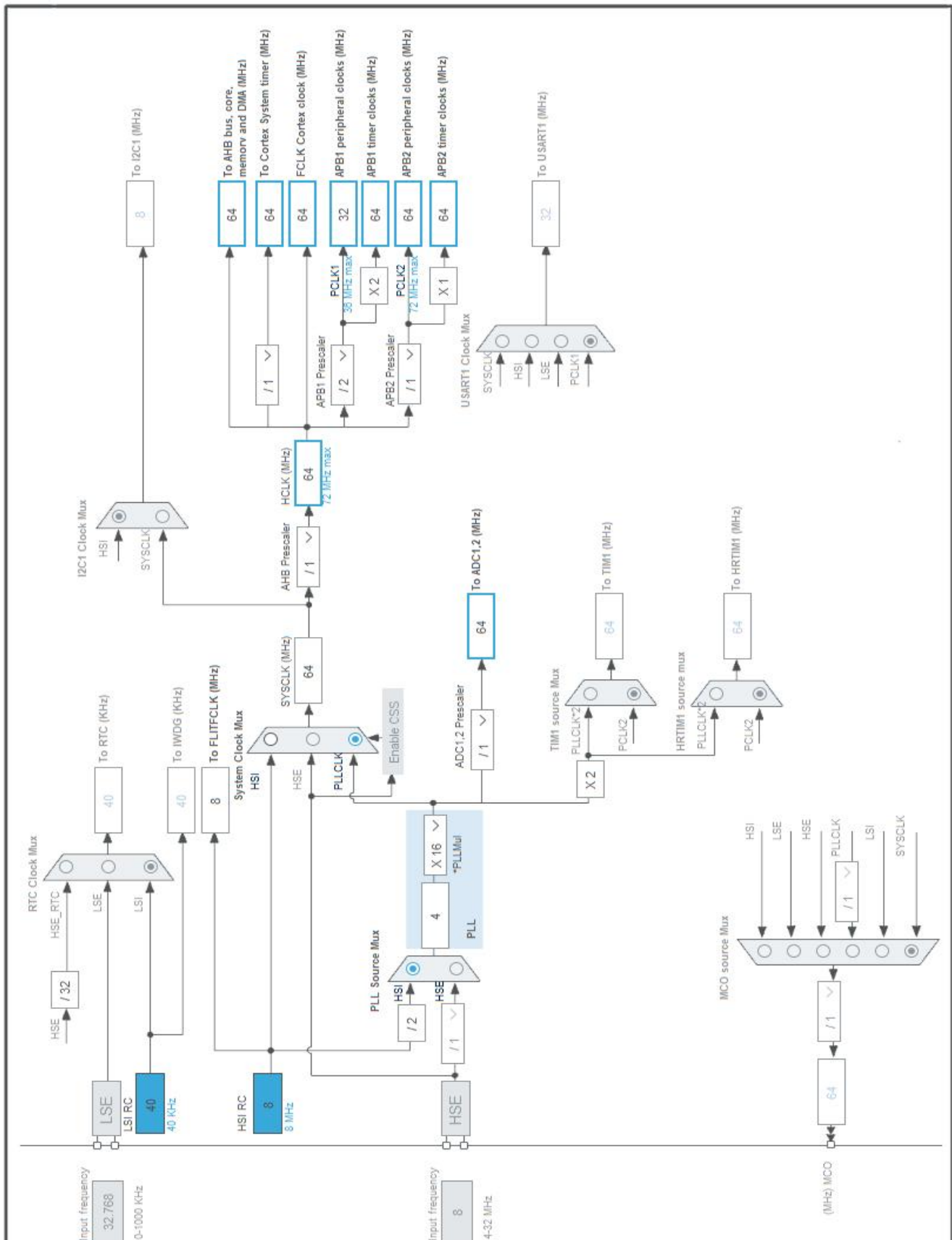
| Pin Number LQFP64 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|----------------------|
| 1 | VBAT | Power | | |
| 2 | PC13 | I/O | GPIO_EXTI13 | B1 [Blue PushButton] |
| 3 | PC14 / OSC32_IN * | I/O | RCC_OSC32_IN | |
| 4 | PC15 / OSC32_OUT * | I/O | RCC_OSC32_OUT | RCC_OSC32_OUT |
| 5 | PF0 / OSC_IN * | I/O | RCC_OSC_IN | |
| 6 | PF1 / OSC_OUT * | I/O | RCC_OSC_OUT | |
| 7 | NRST | Reset | | |
| 12 | VSSA/VREF- | Power | | |
| 13 | VDDA/VREF+ | Power | | |
| 14 | PA0 | I/O | ADC1_IN1 | |
| 15 | PA1 | I/O | ADC1_IN2 | |
| 16 | PA2 | I/O | USART2_TX | USART_TX |
| 17 | PA3 | I/O | USART2_RX | USART_RX |
| 18 | VSS | Power | | |
| 19 | VDD | Power | | |
| 20 | PA4 | I/O | DAC1_OUT1 | |
| 21 | PA5 ** | I/O | GPIO_Output | LD2[LED GREEN] |
| 22 | PA6 | I/O | DAC2_OUT1 | |
| 25 | PC5 ** | I/O | GPIO_Input | DI_6 |
| 28 | PB2 ** | I/O | GPIO_Input | DI_1 |
| 29 | PB10 ** | I/O | GPIO_Output | DO_4 |
| 30 | PB11 ** | I/O | GPIO_Input | DI_2 |
| 31 | VSS | Power | | |
| 32 | VDD | Power | | |
| 33 | PB12 ** | I/O | GPIO_Input | DI_3 |
| 34 | PB13 ** | I/O | GPIO_Output | sel_0 |
| 35 | PB14 ** | I/O | GPIO_Output | sel_1 |
| 36 | PB15 ** | I/O | GPIO_Output | sel_2 |
| 37 | PC6 ** | I/O | GPIO_Input | DI_7 |
| 38 | PC7 ** | I/O | GPIO_Output | DO_7 |
| 39 | PC8 ** | I/O | GPIO_Input | DI_8 |
| 41 | PA8 ** | I/O | GPIO_Output | DO_5 |
| 42 | PA9 ** | I/O | GPIO_Output | DO_6 |
| 43 | PA10 ** | I/O | GPIO_Output | DO_1 |
| 44 | PA11 ** | I/O | GPIO_Input | DI_4 |
| 45 | PA12 ** | I/O | GPIO_Input | DI_5 |

| Pin Number LQFP64 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|-------|
| 46 | PA13 * | I/O | SYS_JTMS-SWDIO | TMS |
| 47 | VSS | Power | | |
| 48 | VDD | Power | | |
| 49 | PA14 * | I/O | SYS_JTCK-SWCLK | TCK |
| 55 | PB3 * | I/O | SYS_JTDO-TRACESWO | SWO |
| 56 | PB4 ** | I/O | GPIO_Output | DO_3 |
| 57 | PB5 ** | I/O | GPIO_Output | DO_2 |
| 58 | PB6 ** | I/O | GPIO_Output | DO_8 |
| 60 | BOOT0 | Boot | | |
| 63 | VSS | Power | | |
| 64 | VDD | Power | | |

** The pin is affected with an I/O function

* The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

| Name | Value |
|-----------------------------------|---|
| Project Name | instrumento_virtual |
| Project Folder | E:\works\Personal\utn\5to\tecnicas 3\tps_tecnicas\Tecnicas- |
| Toolchain / IDE | STM32CubeIDE |
| Firmware Package Name and Version | STM32Cube FW_F3 V1.11.4 |
| Application Structure | Advanced |
| Generate Under Root | Yes |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

5.2. Code Generation Settings

| Name | Value |
|---|---------------------------------------|
| STM32Cube MCU packages and embedded software | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | No |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power consumption) | No |
| Enable Full Assert | No |

5.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|---------------------|--------------------------|
| 1 | SystemClock_Config | RCC |
| 2 | MX_GPIO_Init | GPIO |
| 3 | MX_DMA_Init | DMA |
| 4 | MX_USART2_UART_Init | USART2 |
| 5 | MX_TIM6_Init | TIM6 |
| 6 | MX_ADC1_Init | ADC1 |
| 7 | MX_DAC2_Init | DAC2 |
| 8 | MX_DAC1_Init | DAC1 |

6. Power Consumption Calculator report

6.1. Microcontroller Selection

| | |
|-----------|---------------|
| Series | STM32F3 |
| Line | STM32F334 |
| MCU | STM32F334R8Tx |
| Datasheet | DS9994_Rev6 |

6.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.6 |

6.3. Battery Selection

| | |
|-------------------|-----------------|
| Battery | Li-SOCL2(A3400) |
| Capacity | 3400.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 100.0 mA |
| Max Pulse Current | 200.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

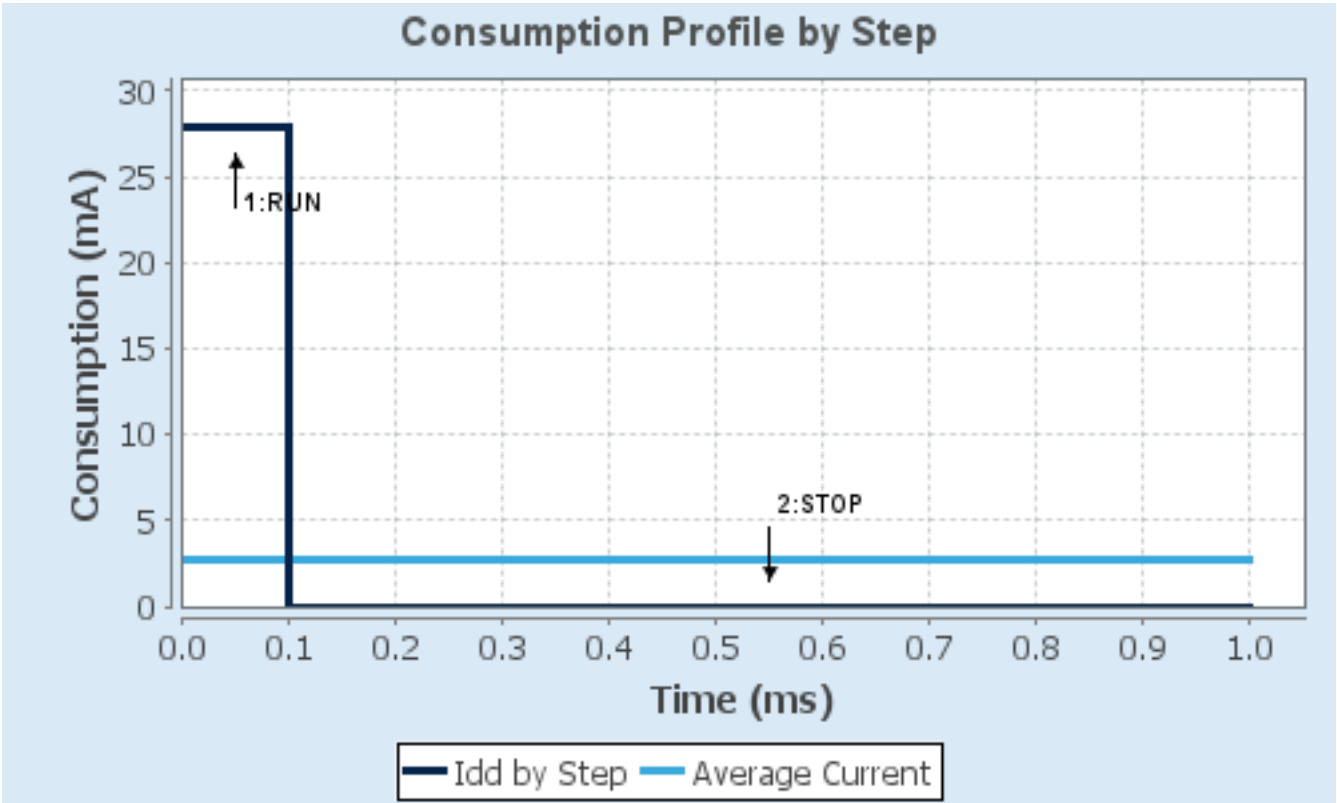
6.4. Sequence

| | | |
|-------------------------------|-------------|--------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP |
| Vdd | 3.6 | 3.6 |
| Voltage Source | Battery | Battery |
| Range | No Scale | No Scale |
| Fetch Type | RAM | n/a |
| CPU Frequency | 72 MHz | 0 Hz |
| Clock Configuration | HSEBYP PLL | Regulator LP |
| Clock Source Frequency | 8 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 27.84 mA | 9.55 μ A |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 90.0 | 0.0 |
| Ta Max | 100.49 | 105 |
| Category | In DS Table | In DS Table |

6.5. Results

| | | | |
|---------------|------------------------------|-----------------|------------|
| Sequence Time | 1 ms | Average Current | 2.79 mA |
| Battery Life | 1 month, 20 days, 5 hours | Average DMIPS | 90.0 DMIPS |

6.6. Chart



7. Peripherals and Middlewares Configuration

7.1. ADC1

IN1: IN1 Single-ended

IN2: IN2 Single-ended

7.1.1. Parameter Settings:

ADCs_Common_Settings:

Mode Independent mode

ADC_Settings:

Clock Prescaler ADC Asynchronous clock mode

Resolution ADC 12-bit resolution

Data Alignment Right alignment

Scan Conversion Mode Disabled

Continuous Conversion Mode Disabled

Discontinuous Conversion Mode Disabled

DMA Continuous Requests Disabled

End Of Conversion Selection End of single conversion

Overrun behaviour Overrun data overwritten

Low Power Auto Wait Disabled

ADC_Regular_ConversionMode:

Enable Regular Conversions Enable

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None

SequencerNbRanks 1

Rank 1

Channel Channel 1

Sampling Time **601.5 Cycles ***

Offset Number No offset

Offset 0

ADC_Injected_ConversionMode:

Enable Injected Conversions Enable

Number Of Conversions 0

Analog Watchdog 1:

Enable Analog WatchDog1 Mode false

Analog Watchdog 2:

Enable Analog WatchDog2 Mode false

Analog Watchdog 3:

Enable Analog WatchDog3 Mode false

7.2. DAC1

mode: OUT1 Configuration

7.2.1. Parameter Settings:

DAC Out1 Settings:

| | |
|---------------|--------|
| Output Buffer | Enable |
| Trigger | None |

7.3. DAC2

OUT1 Configuration: DAC Output switch Enable

7.3.1. Parameter Settings:

DAC Out1 Settings:

| | |
|---------|------|
| Trigger | None |
|---------|------|

7.4. RCC

7.4.1. Parameter Settings:

System Parameters:

| | |
|-------------------|--------------------|
| VDD voltage (V) | 3.3 |
| Prefetch Buffer | Enabled |
| Flash Latency(WS) | 2 WS (3 CPU cycle) |

RCC Parameters:

| | |
|--------------------------------|------|
| HSI Calibration Value | 16 |
| HSE Startup Timeout Value (ms) | 100 |
| LSE Startup Timeout Value (ms) | 5000 |

7.5. SYS

Timebase Source: SysTick

7.6. TIM6

mode: Activated

7.6.1. Parameter Settings:

Counter Settings:

| | |
|---|-----------------|
| Prescaler (PSC - 16 bits value) | 0 |
| Counter Mode | Up |
| Counter Period (AutoReload Register - 16 bits value) | 64000 * |
| auto-reload preload | Enable * |

Trigger Output (TRGO) Parameters:

| | |
|-------------------------|------------------------------|
| Trigger Event Selection | Reset (UG bit from TIMx_EGR) |
|-------------------------|------------------------------|

7.7. USART2

Mode: Asynchronous

7.7.1. Parameter Settings:

Basic Parameters:

| | |
|-------------|---------------------------|
| Baud Rate | 115200 * |
| Word Length | 8 Bits (including Parity) |
| Parity | None |
| Stop Bits | 1 |

Advanced Parameters:

| | |
|----------------|----------------------|
| Data Direction | Receive and Transmit |
| Over Sampling | 16 Samples |
| Single Sample | Disable |

Advanced Features:

| | |
|-------------------------------|---------|
| TX Pin Active Level Inversion | Disable |
| RX Pin Active Level Inversion | Disable |
| Data Inversion | Disable |
| TX and RX Pins Swapping | Disable |
| Overrun | Enable |
| DMA on RX Error | Enable |
| MSB First | Disable |

*** User modified value**

8. System Configuration

8.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|-----------------------|------------------|-------------------|--|-----------------------------|-----------|----------------------|
| ADC1 | PA0 | ADC1_IN1 | Analog mode | No pull-up and no pull-down | n/a | |
| | PA1 | ADC1_IN2 | Analog mode | No pull-up and no pull-down | n/a | |
| DAC1 | PA4 | DAC1_OUT1 | Analog mode | No pull-up and no pull-down | n/a | |
| DAC2 | PA6 | DAC2_OUT1 | Analog mode | No pull-up and no pull-down | n/a | |
| USART2 | PA2 | USART2_TX | Alternate Function Push Pull | No pull-up and no pull-down | High * | USART_TX |
| | PA3 | USART2_RX | Alternate Function Push Pull | No pull-up and no pull-down | High * | USART_RX |
| Single Mapped Signals | PC14 / OSC32_IN | RCC_OSC32_IN | n/a | n/a | n/a | |
| | PC15 / OSC32_OUT | RCC_OSC32_OUT | n/a | n/a | n/a | RCC_OSC32_OUT |
| | PF0 / OSC_IN | RCC_OSC_IN | n/a | n/a | n/a | |
| | PF1 / OSC_OUT | RCC_OSC_OUT | n/a | n/a | n/a | |
| | PA13 | SYS_JTMS-SWDIO | n/a | n/a | n/a | TMS |
| | PA14 | SYS_JTCK-SWCLK | n/a | n/a | n/a | TCK |
| | PB3 | SYS_JTDO-TRACESWO | n/a | n/a | n/a | SWO |
| GPIO | PC13 | GPIO_EXTI13 | External Interrupt Mode with Falling edge trigger detection | No pull-up and no pull-down | n/a | B1 [Blue PushButton] |
| | PA5 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | LD2[LED GREEN] |
| | PC5 | GPIO_Input | Input mode | Pull down * | n/a | DI_6 |
| | PB2 | GPIO_Input | Input mode | Pull down * | n/a | DI_1 |
| | PB10 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_4 |
| | PB11 | GPIO_Input | Input mode | Pull down * | n/a | DI_2 |
| | PB12 | GPIO_Input | Input mode | Pull down * | n/a | DI_3 |
| | PB13 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | sel_0 |
| | PB14 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | sel_1 |
| | PB15 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | sel_2 |
| | PC6 | GPIO_Input | Input mode | Pull down * | n/a | DI_7 |
| | PC7 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_7 |
| | PC8 | GPIO_Input | Input mode | Pull down * | n/a | DI_8 |

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|----|------|-------------|------------------|-----------------------------|-----------|------------|
| | PA8 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_5 |
| | PA9 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_6 |
| | PA10 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_1 |
| | PA11 | GPIO_Input | Input mode | Pull down * | n/a | DI_4 |
| | PA12 | GPIO_Input | Input mode | Pull down * | n/a | DI_5 |
| | PB4 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_3 |
| | PB5 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_2 |
| | PB6 | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low | DO_8 |

8.2. DMA configuration

| DMA request | Stream | Direction | Priority |
|-------------|---------------|----------------------|----------|
| USART2_TX | DMA1_Channel7 | Memory To Peripheral | Low |

USART2_TX: DMA1_Channel7 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: **Enable ***
Peripheral Data Width: Byte
Memory Data Width: Byte

8.3. NVIC configuration

8.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| Memory management fault | true | 0 | 0 |
| Pre-fetch fault, memory access fault | true | 0 | 0 |
| Undefined instruction or illegal state | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Debug monitor | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 0 | 0 |
| DMA1 channel7 global interrupt | true | 0 | 0 |
| USART2 global interrupt / USART2 wake-up interrupt through EXT line 26 | true | 0 | 0 |
| TIM6 global and DAC1 underrun error interrupts | true | 0 | 0 |
| PVD interrupt through EXTI line 16 | unused | | |
| Flash global interrupt | unused | | |
| RCC global interrupt | unused | | |
| ADC1 and ADC2 interrupts | unused | | |
| EXTI line[15:10] interrupts | unused | | |
| TIM7 global and DAC2 underrun error interrupts | unused | | |
| Floating point unit interrupt | unused | | |

8.3.2. NVIC Code generation

| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|--|--------------------------------------|-------------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| Memory management fault | false | true | false |
| Pre-fetch fault, memory access fault | false | true | false |
| Undefined instruction or illegal state | false | true | false |
| System service call via SWI instruction | false | true | false |
| Debug monitor | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |
| DMA1 channel7 global interrupt | false | true | true |
| USART2 global interrupt / USART2 wake- up interrupt through EXT line 26 | false | true | true |
| TIM6 global and DAC1 underrun error interrupts | false | true | true |

*** User modified value**

9. System Views

9.1. Category view

9.1.1. Current

| Middleware | | | | |
|-------------|--------|--------|--------------|-----------|
| System Core | Analog | Timers | Connectivity | Computing |
| DMA | ADC1 | TIM6 | USART2 | |
| GPIO | DAC1 | | | |
| IVIC | DAC2 | | | |
| RCC | | | | |
| SYS | | | | |

10. Docs & Resources

| Type | Link |
|-------------------------|---|
| BSDL files | https://www.st.com/resource/en/bsdl_model/stm32f3_bsd.zip |
| System View Description | https://www.st.com/resource/en/svd/stm32f3-svd.zip |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf |
| Brochures | https://www.st.com/resource/en/brochure/breveco0518.pdf |
| Brochures | https://www.st.com/resource/en/brochure/brstm32f3.pdf |
| Brochures | https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32nucleo.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstmcsuite.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flpowerstbd.pdf |
| Flyers | https://www.st.com/resource/en/flyer/fldpstpf11120.pdf |
| Product Certifications | https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus |

and-mpus-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3236-increase-the-number-of-touchkeys-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3960-esd-considerations-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4045-stm32f3-series-inapplication-programming-iap-using-the-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4099-implementation-of-transmitters-and-receivers-for-infrared-remote-control-protocols-with-mcus-of-the-stm32f0-and-stm32f3-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4206-getting-started-with-stm32f3-series-hardware-development-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4228-migrating-from-stm32f1-series-to-stm32f3-series-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes [---

Page 20](https://www.st.com/resource/en/application_note/an4232-getting-started-</p></div><div data-bbox=)

with-analog-comparators-for-stm32f3-series-and-stm32g4-series-devices-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4296-use-stm32f3stm32g4-ccm-sram-with-iar-embedded-workbench-keil-mdkarm-stmicroelectronics-stm32cubeide-and-other-gnubased-toolchains-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4299-improve-conducted-noise-robustness-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4310-sampling-capacitor-selection-guide-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4312-design-with-surface-sensors-for-touch-sensing-applications-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4316-tuning-a-touch-sensing-application-on-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4538-power-consumption-optimization-with-stm32f3xx-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4539-hrtim-cookbook-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4651-stm32f3-series-peripheral-interconnect-matrix-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4734-stm32cube-firmware-examples-for-stm32f3-series-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4759-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4908-stm32-usart-automatic-baud-rate-detection-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf

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