| Time | | |
|---|---|---|
| rst=x | | |
| clk_ice=0 | | |
| clk_ULPI=0 | | |
| clk_div_ice=0 | | |
| clk_div_ULPI=0 | | |
| clk_debounce=0 | | |
| IO_LEDs[4:0]=0 | | |

UART

| clk_Rx=0 | | |
|---|---|---|
| Rx=1 | | |
| O_DATA[7:0]=xx | 00 | 00 |
| clk_Tx=0 | | |
| Tx=1 | | |
| I_DATA[7:0]=00 | FF | 03 |
| NxT=0 | | |
| send_data=0 | | |
| Rx_EMPTY=1 | | |
| Tx_FULL=0 | | |

ULPI

| ADDR[5:0]=00 | 00 | |
|---|---|---|
| PrR=0 | | |
| REG_VAL_R[7:0]=00 | 34 | |
| PrW=0 | | |
| REG_VAL_W[7:0]=00 | E9 | |
| ULPI_state_r[2:0]=000 | 001 | + 001 | 001 |
| RxCMD[7:0]=00 | 96 | 96 | B7 |
| USB_DATA[7:0]=xx | xx | A4 xx | |
| USB_INFO_DATA[15:0]=xxxx | xxxx | xxxx | |
| ULPI_DIR=0 | | |
| ULPI_DATA[7:0]=00 | 00 | 00 | 00 |
| ULPI_NXT=0 | | |
| ULPI_RST=x | | |
| ULPI_STP=0 | | |

Operation buffer

| op_stack_pull=0 | | |
|---|---|---|
| op_stack_msg[15:0]=xxxx | xxxx | x+ |
| op_stack_empty=1 | | |

Main controller

| force_send=0 | | |
|---|---|---|
| MAIN_state_r[3:0]=0 | 0 | 0 | 0 |
| MAIN_cmd_r[1:0]=00 | 10 | |
| ULPI_ADDR_r[5:0]=00 | 00 | |
| UART_Tx_DATA_r[7:0]=00 | FF | 03 |

Buttons

| IO_BTNs[1:0]=11 | 11 | |
|---|---|---|
| IO_BTNs[1]=1 | | |
| IO_BTNs[0]=1 | | |
| test_counter[31:0]=0 | 7 | 8 | 9 | 10 |

Random

| toggle_r=0 | | |
|---|---|---|
| btn2_pulse=0 | | |