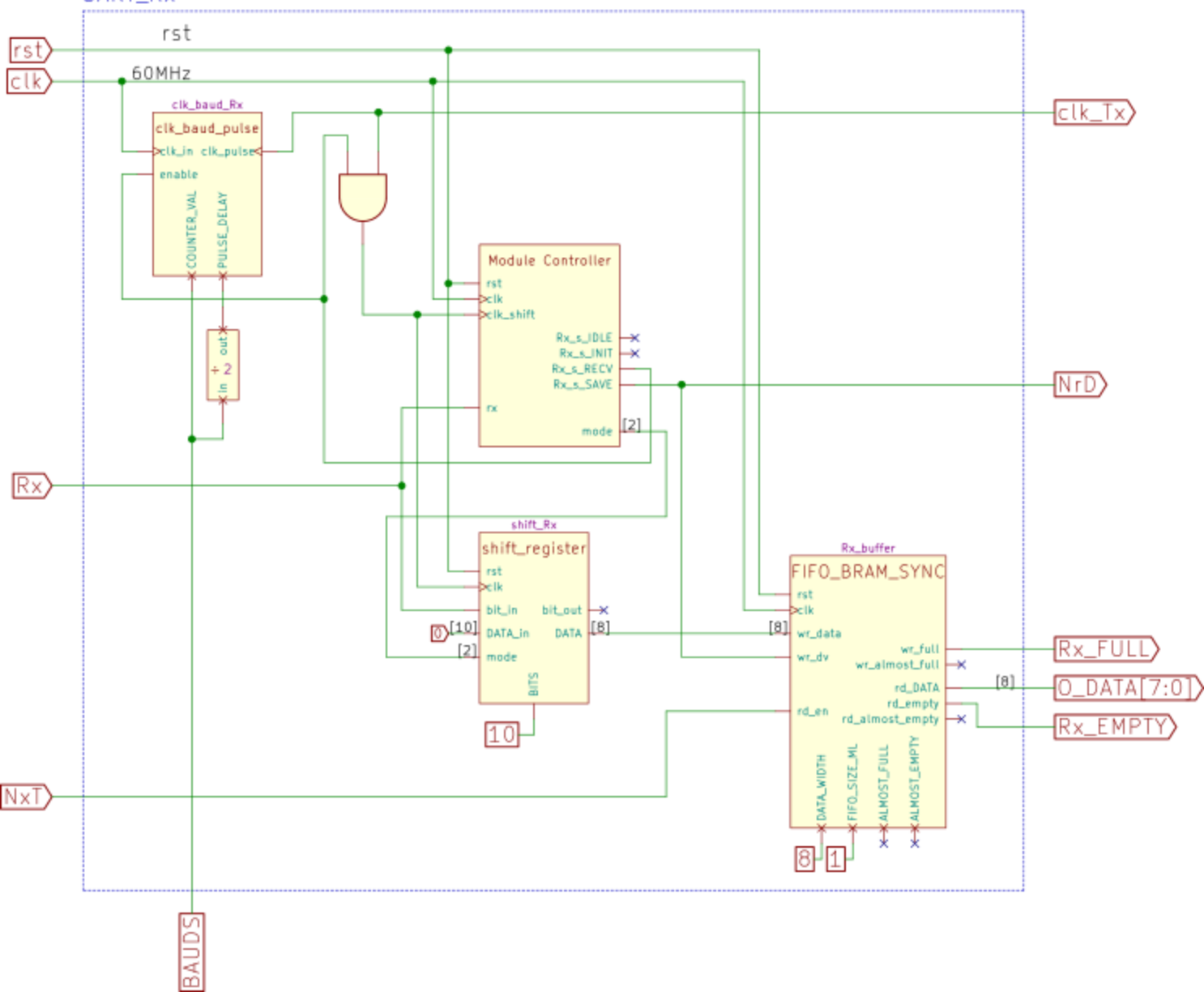
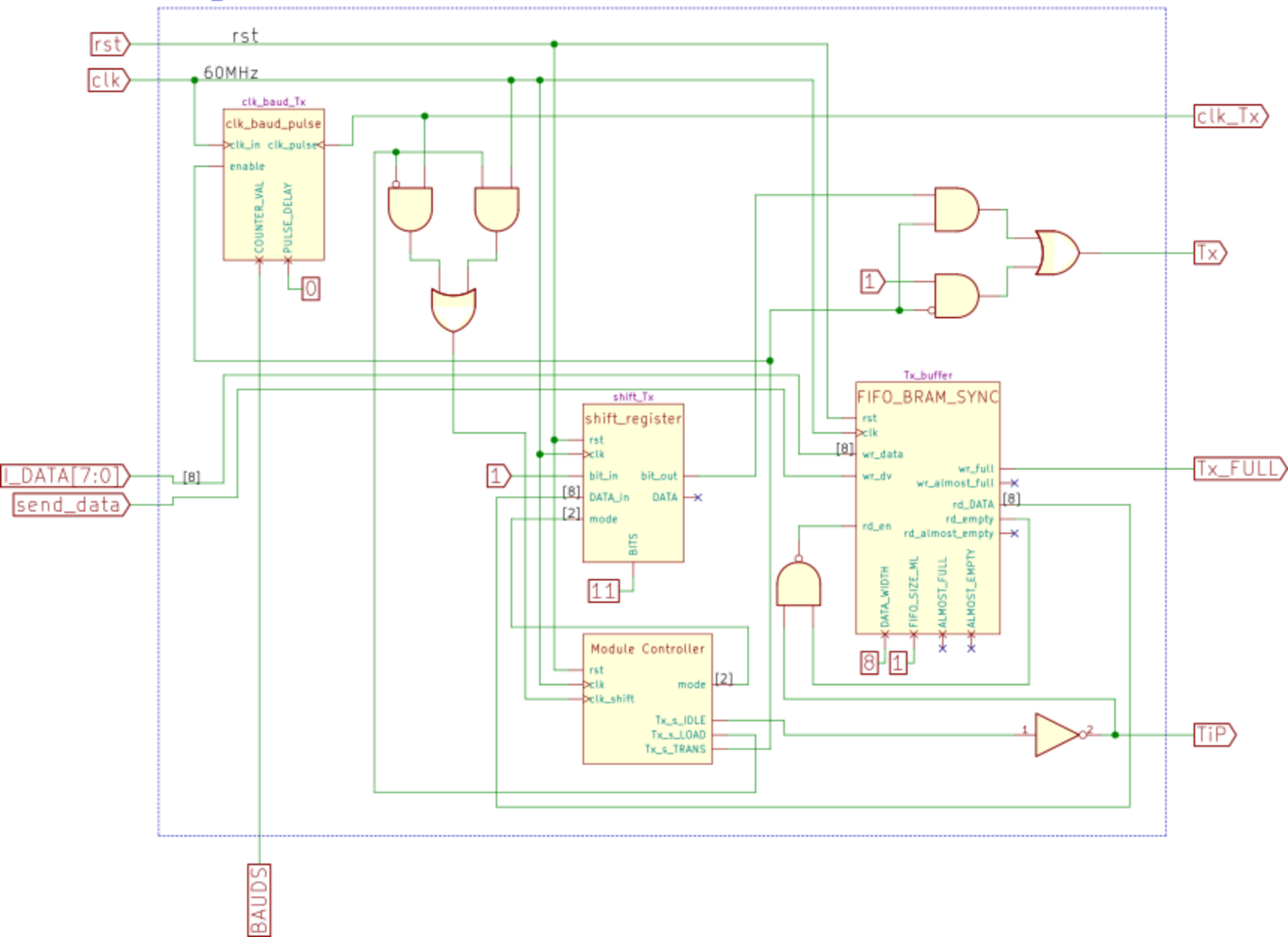


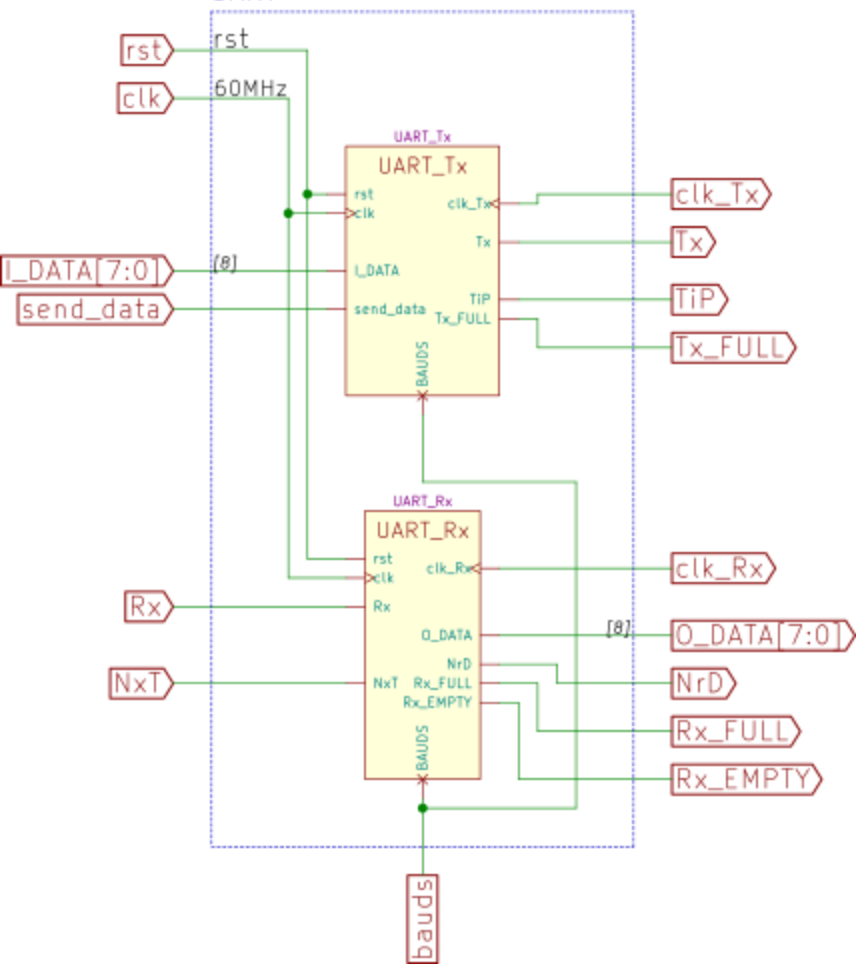
## UART\_Rx



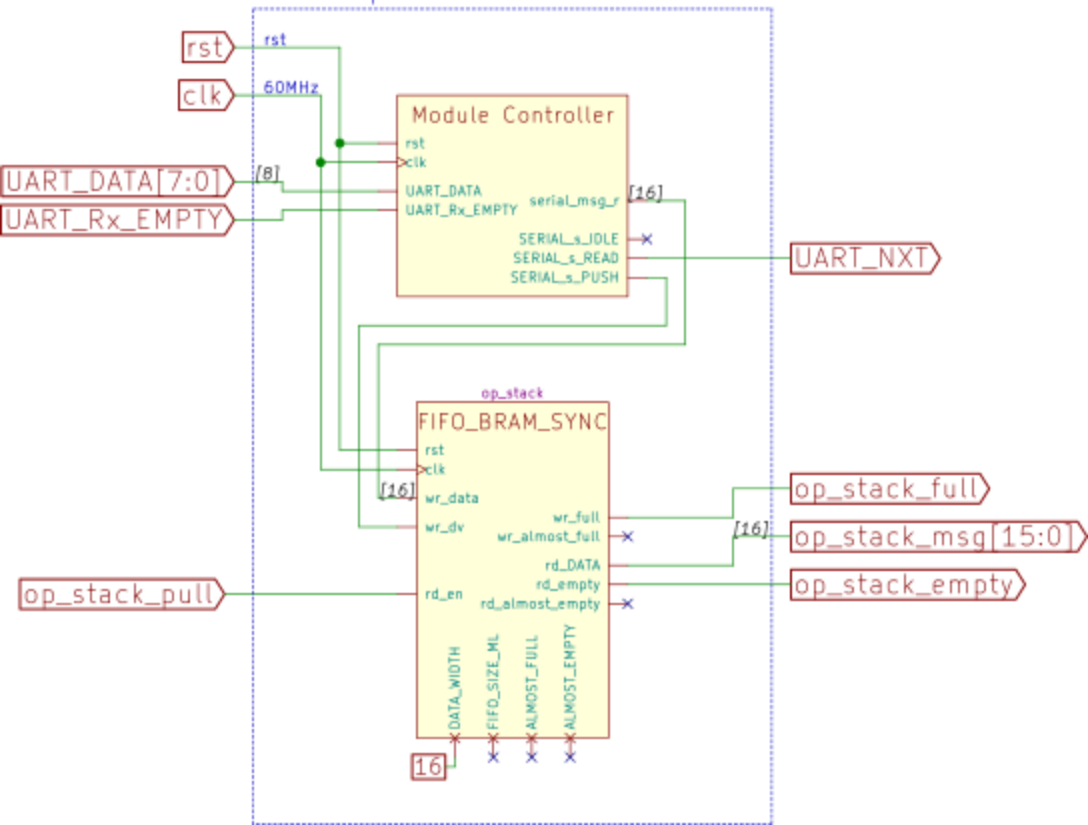
## UART\_Tx



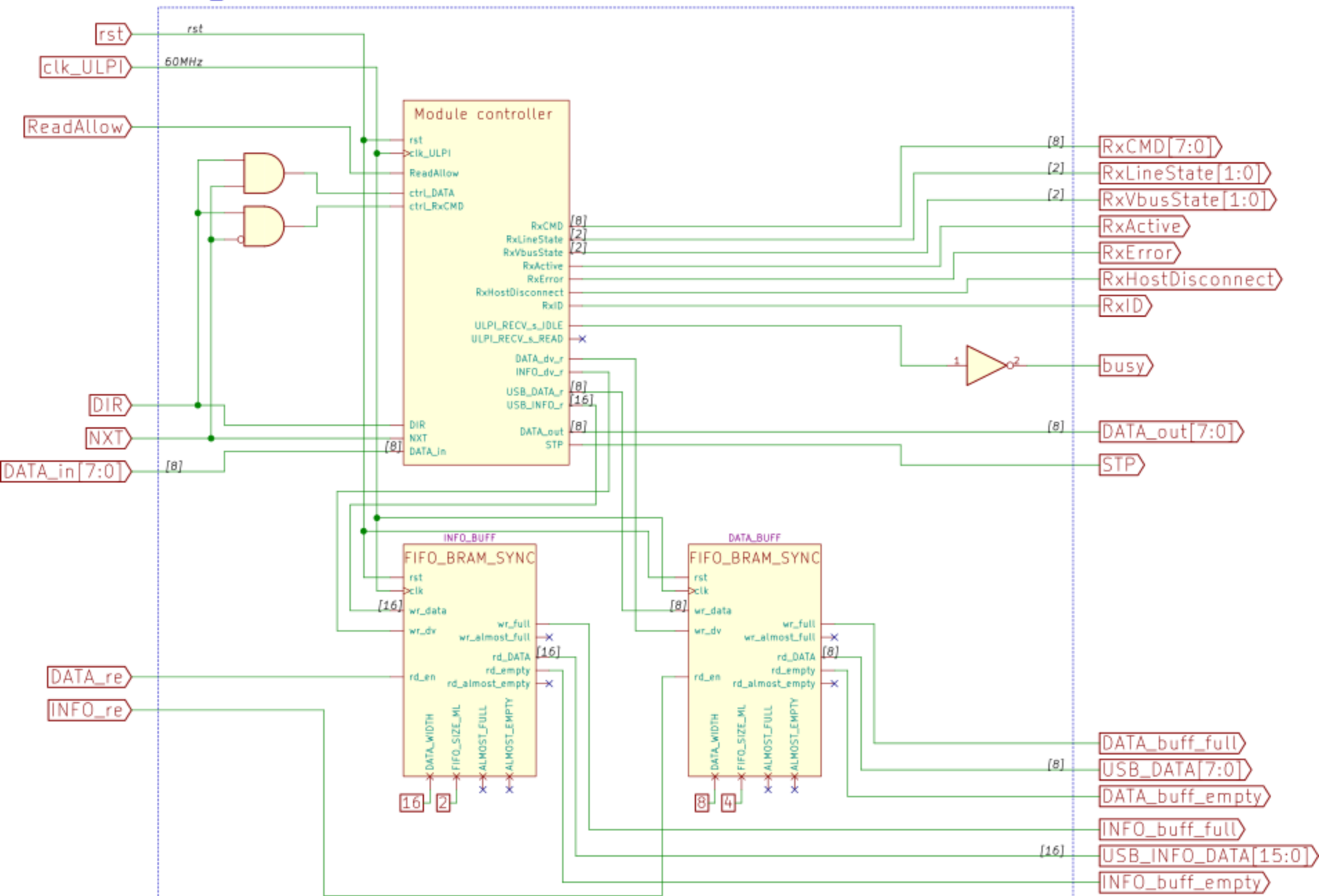
## UART



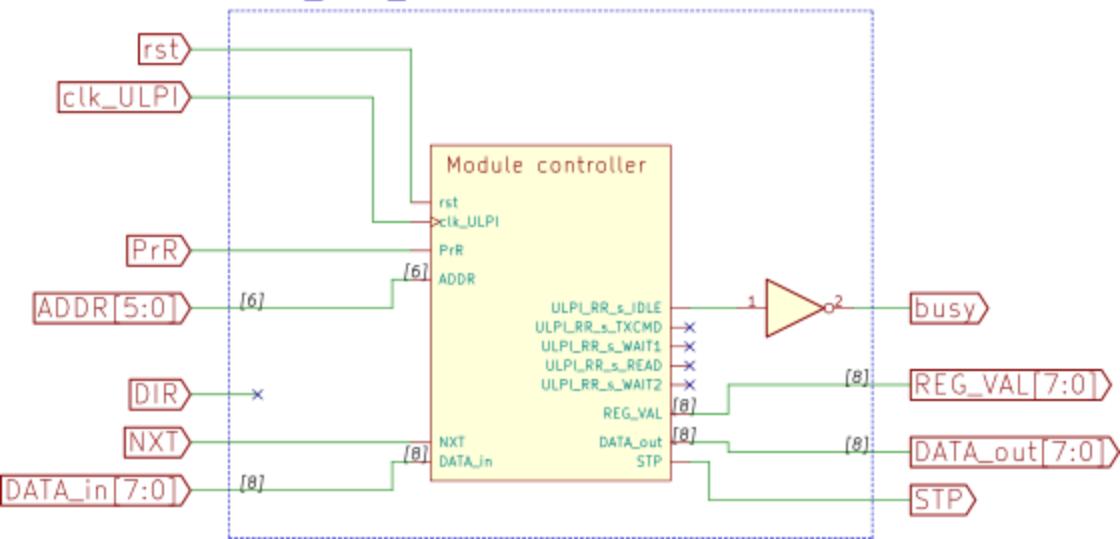
# ULPI\_op\_stack



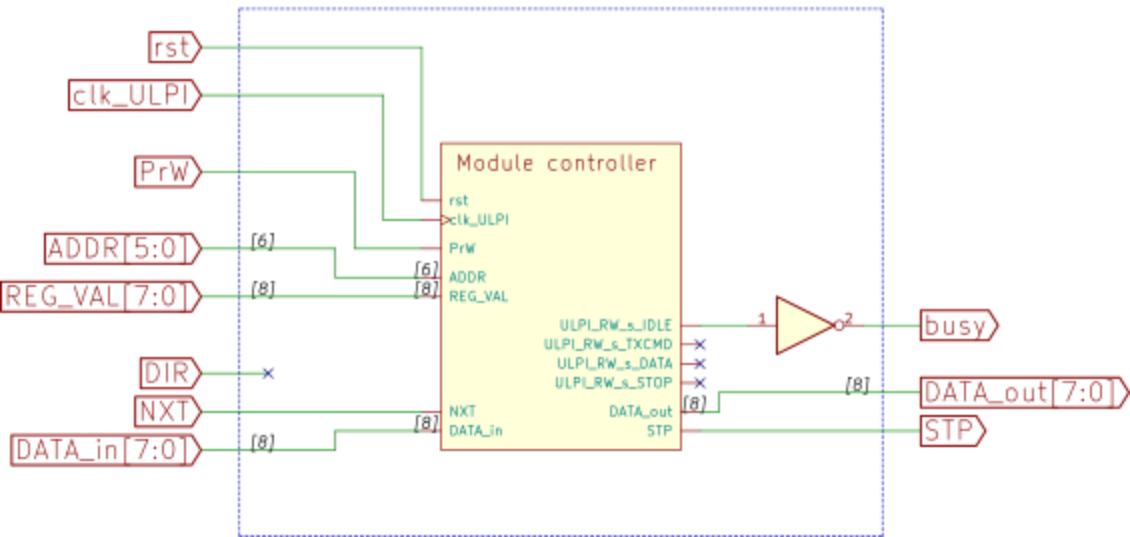
# ULPI\_RECV



# ULPI\_REG\_READ



# ULPI\_REG\_WRITE





# ULPI

