

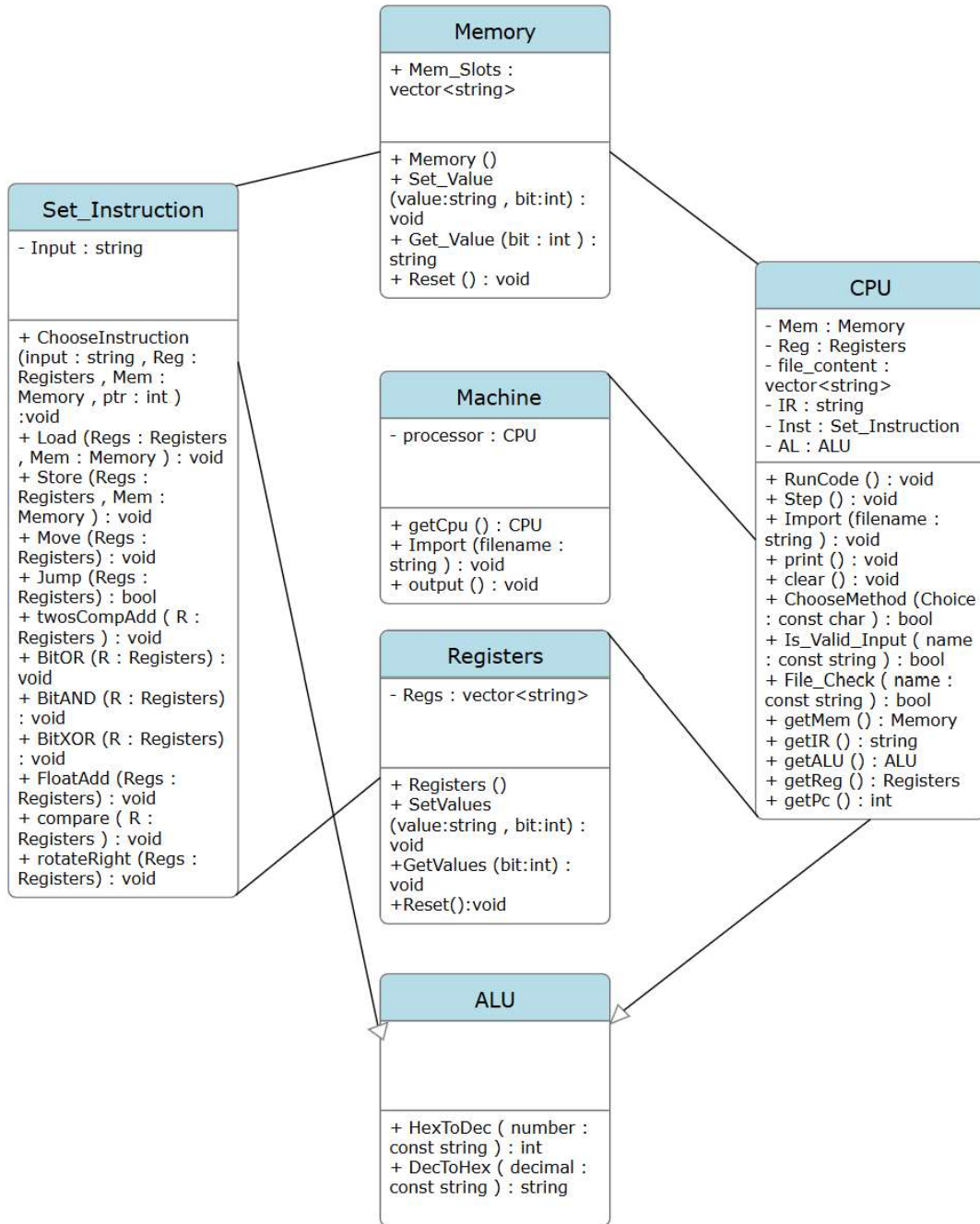


**Cairo University**  
**Faculty of Computers and Artificial**  
**Intelligence**  
**CS-213**  
**second year**  
**Assignment 1 - Task4,5**  
**Subjected To: Dr Mohamed EL Ramly**

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## UML design for the relation of the classes:



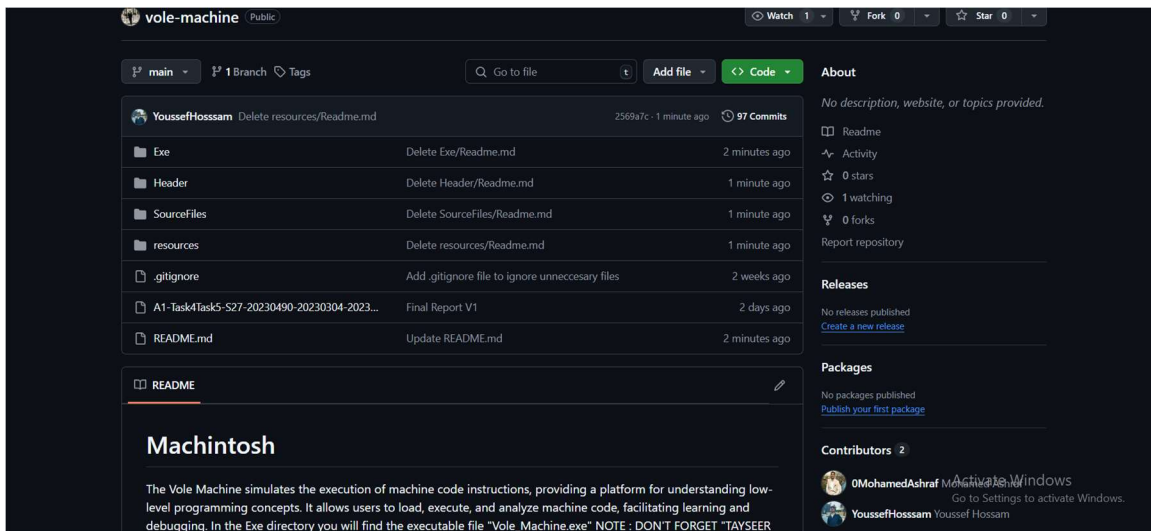
## Work break-down table:

The whole team collaborate in each function to create the vole machine perfectly but to be more specified:

Mario Dawod Botros	Youssef Hossam Ibrahim	Mohamed Ashraf saeed
Did all the header and implementation of these classes 1. CPU 2. Registers 3. Memory And the headers of 1. ALU 2. Set_Instructions	Did these instructions 1. Float addition 2. Bitwise OR 3. Bitwise XOR 4. Bitwise AND 5. Jump with two's complement	Did the GUI And these instructions 1. Two's complement addition 2. Jump 3. The copy instructions

## GitHub Work:

<https://github.com/mario985/vole-machine>



## Video Link:

<https://www.youtube.com/watch?v=RbV5RSfYBn0>