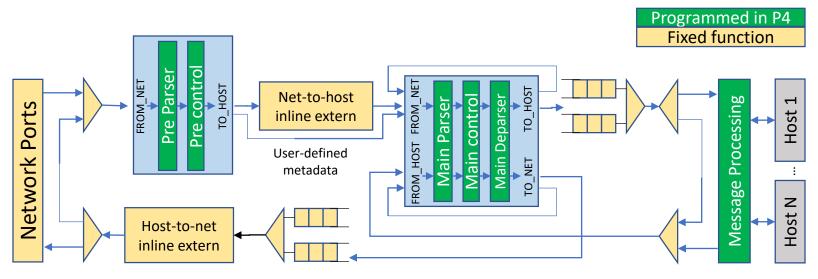


**v3-small**: Same as v3, except reduced in size somewhat, and font sizes correspondly decreased to fit, for use in slides that are half figure, half text.



**v4**: This was proposed and discussed at 2020-Dec-07 PNA meeting. It seems possible that organizing P4 code in 4 blocks as shown here would be straightforward for a P4 compiler to take and target a hardware architecture that looks more like v3. (Clearly code organized like this should be easy to target to a hardware architecture that looks like v4).

