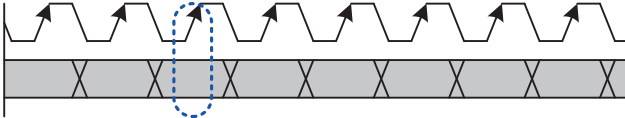


FMC=>FPGA

HDMITX_CLK
VSYNC,HSYNC,
DE,DATA



Output data are synchronous with down edge of HDMITX_CLK.