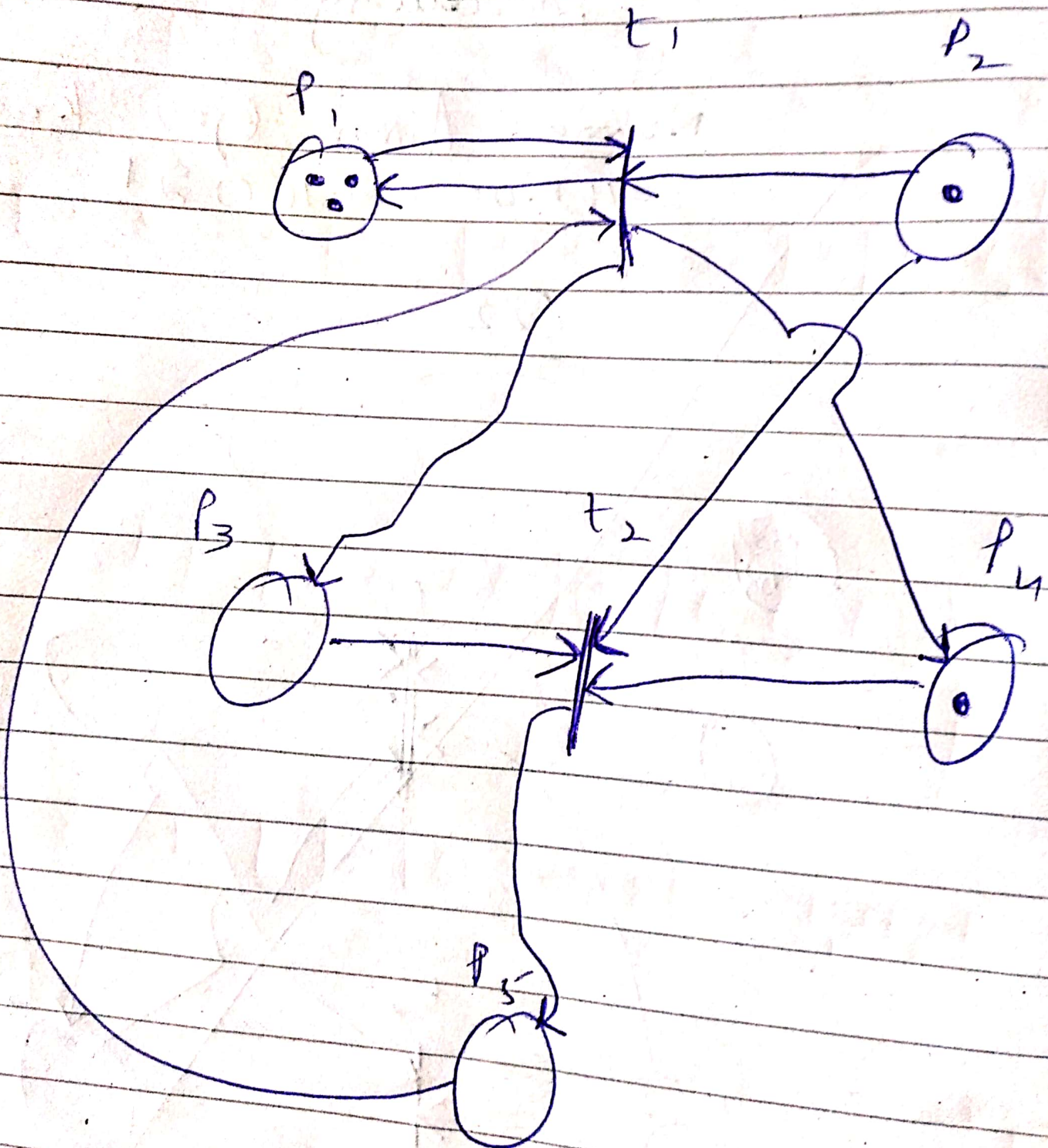


Aimen Ejaz kazi

CS-021

Q2



Q1

$$M' = M_0 + \sigma \Delta$$

multiplication not possible with t_3 2 transistors

$$\begin{bmatrix} 1 & 8 & 0 & 1 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 1 & 0 \end{bmatrix} + \begin{bmatrix} t_1 & t_2 & t_3 \end{bmatrix} \begin{bmatrix} 0 & -1 & 1 & 0 \\ 0 & 2 & 1 & -1 \\ 0 & 0 & -1 & 1 \end{bmatrix}$$

$$\begin{bmatrix} 1 & 8 & 0 & 1 \end{bmatrix} - \begin{bmatrix} 1 & 0 & 1 & 0 \end{bmatrix} = \begin{bmatrix} t_1 & t_2 & t_3 \end{bmatrix} \begin{bmatrix} 0 & -1 & 1 & 0 \\ 0 & 2 & 1 & -1 \\ 0 & 0 & -1 & 1 \end{bmatrix}$$

$$\begin{bmatrix} 0 & 8 & -1 & 1 \end{bmatrix} = \begin{bmatrix} 0 & -t_1 + 2t_2 & -t_1 + t_2 - t_3 & -t_2 + t_3 \end{bmatrix}$$

$$2t_2 - t_1 = 8 \rightarrow \textcircled{1}$$

$$t_2 - t_1 - t_3 = -1 \rightarrow \textcircled{2}$$

$$t_3 - t_2 = 1 \rightarrow \textcircled{3}$$

$$t' = 0$$

$$t'_2 = 4$$

$$t'_3 = 5$$

$$\sigma = (0 \ 4 \ 5)$$

$$\sigma = t_2 t_2 t_2 t_2 t_3 t_3 t_3 t_3 t_3$$