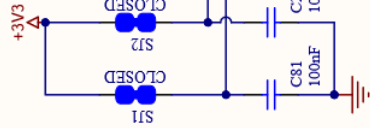
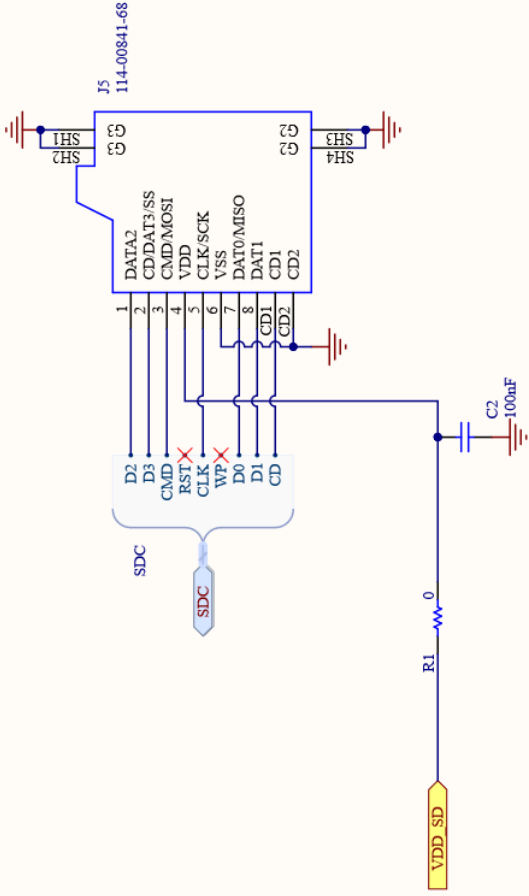


1	2	3	4	5	6	7	8																																												
A	<div><div>LiPo</div><div></div></div>	<div><div>MISC</div><div></div></div>	<div><div>ETHERNET</div><div></div></div>	<div><div>SPI</div><div></div></div>	<div><div>CAN</div><div></div></div>	<div><div>SPDIF</div><div></div></div>																																													
B	<div><div>BOOT/PWRON</div><div></div></div> <div><div>VIN</div><div></div></div>	<div><div>GPIO</div><div></div></div>			<div><div>PDM</div><div></div></div>	<div><div>SAI</div><div></div></div>																																													
C	<div><div>SHARED PINS TABLE (RED WIRES)</div><table><thead><tr><th>NET</th><th>BUS 1</th><th>BUS 2</th></tr></thead><tbody><tr><td>I2C1_SCL</td><td>OPENMV_CAM_SCL</td><td>OPENMV_CAM_SCL</td></tr><tr><td>I2C2_SCL</td><td>OPENMV_CAM_SDA</td><td>OPENMV_CAM_SDA</td></tr><tr><td>SPI1_MISO</td><td>OPENMV_CAM_MISO</td><td>OPENMV_CAM_MISO</td></tr><tr><td>SPI1_MOSI</td><td>OPENMV_CAM_MOSI</td><td>OPENMV_CAM_MOSI</td></tr><tr><td>SPI1_CS</td><td>OPENMV_CAM_CS</td><td>OPENMV_CAM_CS</td></tr><tr><td>GPIO_0</td><td>OPENMV_CAM_RST</td><td>OPENMV_CAM_RST</td></tr><tr><td>GPIO_1</td><td>OPENMV_CAM_PWDN</td><td>OPENMV_CAM_PWDN</td></tr><tr><td>GPIO_3</td><td>OPENMV_CAM_CLK</td><td>OPENMV_CAM_CLK</td></tr><tr><td>PWM0</td><td></td><td></td></tr><tr><td>GPIO_4</td><td>TRACEDATA0(ITAG)</td><td>TRACEDATA0(ITAG)</td></tr><tr><td>UART2_TX</td><td>UART2_TX</td><td>UART2_TX</td></tr><tr><td>PDM_CK</td><td></td><td></td></tr><tr><td>GPIO_2</td><td></td><td></td></tr><tr><td>GPIO_5</td><td></td><td></td></tr></tbody></table></div>		NET	BUS 1	BUS 2	I2C1_SCL	OPENMV_CAM_SCL	OPENMV_CAM_SCL	I2C2_SCL	OPENMV_CAM_SDA	OPENMV_CAM_SDA	SPI1_MISO	OPENMV_CAM_MISO	OPENMV_CAM_MISO	SPI1_MOSI	OPENMV_CAM_MOSI	OPENMV_CAM_MOSI	SPI1_CS	OPENMV_CAM_CS	OPENMV_CAM_CS	GPIO_0	OPENMV_CAM_RST	OPENMV_CAM_RST	GPIO_1	OPENMV_CAM_PWDN	OPENMV_CAM_PWDN	GPIO_3	OPENMV_CAM_CLK	OPENMV_CAM_CLK	PWM0			GPIO_4	TRACEDATA0(ITAG)	TRACEDATA0(ITAG)	UART2_TX	UART2_TX	UART2_TX	PDM_CK			GPIO_2			GPIO_5			<div><div>ANALOG</div><div></div></div>	<div><div>PWM</div><div></div></div>	<div><div>PCIE</div><div></div></div>	
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Title: Portenta Breakout Carrier

ID: ASX00031

Revision: V1.3

Date: 07/04/2022

Time: 16:39:27

File: SD_CONNECTOR.schDoc



Rev: Author: Silvio Navarretti

