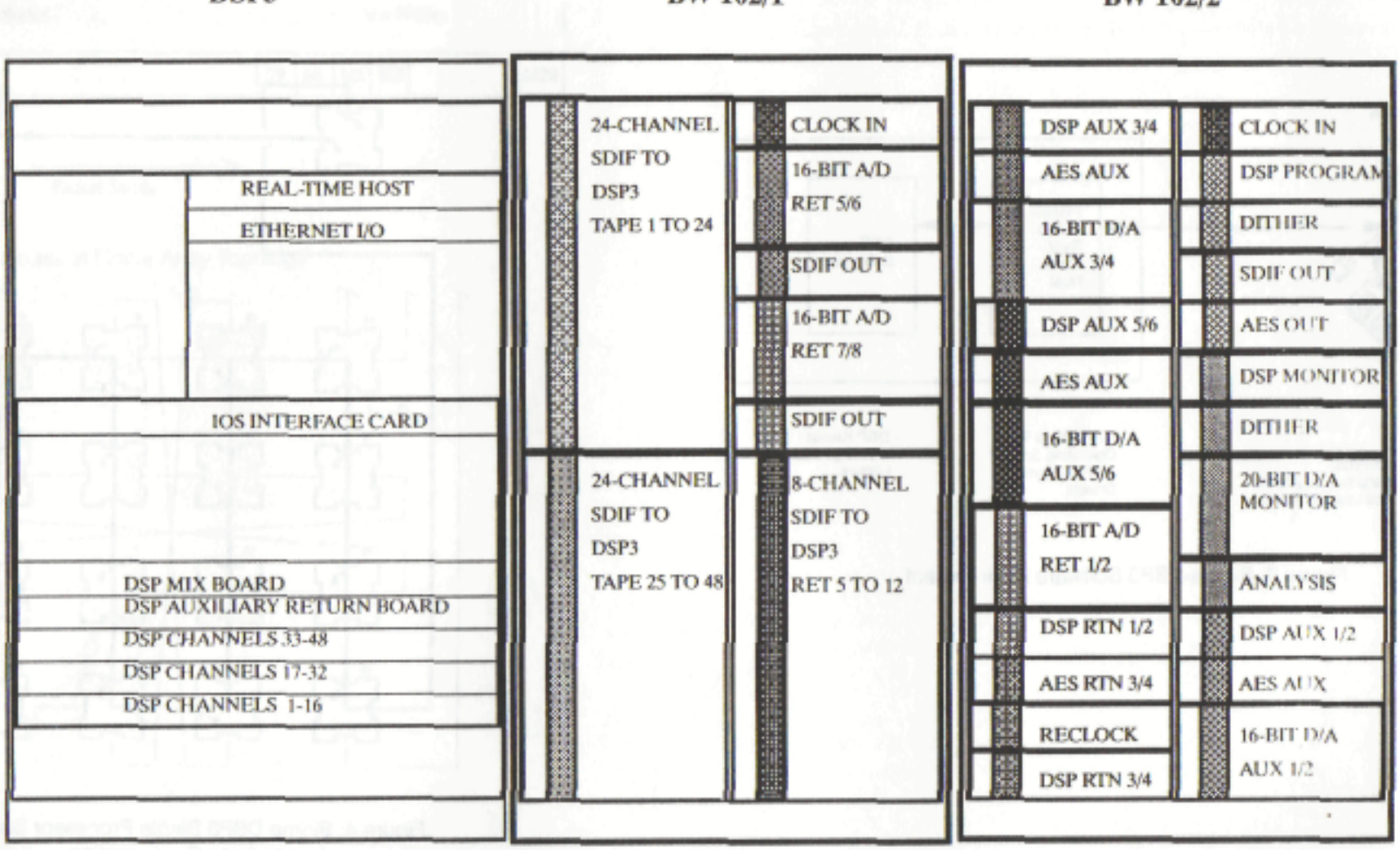

Figure 2: Sigma-DSP3 Architecture