

Figure 3: Sigma-DSP3 Software Environment

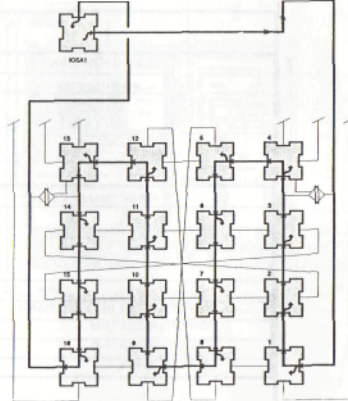


Figure 4: Sigma-DSP3 Single Processor Board Linear Array Configuration