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**ARM** 

G.726

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## G.726 Speech Codec

40, 32, 24, 16 KBIT/S ADAPTIVE DIFFERENTIAL PULSE CODE MODULATION (ADPCM)

## FEATURES

- Functions are C-callable
- Multiple channel operation
- Capable of in-band synchronization.
- Can be integrated with echo cancellers, and tone detection/regeneration.
- The encoder and decoder meet all ITU G.726 compliance data files.

Specifications Availability Description SPECIFICATIONS ARM® DEVICES **ARM Cortex-A** CPU UTILIZATION Function MIPS (Peak) Encode 6.5 Decode 7.1

Contact Sales for memory requirements @ 610-825-0182 x120

## FUNCTION

G726\_ADT\_config(. . .) Configure G.726

G726\_ADT\_init(. . .) Initializes a single channel.

G726\_ADT\_encode(. . .) Executes the G.726 encoder

G726\_ADT\_decode(. . .) Executes the G.726 decoder

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