

Mark L. Chang

CONTACT INFORMATION

Franklin W. Olin College of Engineering
1000 Olin Way
Needham, MA 02492
USA

Mobile: 781.559.0565
Fax: 781.292.2508
Email: mark.chang@olin.edu
Web: <http://markchang.net>

EDUCATION

Ph.D. in Electrical Engineering, University of Washington, Seattle, WA, 2004.

Thesis: *Variable Precision Analysis for FPGA Synthesis*
Adviser: Scott Hauck

M.S., Electrical and Computer Engineering, Northwestern University, Evanston, IL, 2000.

Thesis: *Adaptive Computing in NASA Multi-Spectral Image Processing*
Adviser: Scott Hauck

B.S. with University and Departmental Honors, Electrical and Computer Engineering, The Johns Hopkins University, Baltimore, MD, 1997.

RESEARCH INTERESTS

Mobile, social, and ubiquitous computing; engineering education; design and student motivation; reconfigurable computing

EMPLOYMENT

Advisor	Boston Startup School
Boston, MA	03/2012 - Present
Designed academic program and curriculum, recruited instructors, and taught in the inaugural class of Boston Startup School. As an advisor, responsible for setting strategic vision for future programs.	

Creative Technologist	The Boston Globe / The New York Times
Boston, MA	10/2011 - Present
Research and development of next generation news production and consumption technologies at The Globe Lab.	

Associate Professor	Franklin W. Olin College of Engineering
Needham, MA	08/2011 - Present
Electrical and Computer Engineering faculty member.	

Resident Scholar	Franklin W. Olin College of Engineering
Needham, MA	08/2005 - Present
Living on campus as an academic resource for students at Olin College. Responsible for academic advising and intellectually stimulating activities in the residence halls.	

Assistant Professor	Franklin W. Olin College of Engineering
Needham, MA	08/2004 - 08/2011
Electrical and Computer Engineering faculty member.	

Graduate Research Assistant	University of Washington
Seattle, WA	07/2000 - 07/2004
Developed variable precision design tools for FPGAs.	

Software Developer
Seattle, WA
Quicksilver Technologies, Inc.
07/2001 - 10/2001
Assisted design and development of software development tools for Quicksilver's reconfigurable hardware.

Graduate Research Assistant
Evanston, IL
Northwestern University
09/1997 - 06/2000
Developed FPGA implementations of NASA image processing applications.

Customer service operator
Iowa City, IA
National Computer Systems
06/1997 - 08/1997
Phone operator for the Department of Education.

Undergraduate Research Assistant
Baltimore, MD
Johns Hopkins University
10/1994 - 06/1997
Worked on a portable high performance linear algebra library. Investigated the IEEE-1394 draft standard in conjunction with the JHU Applied Physics Laboratory for a 1394-based spacecraft bus design.

Assistant System Administrator
Baltimore, MD
Johns Hopkins University
03/1995 - 06/1997
Maintained a network of servers and workstations for the Center for Language and Speech Processing.

Webmaster
Baltimore, MD
Johns Hopkins University
05/1995 - 01/1997
Designed and maintained a web site for the Maryland Space Grant Consortium.

Embedded Software Developer
Graz, Austria
Anton-Paar, GmbH
06/1996 - 07/1996
Participated in a cooperative internship with the Technical University of Graz, Austria. Developed embedded software for use in concentration determination instruments.

Programmer and technician
Iowa City, IA
Products Unlimited, Corp.
Summer 1989 - 1994
Set up and maintained a network of PCs for a small engineering office. Developed computer-aided testing facilities using IEEE-488 instruments and hardware.

PUBLICATIONS

Orit Shaer, Marina Umaschi Bers, Mark L. Chang, "Making the Invisible Tangible: Learning Biological Engineering in Kindergarten", *Proceedings of the 2nd Workshop on UI Technologies and Their Impact on Educational Pedagogy*, May 2011.

Ilari Shafer, Mark L. Chang, "Movement Detection for Power-Efficient Smartphone WLAN Localization", *13th ACM International Conference on Modeling, Analysis and Simulation of Wireless and Mobile Systems*, October 2010.

Andrew Barry, Noah Tye, Mark L. Chang, "Interactionless Calendar-Based Training for 802.11 Localization," *The 7th IEEE International Conference on Mobile Ad-hoc and Sensor Systems*, November 2010.

Mark L. Chang, "Work in Progress: synthesizing design, engineering, and entrepreneurship through a course in mobile application development", *Frontiers in Education Conference*, 2010.

Jessica Townsend, Mark L. Chang “Work in Progress: Impact of early design instruction on capstone experiences”, *Frontiers in Education Conference*, 2010.

Andrew Barry, Benjamin Fisher, Mark L. Chang, “A Long-Duration Study of User-Trained 802.11 Localization,” *Proceedings of the Second ACM International Workshop on Mobile Entity Localization and Tracking in GPS-less Environments*, September 2009. *Awarded best paper and best presentation.*

Stephen Longfield, Jr., Mark L. Chang, “A Parameterized Stereo Vision Core for FPGAs”, *IEEE Symposium on Field-Programmable Custom Computing Machines*, April 2009.

Mark L. Chang, Allen Downey, “A Semi-Automatic Approach for Project Assignment in a Capstone Course”, *Proceedings of the American Society for Engineering Education Annual Conference*, June, 2008.

Mark L. Chang, Jessica Townsend, “A Blank Slate: Creating a New Senior Engineering Capstone Experience”, *Proceedings of the American Society for Engineering Education Annual Conference*, June, 2008.

Mark L. Chang, “Device Architecture”, in *Reconfigurable Computing: The Theory and Practice of FPGA-Based Computation*; Scott Hauck, Andre DeHon, Editors; Morgan Kaufmann/Elsevier, 2008, pp. 3-27.

Mark L. Chang, Scott Hauck, “Précis: A Design-Time Precision Analysis Tool”, *IEEE Design and Test of Computers*, Vol. 22, No. 4, pp. 349-361, July-August 2005.

Mark L. Chang, *Variable Precision Analysis for FPGA Synthesis*, Ph.D. Dissertation, University of Washington, Department of Electrical Engineering, 2004.

Mark L. Chang, Scott Hauck, “Automated Least-Significant Bit Datapath Optimization for FPGAs”, *IEEE Symposium on Field-Programmable Custom Computing Machines*, April, 2004.

Mark L. Chang, Scott Hauck, “Variable Precision Analysis for FPGA Synthesis”, *Earth Science Technology Conference*, June, 2003.

Mark L. Chang, Scott Hauck, “Précis: A Design-Time Precision Analysis Tool”, *Earth Science Technology Conference*, June, 2002.

Mark L. Chang, Scott Hauck, “Précis: A Design-Time Precision Analysis Tool”, *IEEE Symposium on Field-Programmable Custom Computing Machines*, pp. 229–238, 2002.

Mark L. Chang, *Adaptive Computing in NASA Multi-Spectral Image Processing*, M.S. thesis, Northwestern University, Dept. of ECE, December, 1999.

Mark L. Chang, Scott Hauck, “Adaptive Computing in NASA Multi-Spectral Image Processing”, *Military and Aerospace Applications of Programmable Devices and Technologies International Conference*, 1999.

P. Banerjee, A. Choudhary, S. Hauck, N. Shenoy, C. Bachmann, M. Chang, M. Haldar, P. Joisha, A. Jones, A. Kanhare, A. Nayak, S. Periyacheri, M. Walkden, “MATCH: A MATLAB Compiler for Adaptive Computing Systems”, *Northwestern University Department of Electrical and Computer Engineering Technical Report CPDC-TR-9908-013*, 1999.

CONFERENCE POSTERS

Mihir Ravel, Mark L. Chang, Mark McDermott, Michael Morrow, Nikola Teslic, Mihajlo Katona,

Jyotsna Bapat, “A Cross-Curriculum Open Design Platform Approach to Electronic and Computing Systems Education,” *IEEE International Conference on Microelectronic Systems Education*, July 2009.

C. Murphy, D. Lindquist, A.M. Rynning, T. Cecil, S. Leavitt, M.L. Chang, “Low-Cost Stereo Vision on an FPGA”, IEEE Symposium on Field-Programmable Custom Computing Machines, 2007.

Mark L. Chang, Scott Hauck, “Least-Significant Bit Optimization Techniques for FPGAs”, *ACM/SIGDA International Symposium on Field-Programmable Gate Arrays*, February, 2004.

PANELS AND WORKSHOPS

Hal Abelson, Mark L. Chang, Cyprien Lomas, David Wolber, “Google App Inventor for Android: Building mobile applications as a first computing experience” *Frontiers in Education Conference*, 2010.

Hal Abelson, Mark L. Chang, Eni Mustafaraj, Franklyn Turbak, “Mobile Phone Apps in CS0 Using App Inventor for Android”, *15th Annual Conference of the Northeast region of the Consortium for Computing Sciences in Colleges*, 2010.

Ellen Spertus, Mark L. Chang, Paul Gestwicki, David Wolber, “Novel Approaches to CS0 with App Inventor for Android”, *The 41st ACM Technical Symposium on Computer Science Education*, 2010.

INVITED TALKS

“Play With Others”, *World Lab Summer Institute*, University of Washington Department of Computer Science and Engineering, July 2012.

Invited speaker, Scientia Conference on Research and Innovation in Undergraduate Science and Engineering Education, Rice University, February, 2011

Mark L. Chang, “Master of motivation: engaging students with smartphones and Google Android”, *Boston-area Advanced Technological Education Connections IT Futures Forum*, May 2010.

“Spinning the World Wide Web: How the Internet Really Works”, Needham Exchange Club presentation, October 2008.

“Olin College: Accrediting an Innovative Engineering Curriculum”, Yonsei University Engineering Seminar, August 2008.

“Olin College: Rethinking Engineering Education”, Microsoft Research, June 2008

“A Beginner’s Guide to Bad Engineering Presentations”, University of Hartford, November 2007.

“Spinning the World Wide Web: How the Internet Really Works”, Olin College Lecture Series, Needham Adult Education Program, October 2007.

GRANTS

MIT Lincoln Laboratory, “Active Crowdsourcing in Support of Disaster Response”.

Olin Innovation Grant funding for “Network Hacking and Cyber Security” course development.

Wellesley Tanner conference grant for work on extending the reach of the 10th anniversary Wellesley Tanner conference.

DONATIONS

Altera Corp., donation of FPGA hardware and software (2006-2008).

AndroidCentral.com, financial support for Mobile Application Development Course (2009).

Applications Technology, Inc., financial support for Mobile Application Development Course (2009).

CommonsWare, textbook for Mobile Application Development Course (2009, 2010).

Google, support for AppInventor curriculum development (2010).

Google, hardware support for Mobile Application Development course (2011).

Hewlett-Packard, Inc., donation of workstations for VLSI teaching laboratory (2005).

Microsoft, hardware and software for Mobile Application Development Course (2009).

Nokia Research Center, donation of handheld computing hardware (2008).

Palm, Inc., donation of textbook for all students in Mobile Application Development Course (2009).

Xilinx, Inc., donation of FPGA hardware and software (2004-present).

PROFESSIONAL ACTIVITIES

Program Committee Member, Publicity Chair, IEEE Conference on Field-Programmable Custom Computing Machines, 2010.

General co-chair, IEEE Workshop on Mobile Entity Localization and Tracking. Co-located with *The 7th IEEE International Conference on Mobile Ad-hoc and Sensor Systems*, November 2010.

Program Committee Member, IEEE Microelectronic Systems Education Conference, 2005, 2007, 2009, 2011.

Program Committee Member, IEEE International Conference on Field-Programmable Technology, 2007, 2008, 2009, 2010.

Program Committee Member, IEEE International Conference on Field Programmable Logic and Applications, 2005, 2006, 2007, 2008, 2009.

Program Committee Member, International Symposium on Applied Reconfigurable Computing, 2008, 2009, 2010, 2011.

Reviewer, IEE Proceedings of Computers & Digital Techniques, IEEE Transactions on Computers, IEEE Transactions on Education, IEEE Transactions on VLSI Systems, IEEE Transactions on Computer-Aided Design of Integrated Circuits & Systems, IEEE Transactions on Instrumentation & Measurement, ACM Transactions on Design Automation of Electronic Systems, IEEE International Symposium on Circuits and Systems, EURASIP Journal of Embedded Systems, Journal of Real-Time Image Processing, ACM Transactions on Reconfigurable Technology and Systems, Hindawi International Journal of Reconfigurable Computing, ACM International Conference on Interactive Tabletops and Surfaces, ACM Symposium on User Interface Software and Technology.

CONSULTING

MassChallenge

07/2012 - present

Mentor for various startups in the 2012 MassChallenge startup accelerator.

Roundware 12/2011 - present
Integration of indoor localization technologies into an open source distributed, participatory, location-aware audio platform for art installations.

The MITRE Corporation 05/2011 - 09/2011
Research investigating next-generation distributed design and collaboration tools and evaluation of mobile development frameworks.

SAIC (formerly Applications Technology, Inc.) 06/2009-present
Research supporting machine language translation software and systems.

The MITRE Corporation 08/2007
Researcher investigating 3D virtual worlds for collaboration.

NetFrameworks, Inc. & Applied Minds, Inc. 07/2001 - 09/2001
Primary software developer for proprietary groupware system.

Hunter Benefits Consulting Group 09/2000
Lead software developer.

HumaniTree.com, LLC 12/1998 - 03/1999
Web developer and Java programmer.

COMMITTEES AND DEPARTMENT SERVICE

Academic Calendaring committee, Spring 2010 - present.
Ad hoc committee on curricular innovation, Fall 2009.
Committee on Diversity and the Academic Experience, 2005 - 2007.
Electrical and Computer Engineering Faculty Search committee, 2004, 2005, 2007.
Electrical and Computer Engineering Program Group, 2004 - present.
Entrepreneurship Strategic Vision committee, Fall 2012.
Faculty / IT committee, 2004 - 2007.
Honor Board faculty representative, 2004 - 2009.
Intercollegiate Relations Committee, 2007 - present (chair 2007 - present).
Olin Certificate Program in Engineering Studies coordinator, Fall 2008 - present.
Olin College faculty liaison to Babson College and Wellesley College, Spring 2010 - present.
SCOPE Director search, Fall 2009.
Task force on the 2nd and 3rd year curriculum, 2007 - 2008 (chair).
Wellesley Olin Working Group Committee, 2004 - 2007.

TEACHING

Franklin W. Olin College of Engineering, Needham, MA

ENGR 2210: Principles of Engineering, Fall 2010.
ENGR 2250: User-Oriented Collaborative Design, Spring 2011.
ENGR 3410: Computer Architecture, Fall 2004 - present.
ENGR 3430: Digital VLSI Design, Spring 2005, 2006, 2007.
ENGR 3426: Mixed Analog-Digital VLSI I, Fall 2007 - present
ENGR 3427: Mixed Analog-Digital VLSI II, Spring 2008 - present.
ENGR 3499a: Special Topics in Electrical and Computer Engineering, Embedded Systems Design, Spring 2007, 2008.
ENGR 3499a: Principles of Intelligent Systems Engineering, Spring 2009.

ENGR 3499a: Mobile Application Development, Spring 2009, 2010, 2011.
ENGR 3499b: Web Application Development, Spring 2011.
ENGR 4190: Senior Consulting Program for Engineering, 2005 (John Deere and Motorola), 2006 (IBM Research), 2007 (MITRE and Nortel), 2008 (MITRE), 2009 (Microsoft FUSE Labs and Linden Lab), 2010 (Autodesk and Lexmark)
Olin Works Co-Curricular, Fall 2005, Spring 2006.
Green Engineering Co-Curricular, Spring 2005.
Physical Security Systems Co-Curricular, Fall 2008.
Social Justice Reading Group, 2005 - 2009.

Yonsei University, Seoul, Korea

Principles of Engineering, Yonsei University International Summer School, Summer 2008.

University of Washington, Seattle, WA

EE 471: Computer Design and Organization. Instructor, Winter 2003.
Overall class evaluation rating 4.13/5.0.
(<http://www.washington.edu/cec/e/EE471A4003.html>).

Northwestern University, Evanston, IL

B01: Introduction to Digital Logic Design. Instructor, Summer 1999.
Overall class evaluation rating 5.56/6.0.
C91: VLSI Systems Design. Teaching Assistant, Winter 1999.
C92: VLSI Systems Design Projects. Teaching Assistant, Spring 1998.

AWARDS

Intel Corporation: 2002-2003 Intel Foundation Graduate Fellowship

University of Washington: Outstanding Graduate Research Assistant (2002), Nominated for the Yang Research Award (2002)

Northwestern University: Royal E. Cabell Fellowship (1997), ECE Department Best Teaching Assistant Honorable Mention (1998)

Johns Hopkins University: IEEE student chapter president (1996), Eta Kappa Nu chapter president (1996, 1997), Tau Beta Pi, Dean's List, Electrical and Computer Engineering Chair Award

National Merit Scholar, National Computer Systems Merit Scholarship