root
: Root
system
: System
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sys_port_proxy
: RubyPortProxy
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ruby
: RubySystem
tcc_cntrl0
: TCCCntrl
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: MessageBuffer
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requestFromTCP
: MessageBuffer
in_port
probeFromNB
: MessageBuffer
in_port
requestToNB
: MessageBuffer
out_port
responseToCore
: MessageBuffer
out_port
unblockToNB
: MessageBuffer
out_port
responseToNB
: MessageBuffer
out_port
sqc_cntrl0
: SQCCntrl
responseToSQC
: MessageBuffer
in_port
probeToSQC
: MessageBuffer
in_port
requestFromSQC
: MessageBuffer
out_port
sequencer
: VIPERSequencer
in_ports
scalar_cntrl0
: SQCCntrl
responseToSQC
: MessageBuffer
in_port
probeToSQC
: MessageBuffer
in_port
requestFromSQC
: MessageBuffer
out_port
sequencer
: VIPERSequencer
in_ports
tcp_cntrl2
: TCPCntrl
responseToTCP
: MessageBuffer
in_port
probeToTCP
: MessageBuffer
in_port
unblockFromCore
: MessageBuffer
out_port
responseFromTCP
: MessageBuffer
out_port
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out_port
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: VIPERCoalescer
gmTokenPort
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tcp_cntrl3
: TCPCntrl
responseToTCP
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in_port
probeToTCP
: MessageBuffer
in_port
unblockFromCore
: MessageBuffer
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coalescer
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responseToTCP
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: MessageBuffer
in_port
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: MessageBuffer
in_port
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: SimpleNetwork
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: RubySequencer
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: CPCntrl
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in_port
probeToCore
: MessageBuffer
in_port
requestFromCore
: MessageBuffer
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: MessageBuffer
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: MessageBuffer
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sequencer1
: RubySequencer
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mem_request_port
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: RubySequencer
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: DirCntrl
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unblockFromCores
: MessageBuffer
in_port
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: MessageBuffer
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: MessageBuffer
in_port
requestFromDMA
: MessageBuffer
in_port
responseToCore
: MessageBuffer
out_port
probeToCore
: MessageBuffer
out_port
responseToDMA
: MessageBuffer
out_port
dma_cntrl0
: GPU_VIPER_DMA_Controller
responseFromDir
: MessageBuffer
in_port
requestToDir
: MessageBuffer
out_port
dma_sequencer
: DMASequencer
in_ports
dma_cntrl1
: GPU_VIPER_DMA_Controller
responseFromDir
: MessageBuffer
in_port
requestToDir
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dma_sequencer
: DMASequencer
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piobus
: IOXBar
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mem_side_ports
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: MemCtrl
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: X86TimingSimpleCPU
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: X86LocalApic
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pio
int_responder
cpu2
: X86TimingSimpleCPU
icache_port
dcache_port
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: X86LocalApic
int_requestor
pio
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cpu0
: X86TimingSimpleCPU
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dcache_port
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: Process
drivers0
: GPUComputeDriver
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: X86GPUTLB
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mem_side_ports
l2_tlb
: X86GPUTLB
mem_side_ports
cpu_side_ports
sqc_tlb
: X86GPUTLB
mem_side_ports
cpu_side_ports
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mem_side_ports
cpu_side_ports
l1_tlb2
: X86GPUTLB
mem_side_ports
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: X86GPUTLB
mem_side_ports
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l1_tlb0
: X86GPUTLB
mem_side_ports
cpu_side_ports
l1_tlb1
: X86GPUTLB
mem_side_ports
cpu_side_ports
scalar_coalescer
: TLBCoalescer
mem_side_ports
cpu_side_ports
l1_coalescer2
: TLBCoalescer
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l1_coalescer3
: TLBCoalescer
mem_side_ports
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l1_coalescer0
: TLBCoalescer
mem_side_ports
cpu_side_ports
l1_coalescer1
: TLBCoalescer
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sqc_coalescer
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: Shader
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: ComputeUnit
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sqc_port
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scalar_tlb_port
translation_port
sqc_tlb_port
localDataStore
: LdsState
cuPort
ldsBus
: Bridge
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CUs3
: ComputeUnit
ldsPort
sqc_port
scalar_port
gmTokenPort
memory_port
scalar_tlb_port
translation_port
sqc_tlb_port
localDataStore
: LdsState
cuPort
ldsBus
: Bridge
cpu_side_port
mem_side_port
CUs0
: ComputeUnit
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scalar_port
gmTokenPort
memory_port
scalar_tlb_port
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sqc_tlb_port
localDataStore
: LdsState
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CUs1
: ComputeUnit
ldsPort
sqc_port
scalar_port
gmTokenPort
memory_port
scalar_tlb_port
translation_port
sqc_tlb_port
localDataStore
: LdsState
cuPort
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: Bridge
cpu_side_port
mem_side_port