

3.2

What is 5ED4-07A4 when those values represent signed 16-bit hexadecimal numbers stored in sign-magnitude format? The result should be written in hexadecimal.

$$\begin{array}{r} 5ED4 \\ - 07A4 \\ \hline 5730_{16} \end{array}$$

3.7

Assume 185 and 122 are signed 8-bit decimal integers stored in sign-magnitude format. Calculate 185+122. Is there overflow, underflow, or neither.

$$\begin{array}{r} 185 \\ - 122 \\ \hline 63_{10} \end{array}$$

Since both are signed 8-bit decimal integers, there is no under or overflow.

3.20

What decimal number does the bit pattern 0x0C000000 represent if it is a two's complement integer? An unsigned integer?

Two's complement and unsigned have the same value of 201,326,592 since the signed bit is 0.

3.21

If the bit pattern 0x0C000000 is placed into the Instruction Register, what MIPS instruction will be executed?

$$\begin{array}{ll} 0000 \ 11 & 00 \ 0000 \ 0000 \ 0000 \ 0000 \ 0000 \\ \text{Operation} = \text{jal} & \text{target} = 0 \end{array}$$

3.23

Write down the binary representation of the decimal number 63.25 assuming the IEEE 754 single precision format.

$$\begin{array}{l} 253 \times 2^{-2} = 1.1111101 \times 2^5 \\ 0 \ 10000100 \ 111110100000000000000000 \end{array}$$

3.41

Using the IEEE 754 floating point format, write down the bit pattern that would represent -1/4. Can you represent -1/4 exactly?

$$\begin{array}{l} -1.00 \times 2^{-2} \\ 1 \ 10000100 \ 100000000000000000000000 \end{array}$$