winter_25_Quiz_5 - Results   ✕

Attempt 1 of 1

Written Feb 25, 2025 7:52 PM - Feb 25, 2025 8:04 PM

You successfully submitted your quiz.

Attempt Score	**6 / 19 - 31.58 %**
Overall Grade (Highest Attempt)	**6 / 19 - 31.58 %**

Question 1

In parallel data transmission several bits at a time are transmitted e.g. USB

✗ ⚪ True

➡ ⚪ False

Question 2

Only one master device can exist on a master-slave bus, while multiple slave devices can be connected to it.

✔ ⚪ True

⚪ False

Question 3

A bus is a common electrical pathway between multiple devices

✔ ⚪ True

⚪ False

Question 4

The internal components of the processor are connected by _____.

- ○ PCI-E , PCI-X, or SCSI
- ○ Memory bus
- ✓○ Internal bus
- ○ External bus

Question 5

A chip with 16 data pins can read or write an 64-bit word in ___1___ ✗ **(4)** operation(s).

Question 6

4 address pins can address ___128___ ✗ **(16)** memory locations,

Question 7

"A signal is asserted" means:

- ○ it goes high and goes low
- ➡ ○ it goes high or goes low
- ✗ ○ it goes high
- ○ it goes low

Question 8

Some devices that attach to a bus are passive and can initiate bus transfers.

- ✗ ○ True
- ➡ ○ False

Question 9

A processor can support a maximum memory of 4GB, where the memory is word addressable (a word consists of two bytes). How many bits are required

at a minimum to address the main memory? Explain your answer.

If a word is 2 bytes it equals 16 bits, shifting 1 digit for the address would require a minimum of 17 bits to address the main memory

The correct answer is not displayed for Written Response type questions.

▼ Hide question 9 feedback

Feedback

$2^{32}/2 = 2^{31}$
31 bits

Done