- Attach screenshots of both your circuit and chip (8-bits ALU) to this file.
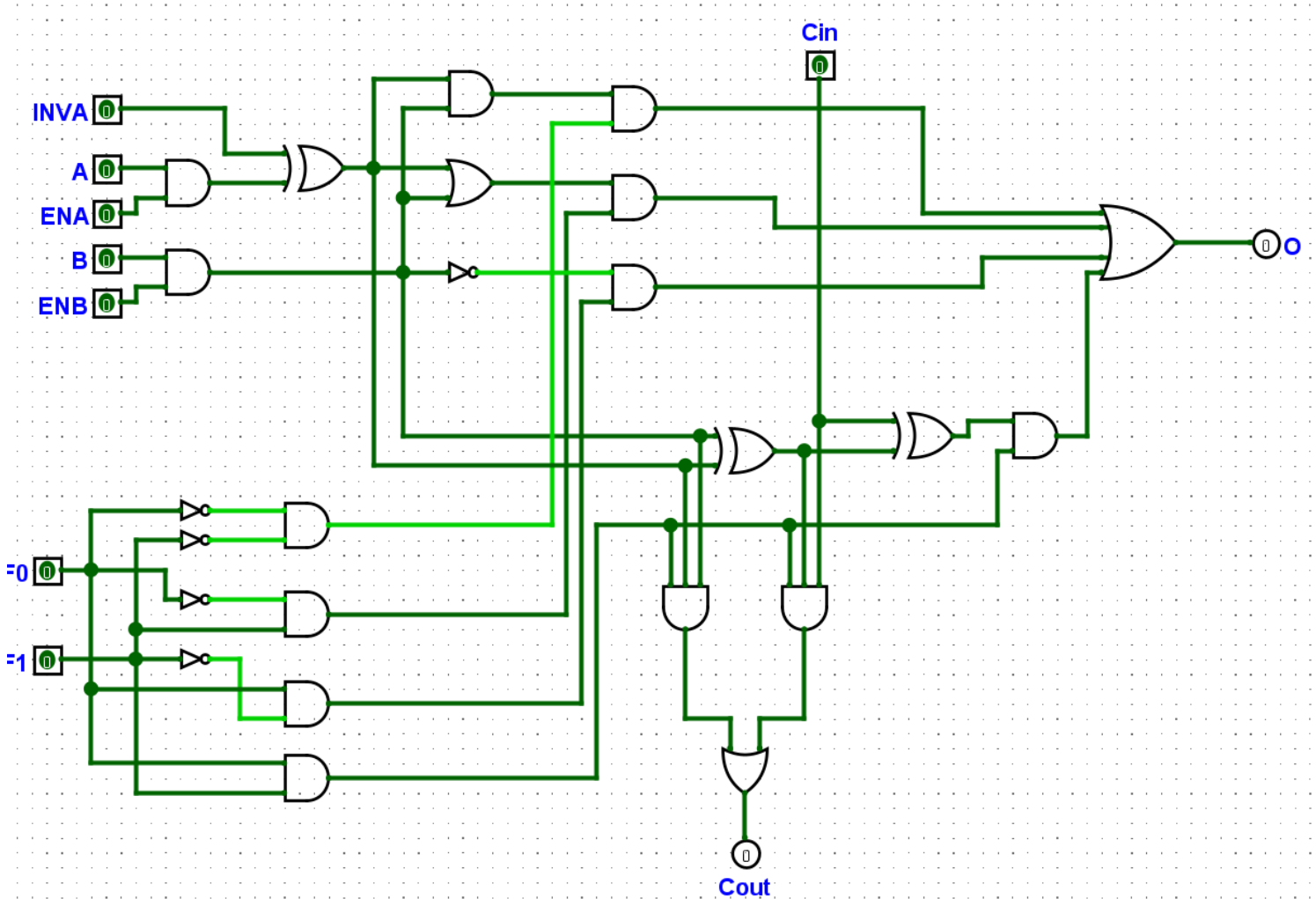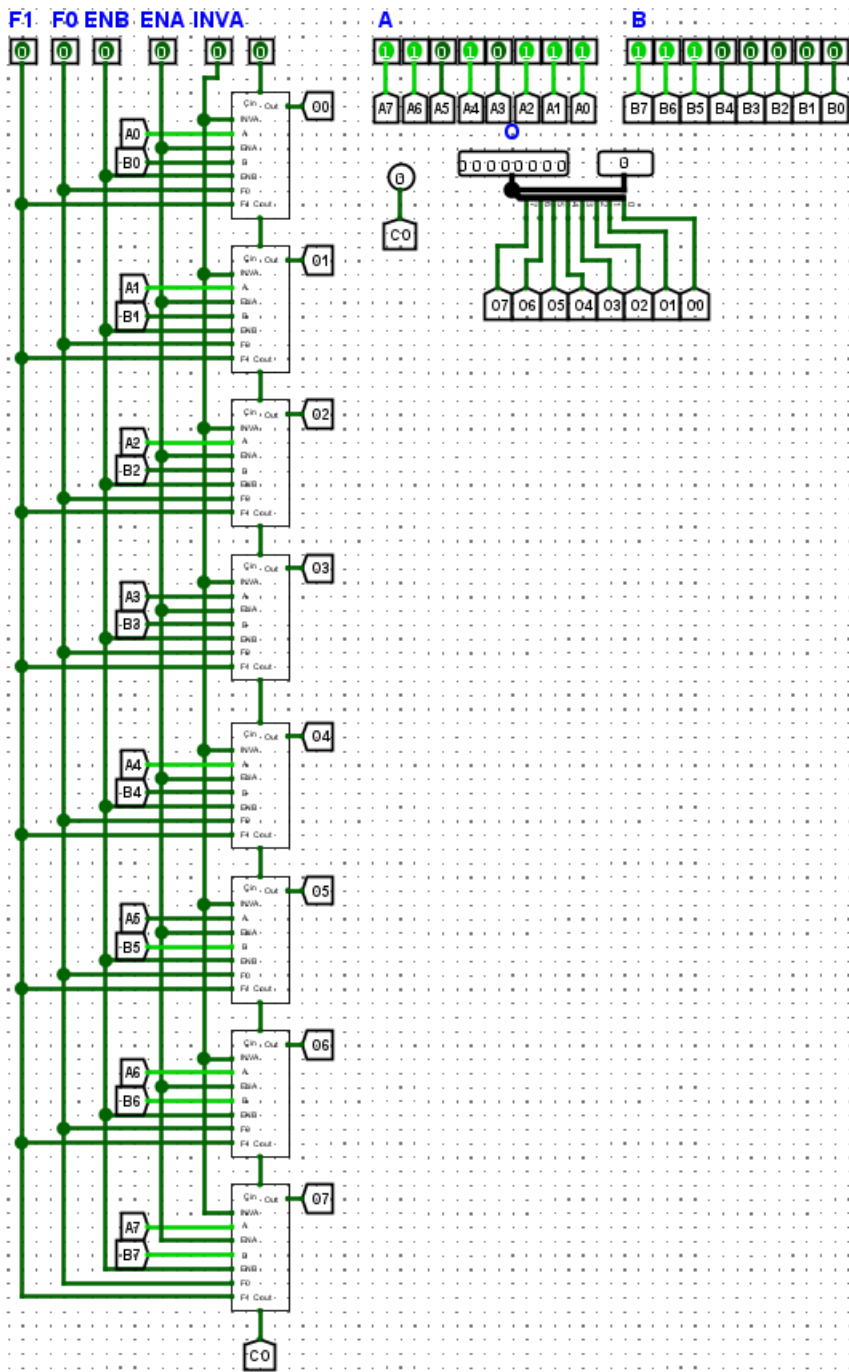
- Attach a screenshot of your ALU test table.

	F0	F1	ENA	ENB	INVA	INC	Res(Binary)	Res(decimal)
1	0	1	1	0	0	0	1101 0111	-41
2	0	1	0	1	0	0	1110 0000	-32
3	0	1	1	0	1	0	0010 1000	40
4	1	0	1	1	0	0	0001 1111	31
5	1	1	1	1	0	0	1011 0111	-73
6	1	1	1	1	0	1	1011 1000	-72
7	1	1	1	0	0	1	1101 1000	-40
8	1	1	0	1	0	1	1110 0001	-31
9	1	1	1	1	1	1	0000 1001	9
10	1	1	0	1	1	0	1101 1111	-33
11	1	1	1	0	1	1	0010 1001	41
12	0	0	1	1	0	0	1100 0000	-64
13	0	1	1	1	0	0	1111 0111	-9
14	0	1	0	0	0	0	0000 0000	0
15	1	1	0	0	0	1	0000 0001	1
16	1	1	0	0	1	0	1111 1111	-1

- Submit your updated Logisim file (cpu.cir) and this file.