# Refinement for Symbolic Trajectory Evaluation

#### Authors

#### Chalmers

**Abstract.** Model refinement such that it preserves symbolic trajectory evalutions.

**Keywords:** STE · Refinement · ?

## 1 Introduction to STE

## 1.1 Original STE

Symbolic trajectory evaluation [5] (STE) is a high-performance model checking technique based on symbolic simulation extended with a temporal next-time operator to describe circuit behaviour over time. In its simplest form, STE tests the validity of an assertion of the form  $A \Rightarrow C$ , where both the antecedent A and consequent C are formulas in the following logic:

$$f ::= p \mid f \land f \mid P \rightarrow f \mid \mathbf{N} f$$

Here, p is a simple predicate over "values" in a circuit and P is a Boolean propositional formula, and the operators  $\land$ ,  $\rightarrow$  and  $\mathbf{N}$  are conjunction, domain restriction and the next-time operator, respectively.

If the circuit contains Boolean signals, p is typically drawn from the following two predicates: n is 1 and n is 0, where n ranges over the signals (or nodes) in a circuit. For example, suppose we have a unit-delayed, two-input AND-gate, then it is reasonable to assume that the assertion  $(in_1$  is  $1 \wedge in_2$  is  $1) \Rightarrow \mathbf{N}(out$  is 1) is true. Indeed, STE efficiently validates such statements for us.

While the truth semantics of an assertion in STE is defined as the satisfaction of its "defining" trajectory (bounded sequence of states) relative to a model structure of the circuit, what the STE algorithm computes is exactly the solution of a data-flow equation [1] in the classic format [4]. . . .

#### 1.2 Set-theoretic STE

Consider an arbitrary, but fixed, digital circuit M operating in discrete time. A configuration of M, denoted by C, is non-empty and finite set that represents a snapshot of M at a discrete point in time. If the circuit M has m boolean signals, then its set of configurations is typically represented as a sequence  $\mathbb{B}^m$ , where  $\mathbb{B} = \{0,1\}$  is the set of boolean values.

Circuit Model A simple conceptual model of M is a transition relation,  $M_R \subseteq C \times C$ , where  $(c, c') \in M_R$  means that M can move from c to c' in one step<sup>1</sup>. The power set of C, denoted by  $\mathcal{P}(C)$ , can be viewed as a the set of predicates on configurations, where  $\cap$ ,  $\cup$ , and  $\subseteq$  correspond to conjunction, disjunction and implication, respectively. We denote by  $\cap Q$  and  $\cup Q$  the intersection and union of all members of any  $Q \subseteq \mathcal{P}(C)$ .

 $M_R$  induces a predicate transformer  $M_F \in \mathcal{P}(C) \to \mathcal{P}(C)$  using the relational image operation:

$$M_F(p) = \{c' \in C \mid \exists c \in p : (c, c') \in M_R\}$$

It is intuitively obvious that if M is in one of the configurations in  $p \in \mathcal{P}(C)$ , then in one time step it must be in one of the configurations in  $M_F(p)$ . We also see that  $M_F$  distributes over arbitrary unions:

$$M_F(\cup Q) = \cup \{M_F(q) \mid q \in Q\}$$

for all  $Q \subseteq \mathcal{P}(C)$ . In general, any  $M_F$  that satisfies this distributive property also defines a  $M_R$  through the equivalence  $(c, c') \in M_R \Leftrightarrow c' \in M_F(\{c\})$ , that is to say, there is no loss of information going from  $M_R$  to  $M_F$  or vice versa. We adopt this functional model of M and drop its subscript.

Exactly what C and its signals are, is not important in this section. In practice, however, signals are typically divided into external, i.e. "input" and "output", and internal parts. While an input signal is generally controlled by the external environment, and thus unconstrained by M itself, non-input signals are determined by the circuit topology and functionality. For example, supposed M is the earlier example of a unit-delayed two-input AND gate, we could then define its model  $M \in \mathcal{P}(\mathbb{B}^3) \to \mathcal{P}(\mathbb{B}^3)$  as follows:

$$M(p) = \{ \langle b_1, b_2, i_1 \wedge i_2 \rangle \in \mathbb{B}^3 \mid \langle i_1, i_2, o \rangle \in p \}$$

Here  $i_1$  and  $i_2$  refer to the two inputs of the AND gate, o the ignored output, and  $b_1$  and  $b_2$  are unconstrained inputs for the new configurations.

Assertions and satisfaction A trajectory assertion for M is quintuple  $A = (S, s_0, R, \pi_a, \pi_c)$ , where S is a finite set of states,  $s_0 \in S$  is an initial state,  $R \subseteq S \times S$  is a transition relation,  $\pi_a \in S \to \mathcal{P}(C)$  and  $\pi_c \in S \to \mathcal{P}(C)$  label each state s with an antecedent  $\pi_a(s)$  and a consequent  $\pi_c(s)$ . We assume that  $(s, s_0) \notin S$  for all  $s \in S$  without any loss of generality.

The circuit model M intuitively satisfies an assertion A if, for every trajectory  $\tau$  through M and every  $run\ \rho$  through A,  $\tau$  satisfying the antecedents of  $\rho$  entails that  $\tau$  also satisfies the consequents of  $\rho$ . To be more specific, a trajectory of M is a non-empty sequences of configurations,  $\tau \in C^+$ , such that  $\tau_n \in M(\{\tau_{n-1}\})$  for all  $n \in \mathbb{N} : 0 < n < |\tau|$ . And a run of A is a non-empty sequence of states,  $\rho \in S^+$ , such that  $\rho_0 = s_0$  and  $(\rho_{n-1}, \rho_n) \in R$  for all  $n \in \mathbb{N} : 0 < n < |\rho|$ .

<sup>&</sup>lt;sup>1</sup> Mention how this affects circuits with zero-delays?

A  $\tau$  satisfies the antecedents of  $\rho$ , denoted by  $\tau \models_a \rho$ , iff  $\tau_n \in \pi_a(\rho_n)$  for all  $n \in \mathbb{N} : n < |\tau| = |\rho|$ ; satisfaction of consequents is defined similarly with  $\pi_c$  and denoted by  $\tau \models_c \rho$ . That M satisfies A, denoted by  $M \models A$ , can then formalized<sup>2</sup>as follows:

$$\forall \tau \in Traj(M) : \forall \rho \in Runs(A) : |\tau| = |\rho| \Rightarrow (\tau \models_a \rho \Rightarrow \tau \models_c \rho)$$

where Traj(M) and Runs(A) denote the sets of all trajectories of M and runs of A, respectively.

## 1.3 Lattice-theoretic STE

Manipulating subsets of  $\mathbb{B}^m$  is impractical for even moderately large m, which leads us to one of the key insights of STE. Namely, instead of manipulating subsets of  $\mathbb{B}^m$  directly, one can use sequences of ternary values  $\mathbb{T} = \mathbb{B} \cup \{X\}$  to approximate them, whose sizes are only linear in m. Here the 1 and 0 from  $\mathbb{B}$  denotes specific, defined values whereas X denotes an "unknown" value that could be either 1 or 0. This intuition induces a partial order  $\sqsubseteq$  on  $\mathbb{T}$ , where  $0 \sqsubseteq X$  and  $1 \sqsubseteq X^3$ . For any  $m \in \mathbb{N}$ , this ordering on  $\mathbb{T}$  is lifted component-wise to  $\mathbb{T}^m$ .

Note that  $\mathbb{T}^m$  does not quite form a complete lattice because it lacks a bottom: both  $0 \sqsubseteq X$  and  $1 \sqsubseteq X$  but 0 and 1 are equally defined. A special bottom element  $\bot$  is therefore introduced, such that  $\bot \sqsubseteq t$  and  $\bot \neq t$  for all  $t \in \mathbb{T}^m$ . The extended  $\mathbb{T}^m_\bot = \mathbb{T}^m \cup \{\bot\}$  then becomes a complete lattice. We denote the top element  $\langle X, \ldots, X \rangle$  of  $\mathbb{T}^m_\bot$  by  $\top$ .

**Ternary lattices** Generalising from any specific domain, let  $(\hat{P}, \sqsubseteq)$  be a finite, complete lattice of *abstract predicates* in which the meet  $\sqcap$  and join  $\sqcup$  of any subset  $Q \subseteq \hat{P}$  exists. Similar to the previous set operations for power sets,  $\sqcap$ ,  $\sqcup$  and  $\sqsubseteq$  correspond to conjunction, disjunction and implication for abstract predicates, respectively. Furthermore, for any  $Q \subseteq \hat{P}$ , we denote by  $\sqcap Q$  and  $\sqcup Q$  the meet and join of all members of Q.

Let there be a Galois connection relating "concrete" predicates  $\mathcal{P}(C)$  and abstract predicates  $\hat{P}$ . The usual definition of a Galois connection is in terms of an abstraction  $\alpha \in \mathcal{P}(C) \to \hat{P}$  and a concretisation  $\gamma \in \hat{P} \to \mathcal{P}(C)$  function, such that  $\alpha(p) \sqsubseteq \hat{p} \Leftrightarrow p \subseteq \gamma(\hat{p})$  for all  $p \in \mathcal{P}(C)$  and  $\hat{p} \in \hat{P}$ . For example, a Galois connection from  $\mathcal{P}(\mathbb{B}^m)$  to  $\mathbb{T}^m_{\perp}$  for any  $m \in \mathbb{N}$  can be defined in a natural way by its concretisation function  $\gamma \in \mathbb{T}^m_{\perp} \to \mathcal{P}(\mathbb{B}^m)$ :

$$\gamma(\langle t_0, \dots, t_{m-1} \rangle) = \{ \langle b_0, \dots, b_{m-1} \rangle \in \mathbb{B}^m \mid \forall i < m : t_i \neq \mathbf{X} \Rightarrow b_i = t_i \}$$
$$\gamma(\bot) = \emptyset$$

<sup>&</sup>lt;sup>2</sup> This is equivalent to a DFA formulation [1].

<sup>&</sup>lt;sup>3</sup> We use the reverse ordering of what is originally used in STE.

Listing each concrete predicate approximated by a given abstract predicate. Its abstraction function  $\alpha \in \mathcal{P}(\mathbb{B}^m) \to \mathbb{T}^m_{\perp}$  instead finds the most precise abstract predicate for a set of concrete predicates:

$$\alpha(p) = \sqcup \{ \langle t_0, \dots, t_{m-1} \rangle \in \mathbb{T}^m_{\perp} \mid \langle b_0, \dots, b_{m-1} \rangle \in p, \forall i < m : b_i = t_i \}$$
  
$$\alpha(\emptyset) = \bot$$

Abstract circuit model An abstract predicate transformer  $\hat{M} \in \hat{P} \to \hat{P}$  is an abstract interpretation [2] of  $M \in \mathcal{P}(C) \to \mathcal{P}(C)$  iff:  $\hat{M}$  preserves  $\bot$ , i.e.  $\hat{M}(\bot) = \bot$ ;  $\hat{M}$  is monotonic, i.e.  $\hat{p} \sqsubseteq \hat{q} \Rightarrow \hat{M}(\hat{p}) \sqsubseteq \hat{M}(\hat{q})$  for all  $\hat{p}, \hat{q} \in \hat{P}$ ; and  $\alpha$ , or  $\gamma$ , form a simulation relation between  $\mathcal{P}(C)$  and  $\hat{P}$ , i.e.  $\alpha(M(p)) \sqsubseteq \hat{M}(\alpha(p))$  for all  $p \in \mathcal{P}(C)$ , or  $M(\gamma(\hat{p})) \subseteq \gamma(\hat{M}(\hat{p}))$  for all  $\hat{p} \in \hat{P}$ .

Unlike its concrete model,  $\hat{M}$  does not distribute over arbitrary join because information is potentially discarded by the ternary logic durin a join. As an example, let the following  $\hat{M}$  abstract the earlier unit-delayed AND gate:

$$\begin{array}{ll} \hat{M}(\langle 1,1,p_2\rangle) &= \langle \mathbf{X},\mathbf{X},1\rangle & \hat{M}(\langle 0,0,p_2\rangle) &= \langle \mathbf{X},\mathbf{X},0\rangle \\ \hat{M}(\langle 0,\mathbf{X},p_2\rangle) &= \langle \mathbf{X},\mathbf{X},0\rangle & \hat{M}(\langle \mathbf{X},0,p_2\rangle) &= \langle \mathbf{X},\mathbf{X},\mathbf{X}\rangle \\ \hat{M}(\langle p_0,p_1,p_2\rangle) &= \langle \mathbf{X},\mathbf{X},\mathbf{X}\rangle & \end{array}$$

where the last, most general matching is overlapped by the more concrete ones. If we apply  $\hat{M}$  to the join of  $\langle 0, 1, \mathbf{X} \rangle$  and  $\langle 1, 0, \mathbf{X} \rangle$ , or if we apply  $\hat{M}$  to them individually and then join, we get two different results:

$$\hat{M}(\langle 0, 1, X \rangle \sqcup \langle 1, 0, X \rangle) = \hat{M}(\langle X, X, X \rangle) = \langle X, X, X \rangle$$

$$\hat{M}(\langle 0, 1, X \rangle) \sqcup \hat{M}(\langle 1, 0, X \rangle) = \langle X, X, 0 \rangle \sqcup \langle X, X, 0 \rangle = \langle X, X, 0 \rangle$$

The inequality  $\sqcup \{\hat{M}(\hat{q}) \mid \hat{q} \in \hat{Q}\} \sqsubseteq \hat{M}(\sqcup \hat{Q})$  for all  $\hat{Q} \sqsubseteq \hat{P}$  does however hold, since it is implied by the monotonicity of  $\hat{M}$ .

Assertions and satisfaction A trajectory assertion for an abstract model  $\hat{M}$  is a quintuple  $\hat{A} = (S, s_0, R, \hat{\pi}_a, \hat{\pi}_c)$ , where  $S, s_0$ , and R are as in section 1.2 and  $\hat{\pi}_a \in S \to \hat{P}$  and  $\hat{\pi}_c \in S \to \hat{P}$  label each state s with an abstract predicate for its antecedent and consequent, respectively.

Here follows the definition in [1]. For all functions  $\hat{\Phi} \in S \to \hat{P}$  and states  $s \in S$ , define  $\hat{F} \in S \to (\hat{P} \to \hat{P})$  and  $\hat{\mathcal{F}} \in (S \to \hat{P}) \to (S \to \hat{P})$  as follows:

$$\hat{F}(s)(\hat{p}) = \hat{M}(\pi_{\mathbf{a}}(s) \sqcap \hat{p}) \tag{1}$$

$$\hat{\mathcal{F}}(\Phi)(s) = \mathbf{if} \ (s = s_0) \ \mathbf{then} \ \top \ \mathbf{else} \ \sqcup \{\hat{F}(s')(\Phi(s')) \mid (s', s) \in R\}$$
 (2)

 $\hat{F}$  preserves  $\perp$ , and both  $\hat{F}$  and  $\hat{\mathcal{F}}$  are monotonic; two  $\hat{\Phi}, \hat{\Phi}' \in S \to \hat{P}$  are ordered as  $\hat{\Phi} \sqsubseteq \hat{\Phi}' \Leftrightarrow \forall s \in S : \hat{\Phi}(s) \sqsubseteq \hat{\Phi}'(s)$ . Let  $\hat{\Phi}_* \in S \to \hat{P}$  be the least fixpoint of the equation  $\hat{\Phi} = \hat{\mathcal{F}}(\hat{\Phi})$  [3]. Since both S and  $\hat{P}$  are finite,  $\hat{\Phi}_*$  is given by  $\lim \hat{\Phi}_n(s)$ , where  $\hat{\Phi}_n$  is defined as follows:

$$\hat{\Phi}_n = \mathbf{if} \ (n=0) \ \mathbf{then} \ (\lambda s \in S : \bot) \ \mathbf{else} \ \hat{\mathcal{F}}(\hat{\Phi}_{n-1})$$
 (3)

 $\hat{M}$  satisfies a trajectory assertion<sup>4</sup> $\hat{A}$ , denoted by  $\hat{M} \models_{\text{lat}} \hat{A}$ , iff  $\hat{\Phi}_*(s) \sqcap \pi_{\alpha}(s) \sqsubseteq \pi_{\text{c}}(s)$  for all  $s \in S$ .

## 2 Refinement

#### 2.1 Set-Theoretic refinement

Consider another fixed, but arbitrary, circuit model  $N \in \mathcal{P}(D) \to \mathcal{P}(D)$ , where D is a non-empty and finite set of configurations. Further, let there be a Galois connection between predicates  $(C,\subseteq)$  and  $(D,\subseteq)$  given by means of a binary relation  $\ll \subseteq \mathcal{P}(C) \times \mathcal{P}(D)$ , where  $c \ll d$  is read as "c can be approximated by d". We derive  $\ll$  from either of the usual functions for abstraction  $\alpha \in \mathcal{P}(C) \to \mathcal{P}(D)$  and concretisation  $\gamma \in \mathcal{P}(D) \to \mathcal{P}(C)$  as follows:

$$c \ll d \Leftrightarrow \alpha(c) \subseteq d$$
  $c \ll d \Leftrightarrow c \subseteq \gamma(d)$ 

Here  $\subseteq$  on the  $\alpha$ -derivation side is the inclusion order of  $\mathcal{P}(D)$ , and on the  $\gamma$ -derivation side  $\subseteq$  is the inclusion order of  $\mathcal{P}(C)$ . Intuitively, the relation  $\ll$  acts as an extension of the orderings inside  $\mathcal{P}(C)$  and  $\mathcal{P}(D)$  to one between them.

Exactly what configurations such as C and D are, were not important previously. To reason about refinement, which relates the visible behaviour of circuits, we make a distinction between their external and internal elements. Let the visible elements of a configuration in C be identified by a function  $\pi_v \in C \to \mathcal{P}(C)$ , where  $\pi_v(c)$  denotes the configurations which are visually equal to a  $c \in C$ . Further, let  $\pi_v(\cdot)$  induce an equivalence relation  $\sim$  on configurations in C, such that  $c \sim c' \Leftrightarrow \pi_v(c) = \pi_v(c')$ ; the equivalence class of c under  $\sim$ , denoted by  $[c]_{\sim}$ , is defined as  $\{c' \in C \mid c' \sim c\}$ . With a slight abuse of notation, we extend  $\pi_v(\cdot)$  to sequences component-wise and overload it to accept configurations in D.

We now formalize an intuition of whether M refines N, denoted by  $M \leq N$ :

$$\forall \tau \in Traj(M) : \exists v \in Traj(N) : |\tau| = |v| \land \pi_{v}(\tau) \ll \pi_{v}(v)$$

where two sequences  $\langle \pi_{\mathbf{v}}(\tau_0), \ldots \rangle \ll \langle \pi_{\mathbf{v}}(v_0), \ldots \rangle$  iff  $\pi_{\mathbf{v}}(\tau_n) \ll \pi_{\mathbf{v}}(v_n)$  for all  $n \in \mathbb{N} : n < |\tau| = |v|$ . In other words,  $M \leq N$  states that, for every sequence of configurations  $\tau$  permitted by M, there must exist a sequence v for N which "covers" the visual behaviour of  $\tau$ .

Recall that a trajectory assertion for N is a quintuple  $A = (S, s_0, R, \pi_a, \pi_c)$ , where  $\pi_a \in S \to \mathcal{P}(D)$  and  $\pi_c \in S \to \mathcal{P}(D)$  label each  $s \in S$  with its antecedents and consequents, respectively. If ... of  $\pi_a$  and  $\pi_c$  is limited to ... in D, i.e.  $d \in \pi_a(s) \Rightarrow [d]_{\sim} \subseteq \pi_a(s)$  for all  $s \in S$ , then we refer to A as a visible trajectory assertion and denote it by  $A_{\text{vis}}$ . Define  $\gamma(A) = (S, s_0, R, \gamma(\pi_a), \gamma(\pi_c))$ , where

<sup>&</sup>lt;sup>4</sup> That  $\hat{M}$  satisfies  $\hat{A}$  implies that a concretisation of  $\hat{A}$  can also be satisfied by the original, set-based model M [1].

 $\gamma(\pi_{\rm a}) = \lambda s \in S : \gamma(\pi_{\rm a}(s))$  and  $\gamma(\pi_{\rm c}) = \lambda s \in S : \gamma(\pi_{\rm c}(s))$ . We can now state that refinement between M and N entails that satisfaction of any  $A_{\rm vis}$  by N implies that M satisfies  $\gamma(A_{\rm vis})$  as well:

**Theorem 1.** 
$$M \leq N \Rightarrow (N \models A_{vis} \Rightarrow M \models \gamma(A_{vis}))$$

The above definition of refinement can be equivalently formulated as a simulation relation. We say that M refines N by set-theoretic refinement, denoted by  $M \leq_{\text{set}} N$ , iff C can be approximated by D, i.e.  $C \ll D$ ;  $\ll$  is a visual approximation, i.e.  $c \ll d \Rightarrow \alpha(\pi_{\mathbf{v}}[c]) \subseteq \pi_{\mathbf{v}}[d]$ ; and  $\ll$  is a simulation relation from  $\mathcal{P}(C)$  to  $\mathcal{P}(D)$ , i.e.  $c \ll d \Rightarrow M(c) \ll N(d)$ . That  $\ll$  is a simulation relation can also be stated in terms of the usual functions for abstraction  $\alpha$  and concretisation  $\gamma$ , as we did in the previous definition of abstract predicate transformers.

Theorem 2.  $M \leq N \Leftrightarrow M \leq_{set} N$ 

## A Appendices

## References

- Chou, C.T.: The mathematical foundation of symbolic trajectory evaluation. In: International Conference on Computer Aided Verification. pp. 196–207. Springer (1999)
- Cousot, P.: Abstract interpretation. ACM Computing Surveys (CSUR) 28(2), 324
   – 328 (1996)
- 3. Davey, B.A., Priestley, H.A.: Introduction to lattices and order. Cambridge university press (2002)
- 4. Muchnick, S., et al.: Advanced compiler design implementation. Morgan kaufmann (1997)
- 5. Seger, C.J.H., Bryant, R.E.: Formal verification by symbolic evaluation of partially-ordered trajectories. Formal Methods in System Design  ${\bf 6}(2)$ , 147–189 (1995)