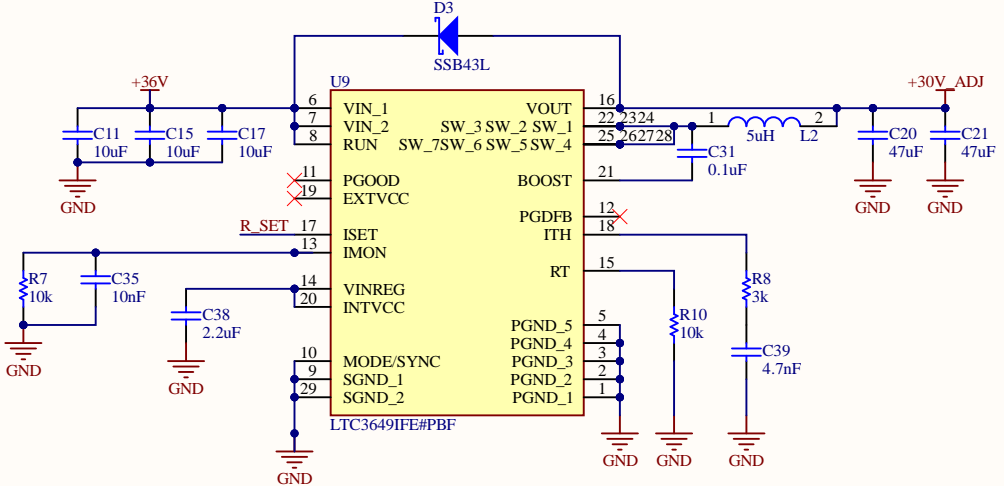
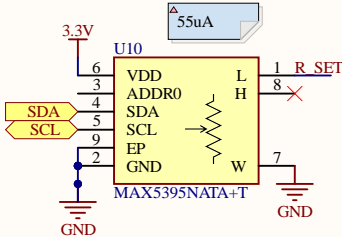
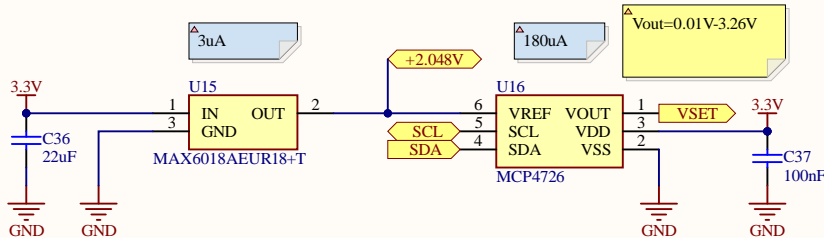


1	2	3	4
<div><div>II version</div><div>III version</div><div>VI version</div><div>V version</div></div>			
A			A
<div><div><div>1. Changed the fuse symbol</div><div>2. Changed components style</div><div>3. Replaced MCU pins</div><div>4. There were 3 different capacitor symbols on schematics, now 1</div><div>5. Changed VCC and 3.3V symbols</div><div>6. Placed ports and changed some net labels</div><div>7. Added datasheet links under parameters</div><div>8. In II version used LTC3649 and LT3080 for feedback and power control</div><div>9. Maybe right now the old sensing sheet is useless?</div></div><div><div>1. Annotated schematics</div><div>2. Changed designators to standard designators</div><div>3. Changed coltage and current sensing logic</div><div>4. Added on-board connectors for components that will be connected with wires</div><div>5. Fixed all errors on schematic</div><div>6. Recalculated all of the resistor values</div><div>7. All of power pins now have their voltage inside their net name</div><div>8.</div></div><div><div>1. Refactored schematics - added output&amp;ext-modules sheet for external modules and outputs.</div><div>2. Changed X2 caps value</div><div>3. Changed J11 designator</div><div>4. Changed 3.3V and 5V LDOs</div><div>5. Changed position of USB connector</div><div>6. Fixed USB connector circuit to program MCU</div><div>7. Took away VBUS cap</div><div>8. Changed 18k resistors to 9k</div><div>9. Removed reverse protection diode between input and output</div></div><div><div>1. Added comments to comparator schematic block</div><div>2. Fixed R2 and R3 values from 22k to 22R</div><div>3. Calculated the current of 5V and 3.3V components - added them as blue notes</div><div>4. Added crystal caps calculations</div><div>5. Modified MCU symbol</div><div>6. Modified the layout of components to keep inputs on the left hand side</div><div>7. Removed two different DACs - now using one</div><div>8. modified J5 and added 20R resistor to have 250mA output and 5V output</div><div>9. Modified connections of U7</div><div>10. Modified U11, U12, U13 symbols (LT3080)</div><div>11. Modified uninformative nets</div><div>12. Took out unnecessary components</div></div></div>			
B			B
C			C
D			D
<div><div><div>Title</div><div><div>Size</div><div>Number</div><div>Revision</div></div><div><div>Date:</div><div>12/07/2023</div><div>Sheet of</div></div><div><div>File:</div><div>\\.\LOG.SchDoc</div><div>Drawn By:</div></div></div></div>			
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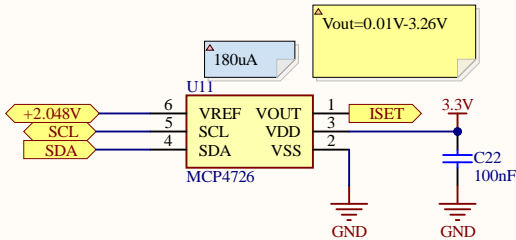
+36V IN -> ADJ V OUT adjusted by E-POT



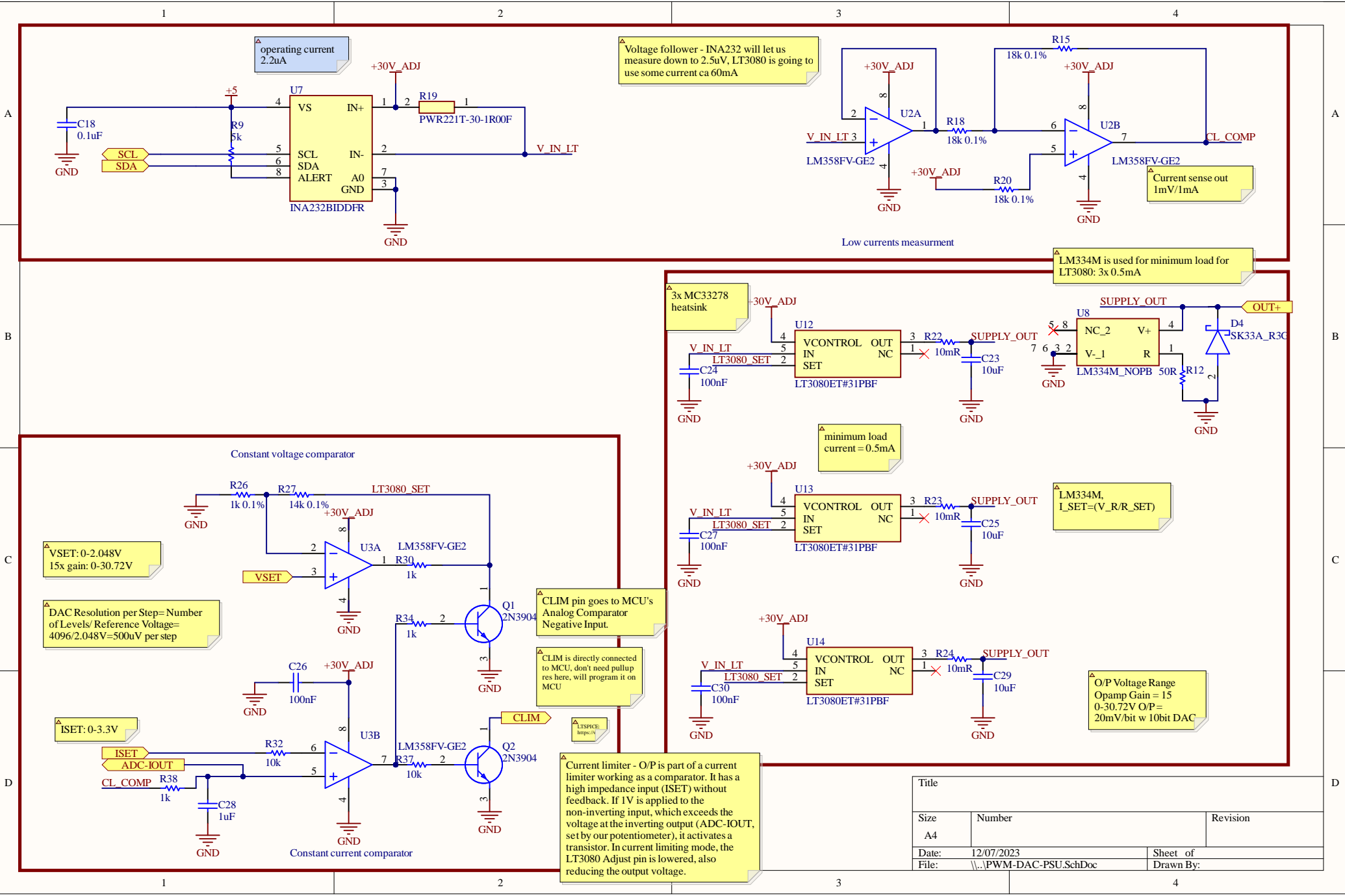
VREF - I2C DAC for VSET



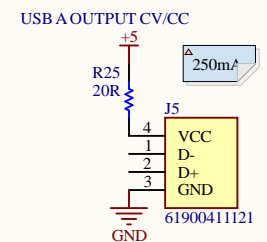
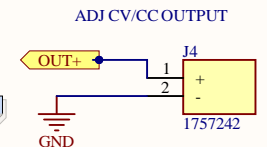
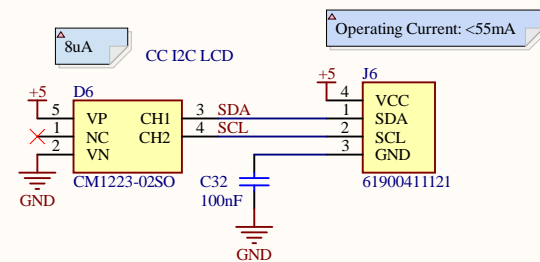
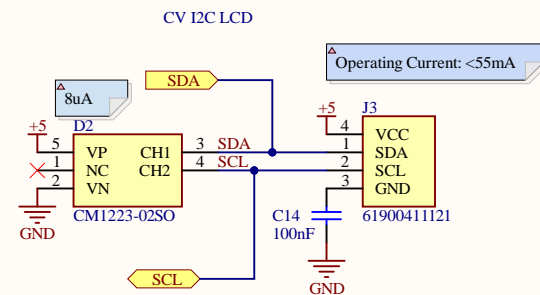
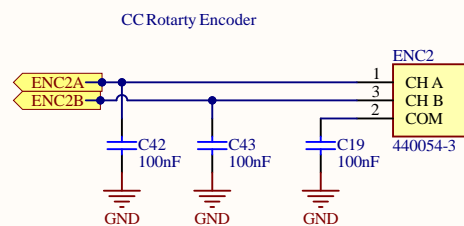
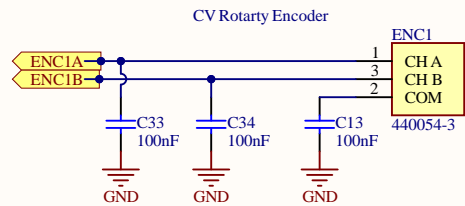
I2C DAC for ISET



Title		
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Title		
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