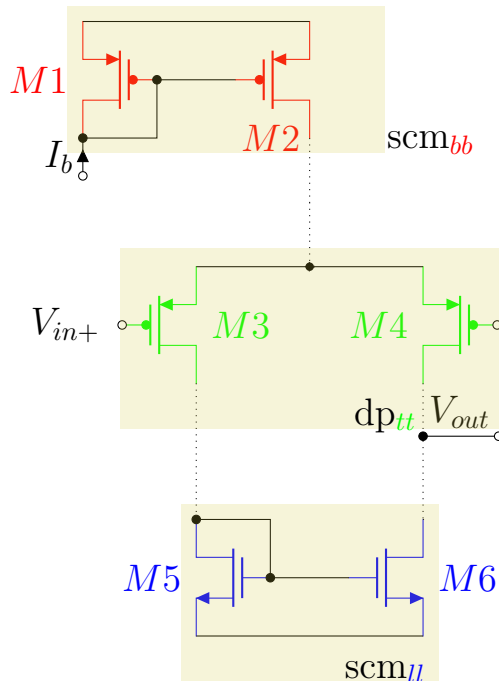


training
samples

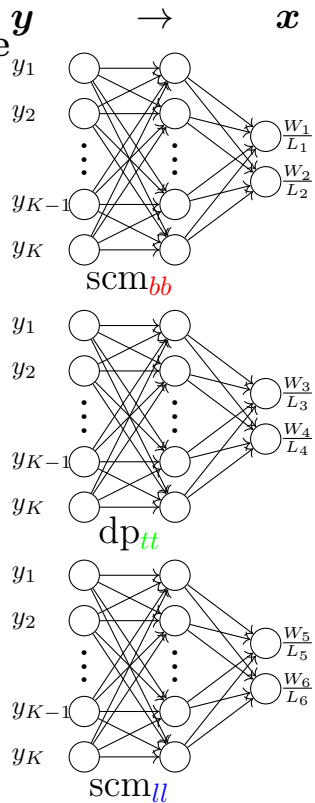


circuit
performance

y

\rightarrow

x device
size



fine-tune

evaluate

test
samples

