|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | RegWrite | ALU\_src | RegDst | Branch | MemWrite | | | | MemRead | | MemtoReg | | | Jump | | RegJal | ALU\_op | |
| R\_type  000000 | 1 | 0 | 1 | 0 | 0 | | | | 0 | | 0 | | | 0 | | 0 |  | |
| Addi  001000 | 1 | 1 | 0 | 0 | 0 | | | | 0 | | 0 | | | 0 | | 0 |  | |
| Beq  000100 | 0 | 0 | X | 1 | 0 | | | | 0 | | 0 | | | 0 | | 0 |  | |
| Lw  100011 | 1 | 1 | 0 | 0 | 0 | | | | 1 | | 0 | | | 0 | | 0 |  | |
| Sw  101011 | 0 | 1 | x | 0 | 1 | | | | 0 | | 1 | | | 0 | | 0 |  | |
| J  000010 | 0 | X | X | 0 | 0 | | | | 0 | | 0 | | | 0 | | 0 |  | |
| Jal  000011 | 1\* | X | X | 0 | 0 | | | | 0 | | 0 | | | 1 | | 0 |  | |
| Jr  000000 | 0 | x | X | 0 | 0 | | | | 0 | | 0 | | | 1 | | 1 |  | |
| Ble  000110 | 0 | 0 | X | 1 | 0 | | | | 0 | | 0 | | | 0 | | 0 |  | |
| Bnez  000101 | 0 | 0 | X | 1 | 0 | | | | 0 | | 0 | | | 0 | | 0 |  | |
| Bltz  000001 | 0 | 0 | X | 1 | 0 | | | | 0 | | 0 | | | 0 | | 0 |  | |
| Li  001111 | 1 | 1 | 0 | 0 | 0 | | | | 1 | | 0 | | | 0 | | 0 |  | |
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|  | MemtoReg | Jump | RegJal | ALU\_op | |  |  | | |  | |  |  | |
| R\_type | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Addi | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Beq | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Bne | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Lw | 1 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Sw | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| J | 0 | 1 | 0 |  | |  |  | | |  | |  |  | |
| Jal | 0 | 1 | 1 |  | |  |  | | |  | |  |  | |
| Jr | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Ble | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Bnez | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Bltz | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |
| Li | 0 | 0 | 0 |  | |  |  | | |  | |  |  | |