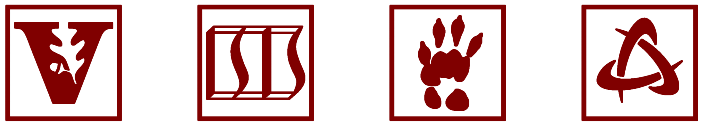
Teton

Mixed-signal computational module for the Marmote platform

http://marmote.googlecode.com



Pages:

1 - Cover page
2 - SF supply pins
3 - Clock, reset, JTAG
4 - SF AFE
5 - External AFE #1
6 - External AFE #2
7 - Ethernet
8 - USB and AFE dig.
9 - I/O signals
10 - Connectors

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Cover page

Title: Teton

Rev: A

Date: 12/12/2012 8:42:37

Sheet: 1/10

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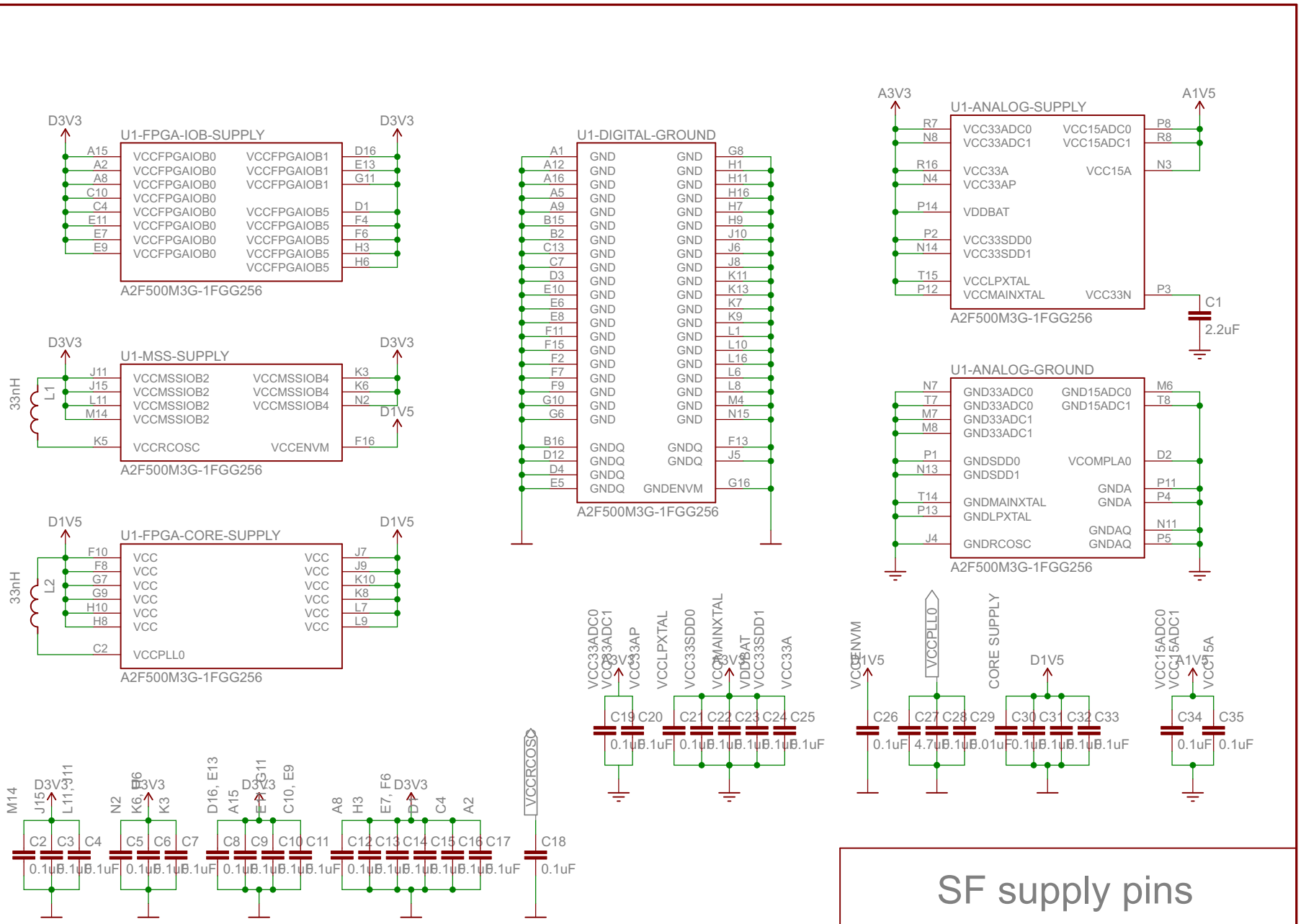

SF supply pins

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 2/10

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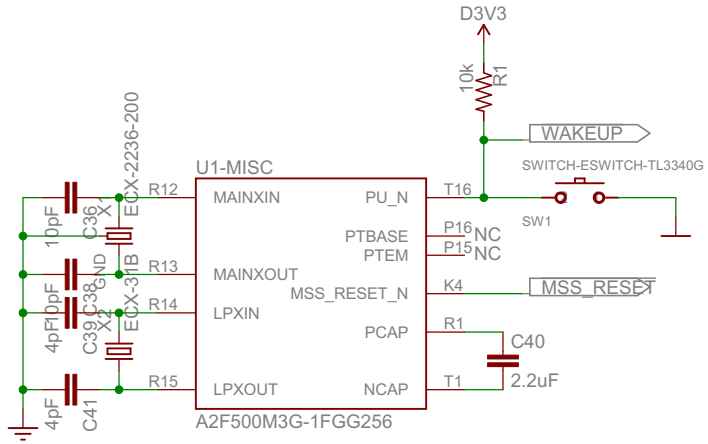
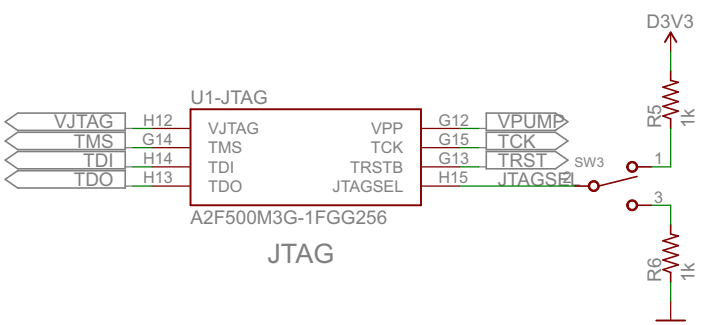
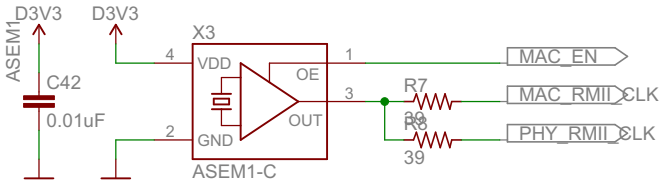


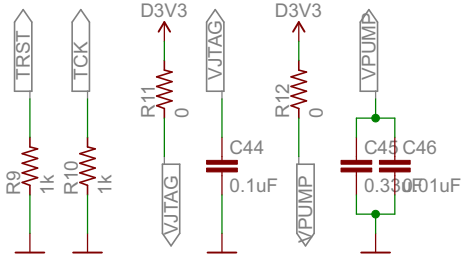
Internal oscillator - 32 kHz

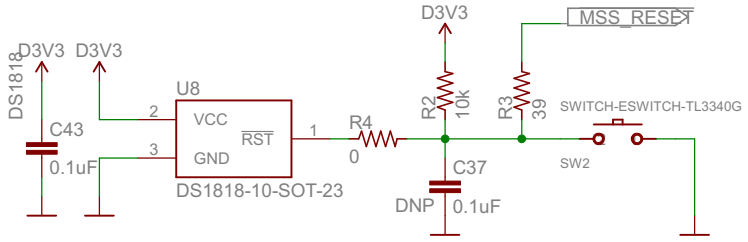


JTAG



Ethernet oscillator - 50 MHz





Reset logic

Clock, reset, JTAG

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 3/10

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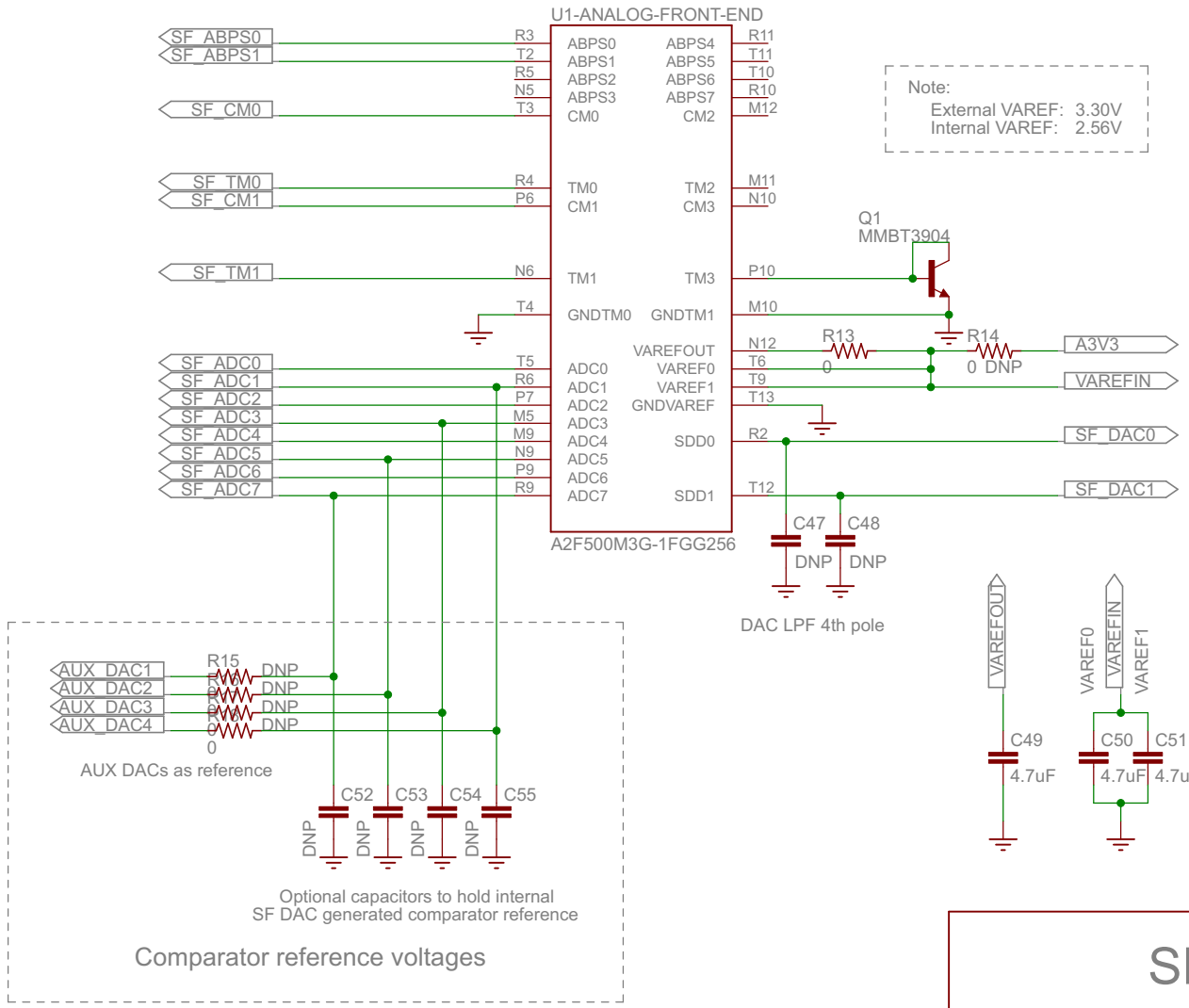

SF AFE

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 4/10

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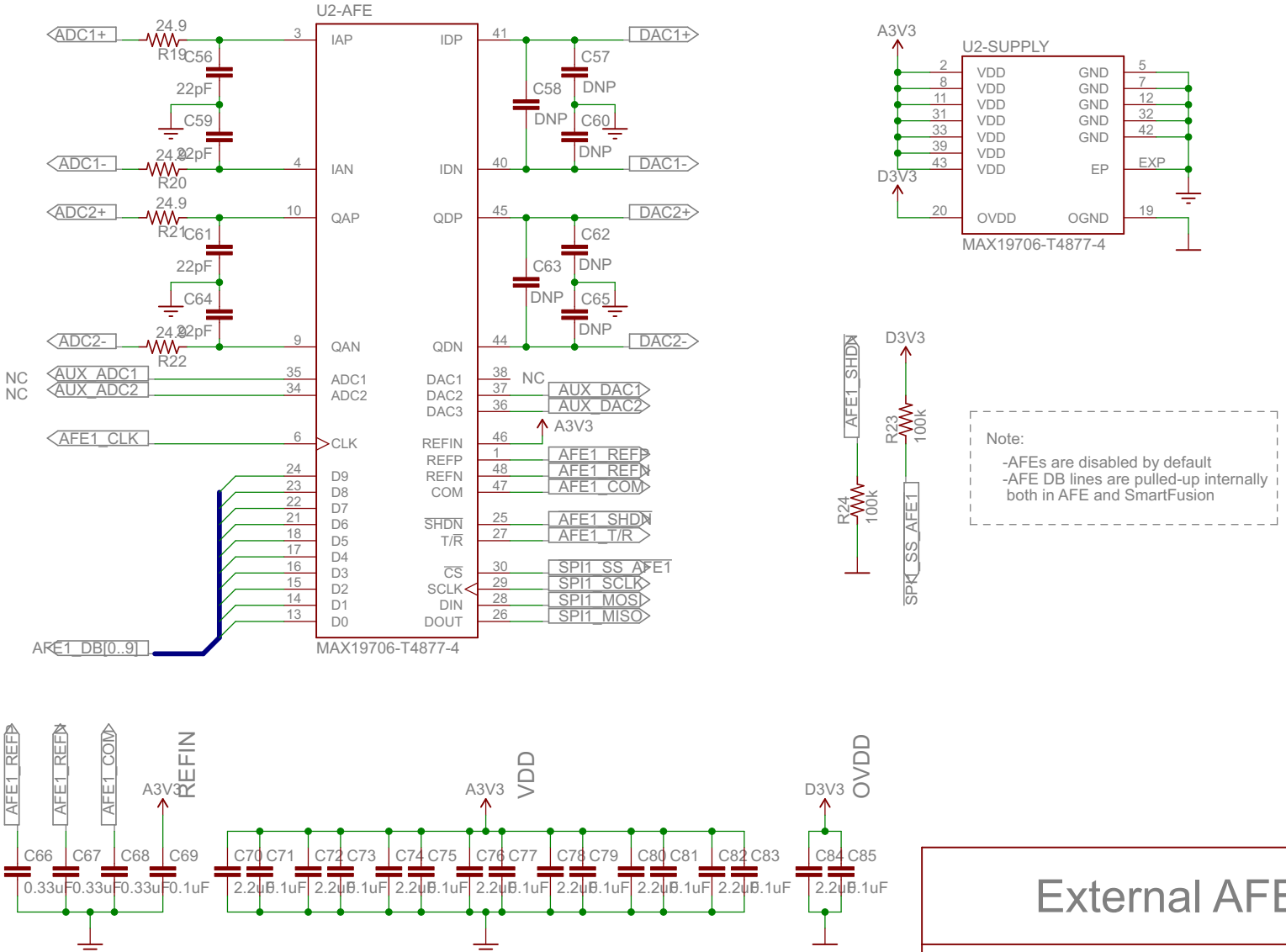

External AFE #1

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 5/10

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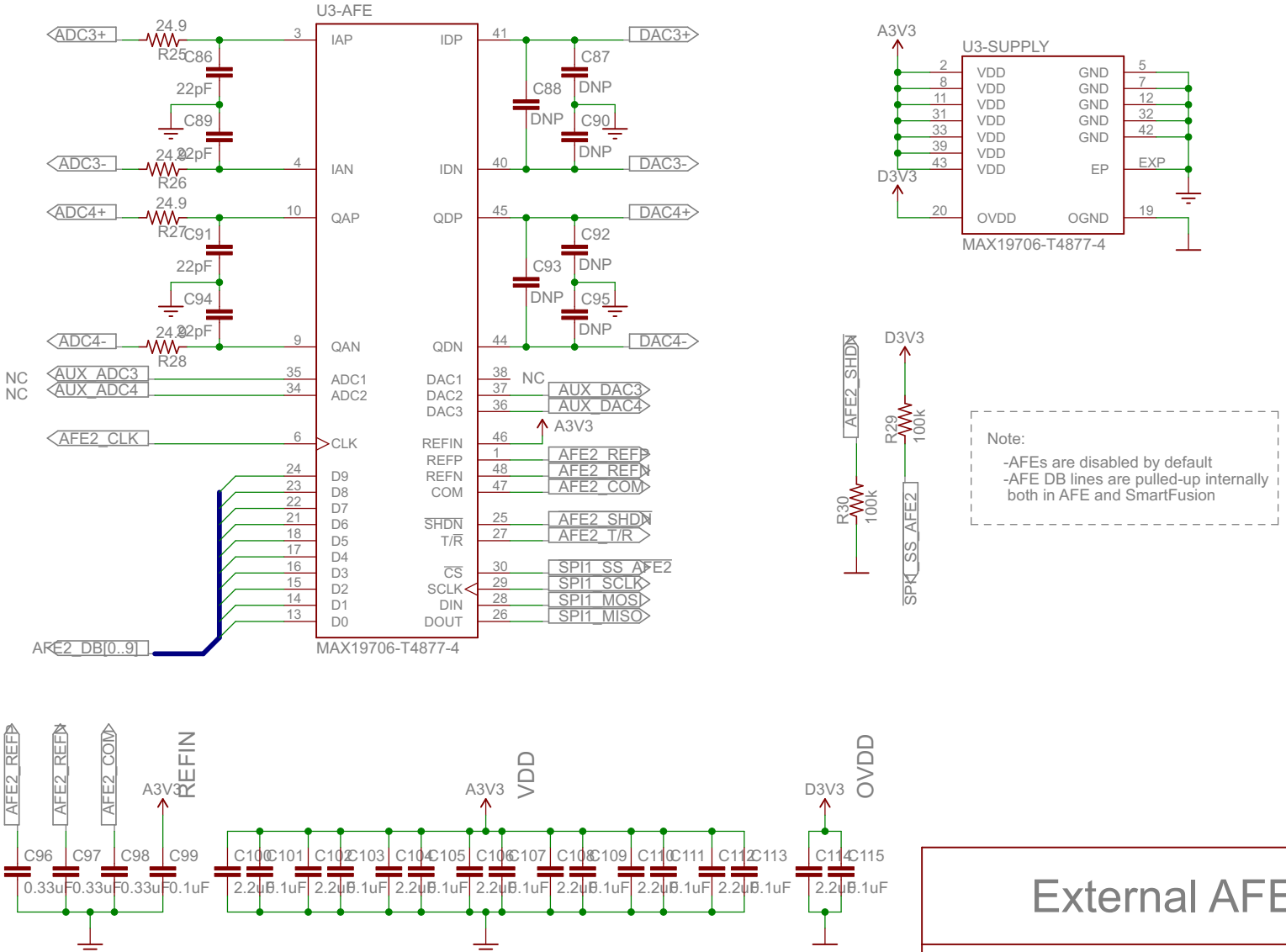

External AFE #2

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 6/10

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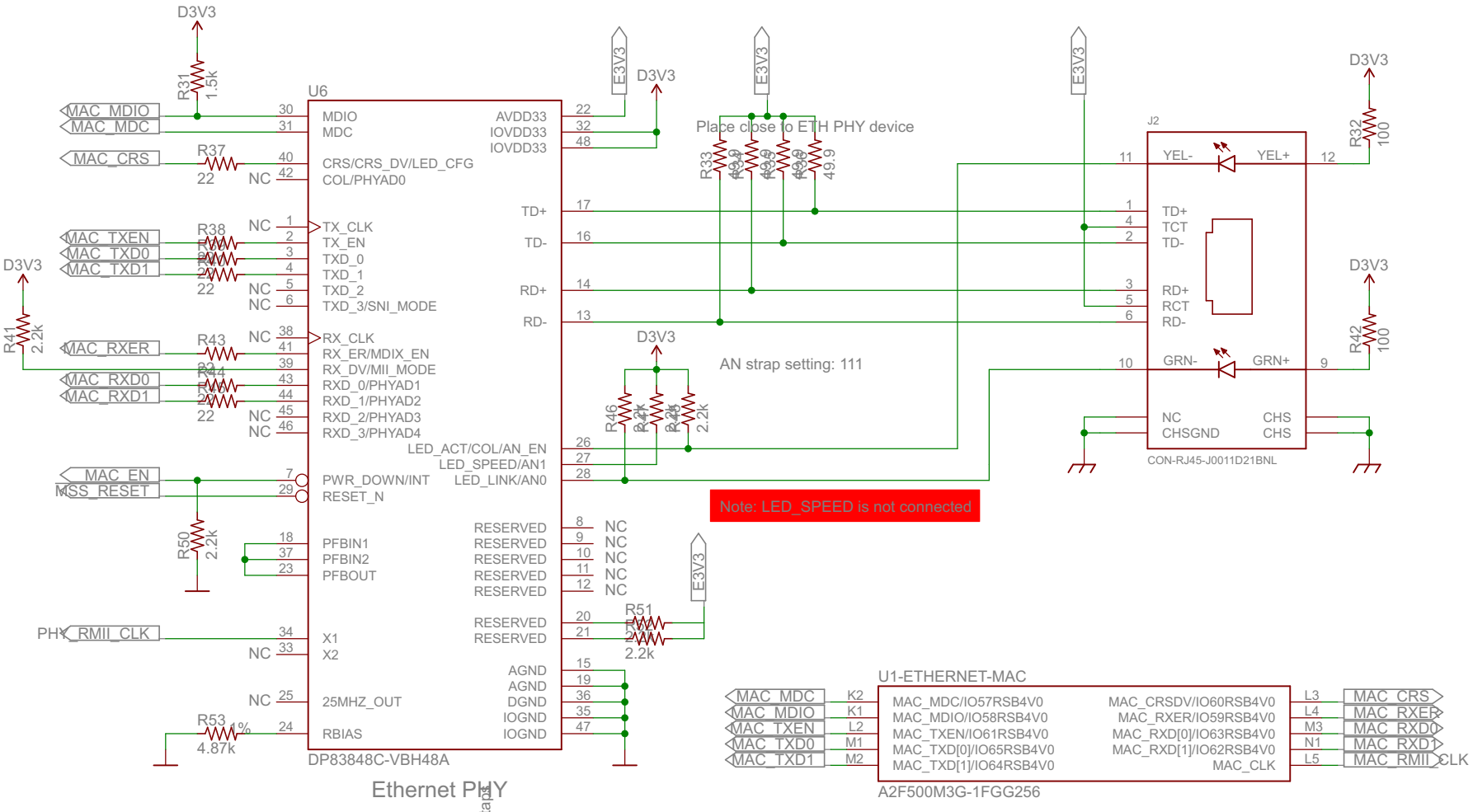

Ethernet

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 7/10

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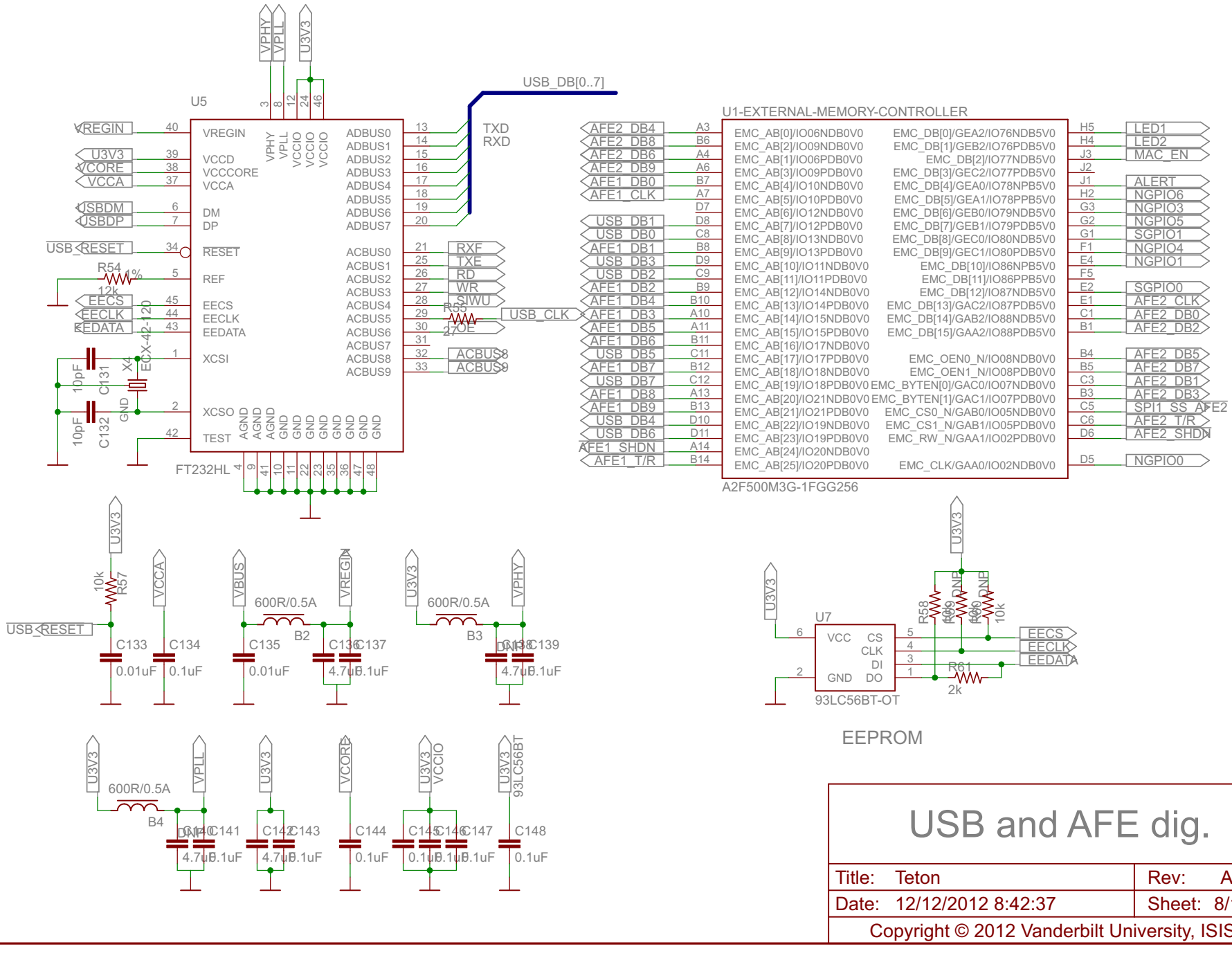

USB and AFE dig.

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 8/10

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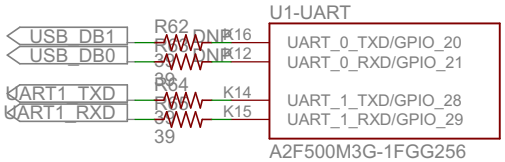
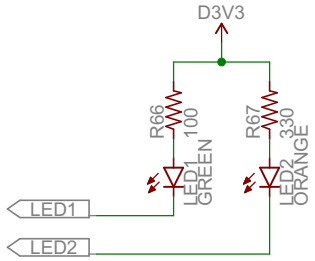
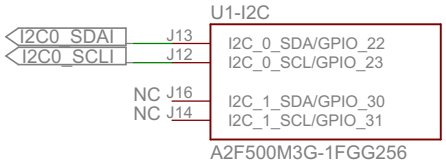
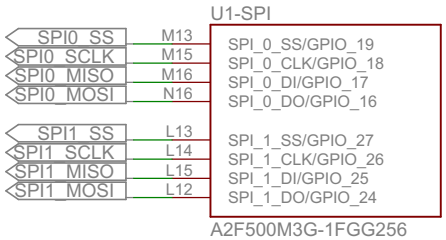
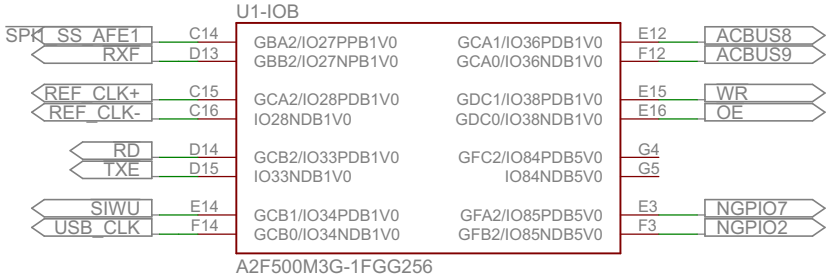


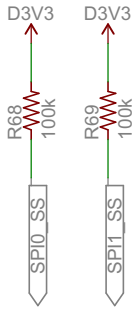


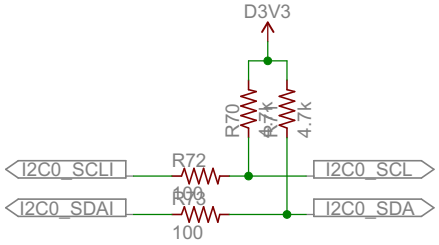
LEDs











I/O signals

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 9/10

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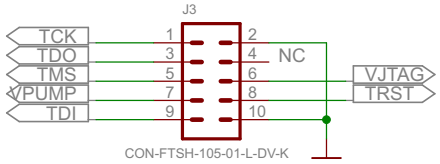
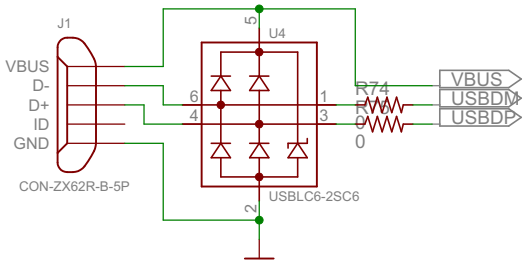
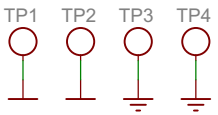
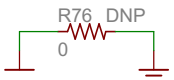


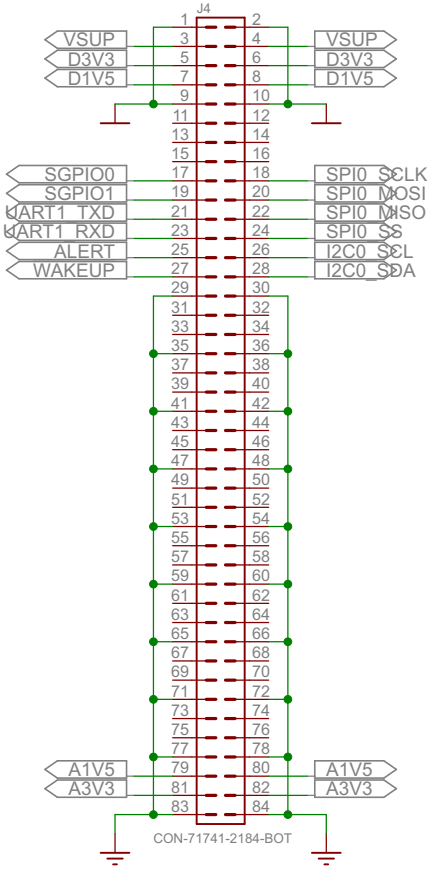
JTAG header



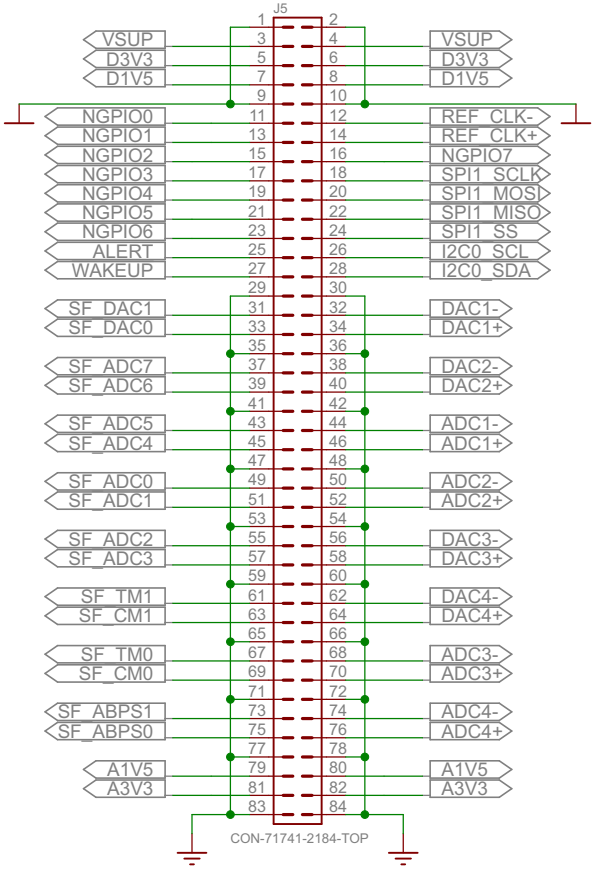
USB connector







South bridge



North bridge

Connectors

Title: Teton	Rev: A
Date: 12/12/2012 8:42:37	Sheet: 10/10

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