

Teton

Mixed-signal computational module for the Marmote platform

<http://marmote.googlecode.com>



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Cover page

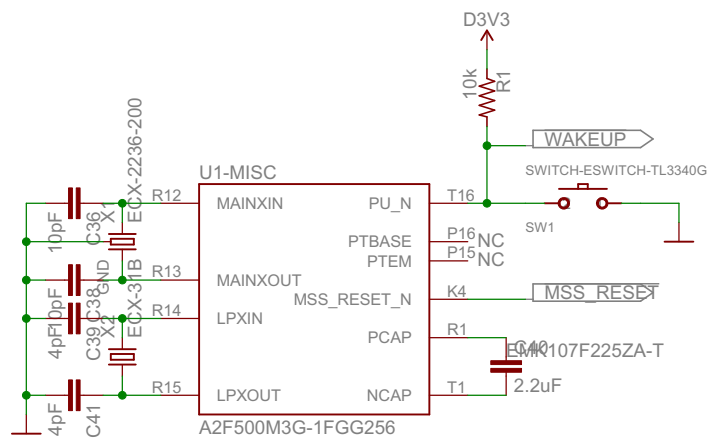
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Rev: A

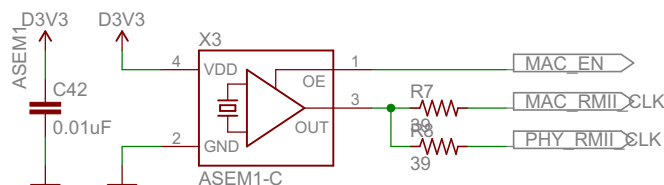
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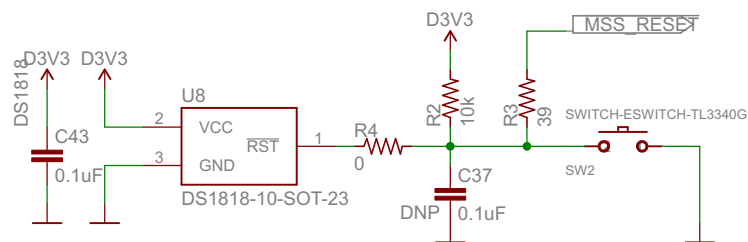
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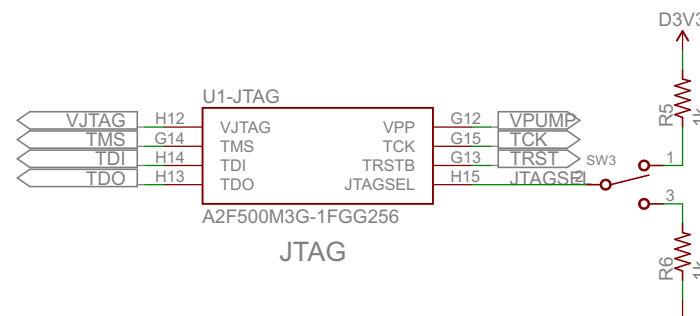
Internal oscillator - 32 kHz



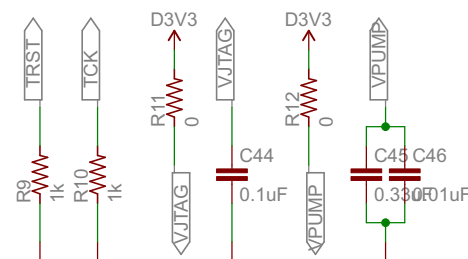
Ethernet oscillator - 50 MHz



Reset logic

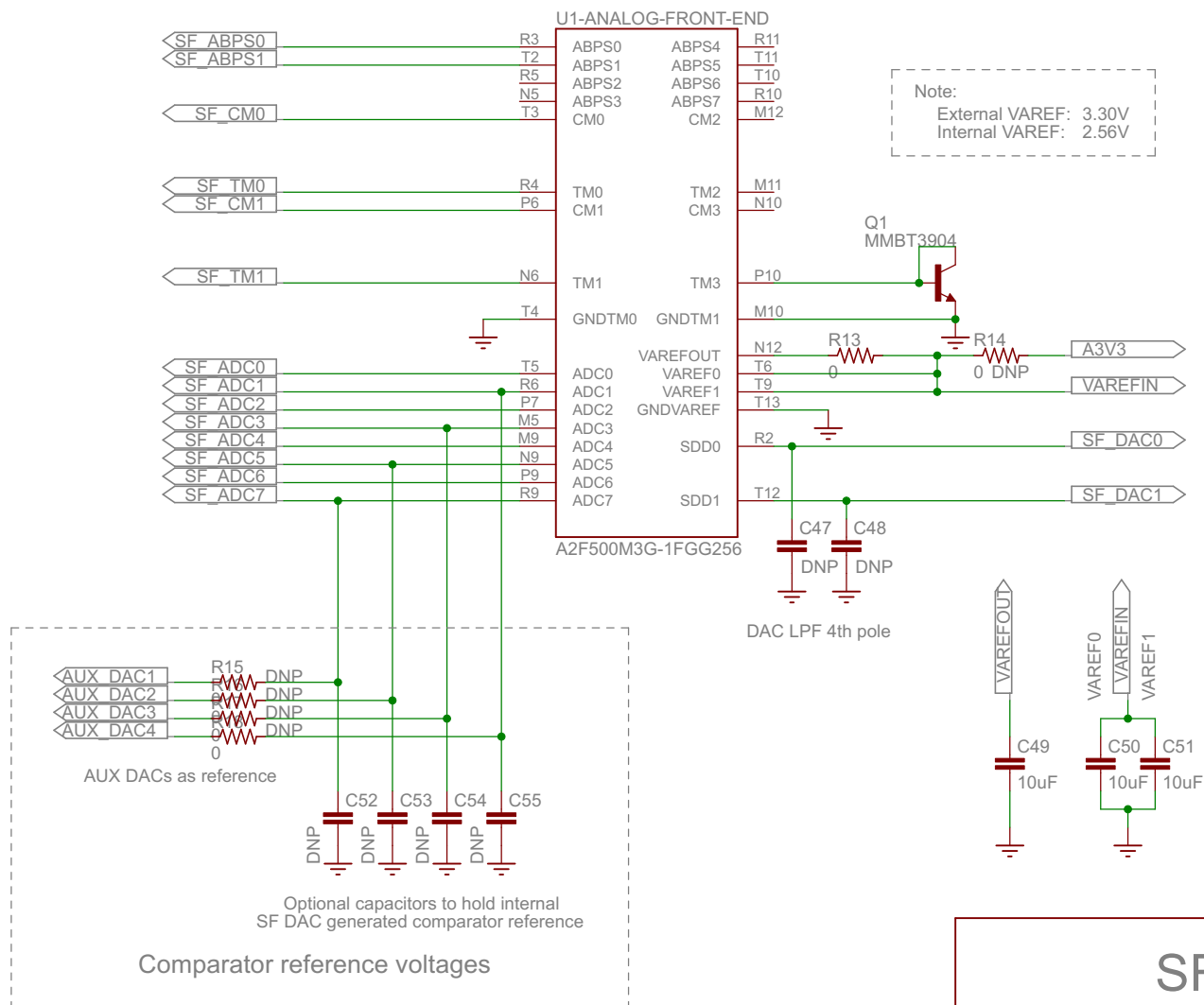


JTAG



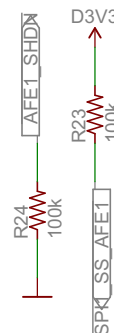
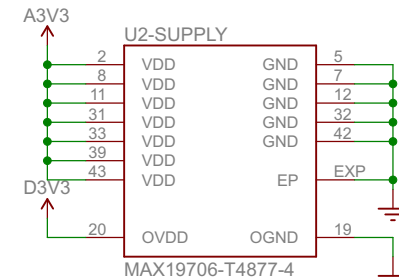
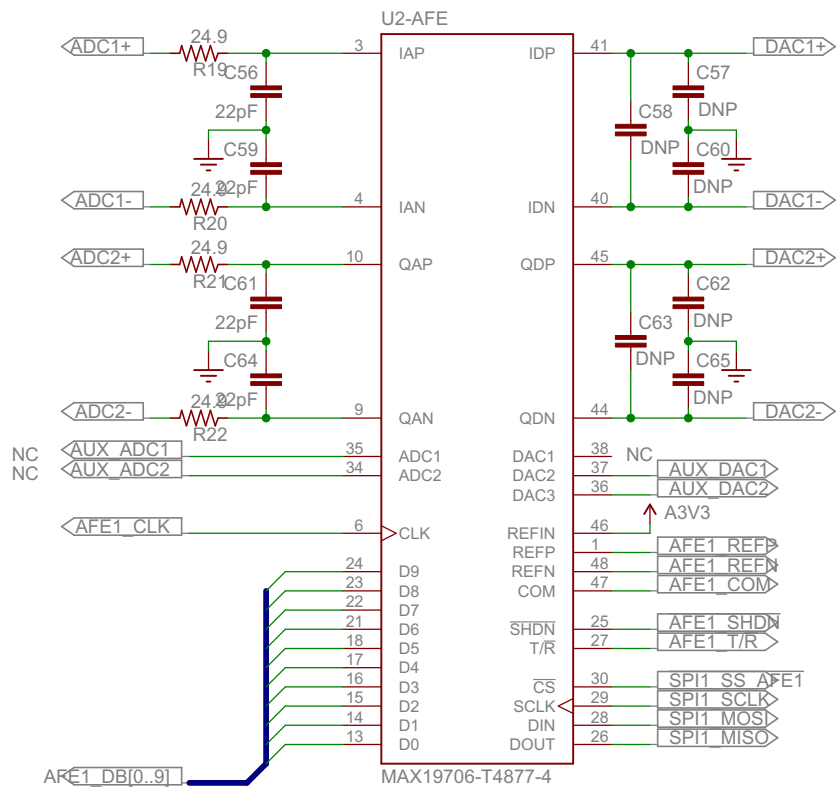
Clock, reset, JTAG

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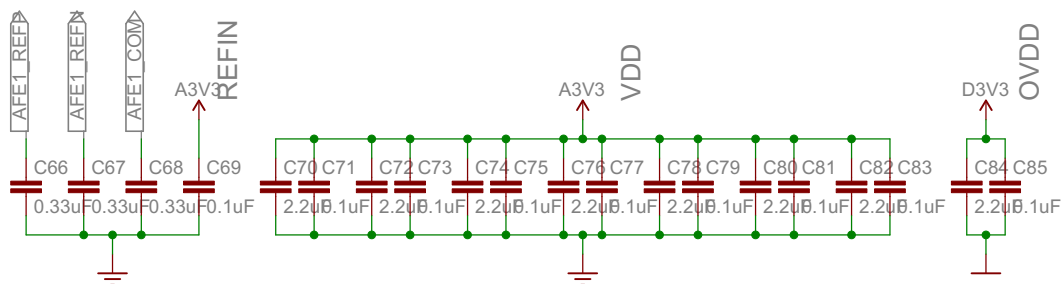


SF AFE

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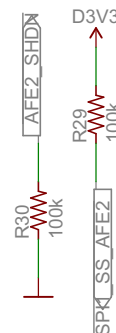
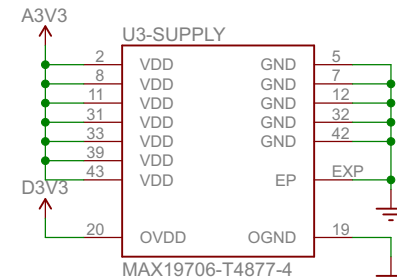
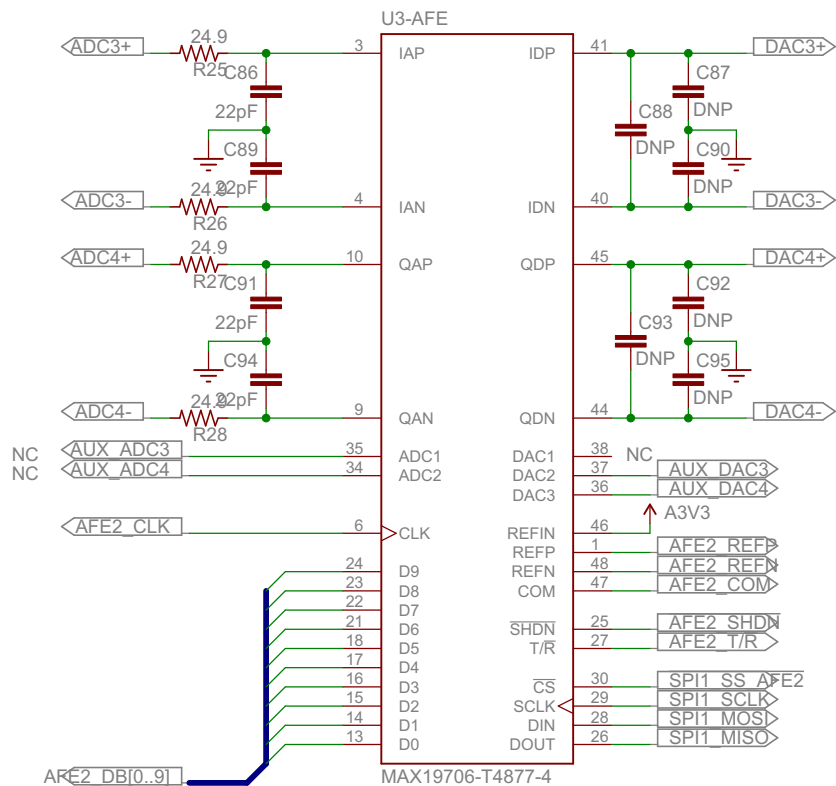


Note:
 -AFEs are disabled by default
 -AFE DB lines are pulled-up internally
 both in AFE and SmartFusion



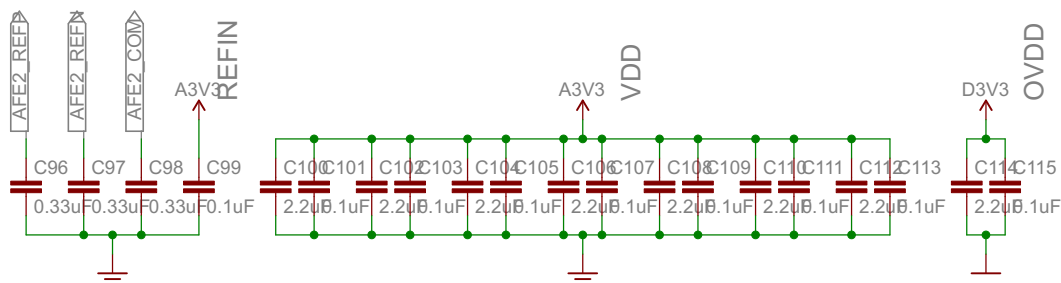
External AFE #1

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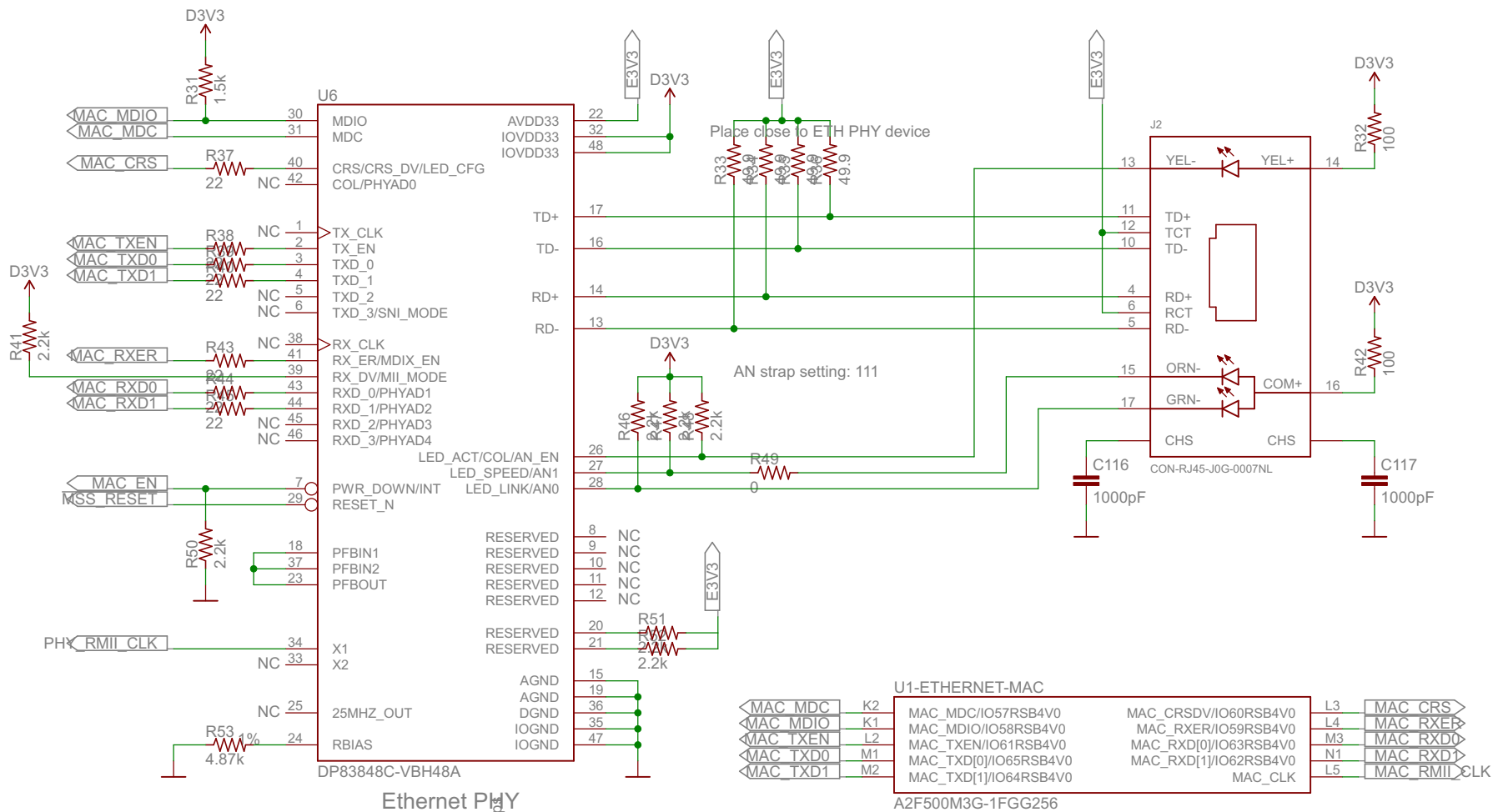
Note:

- AFEs are disabled by default
- AFE DB lines are pulled-up internally both in AFE and SmartFusion



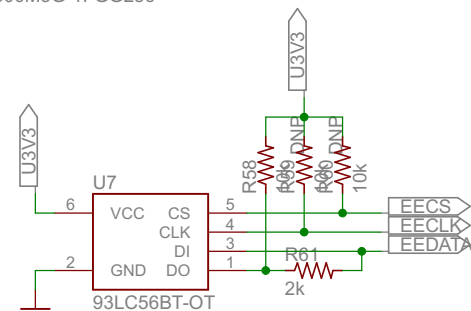
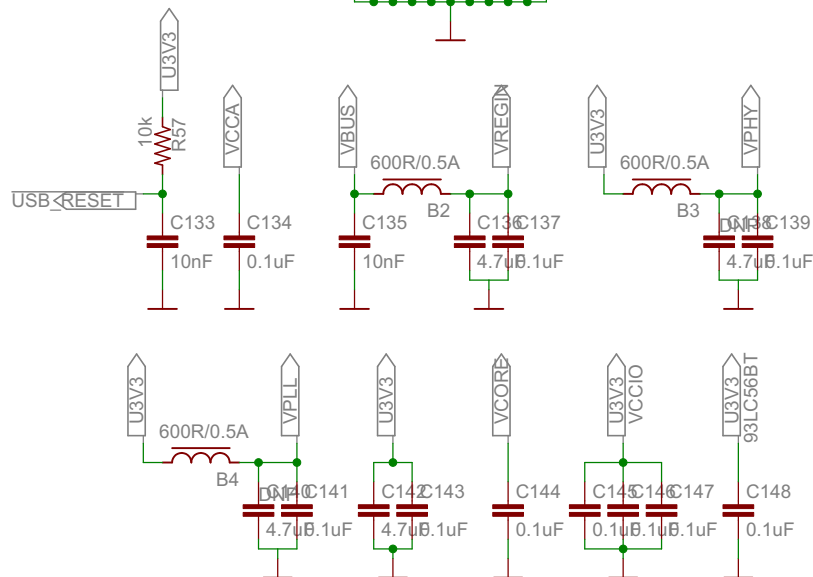
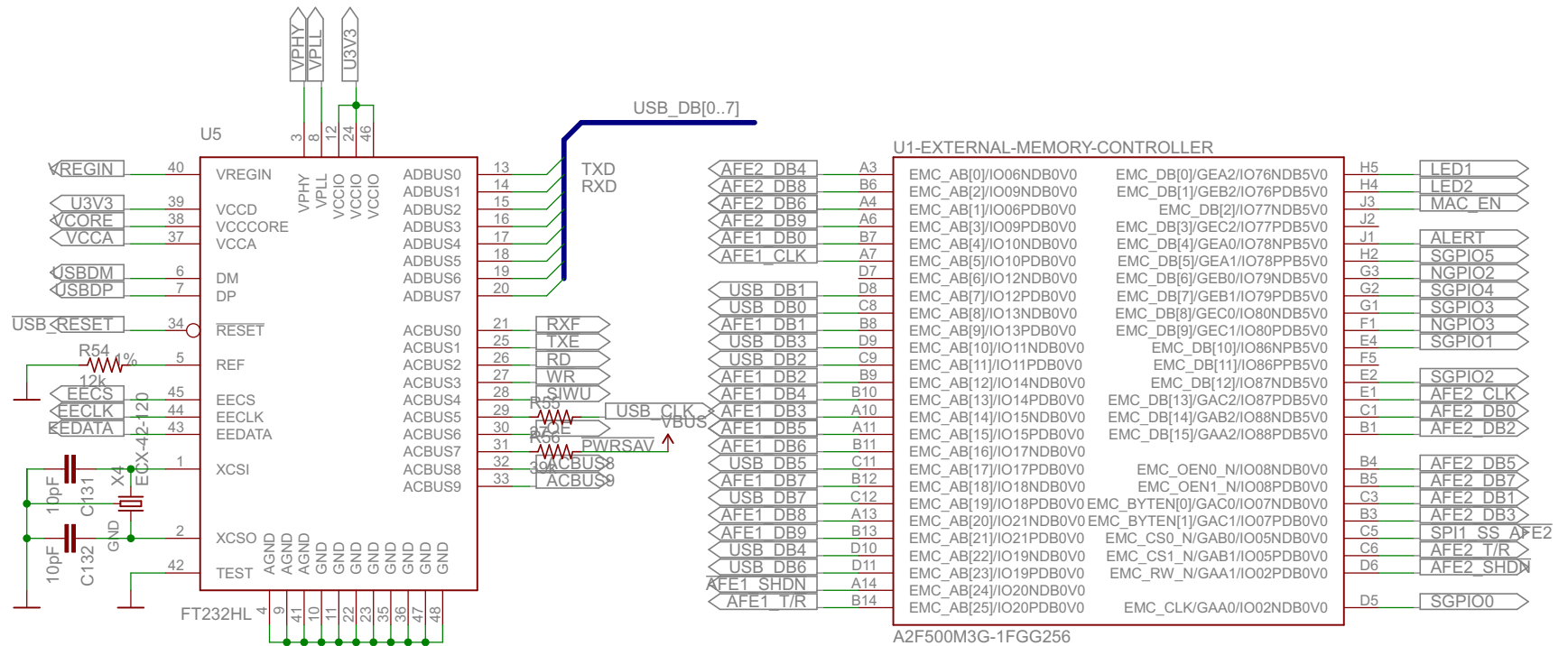
External AFE #2

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Ethernet

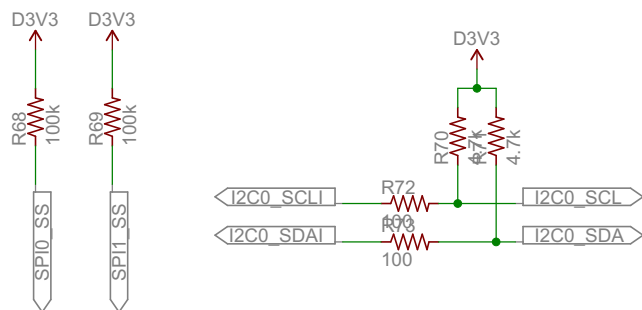
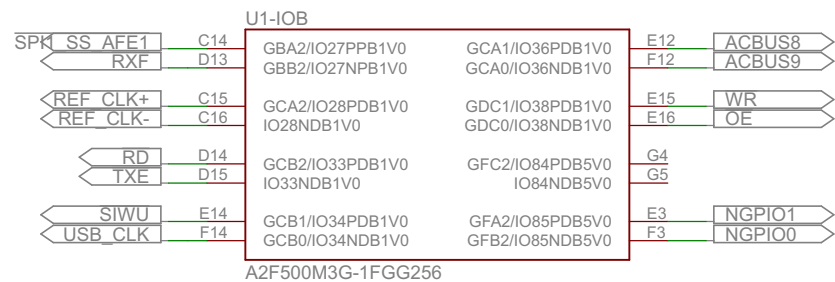
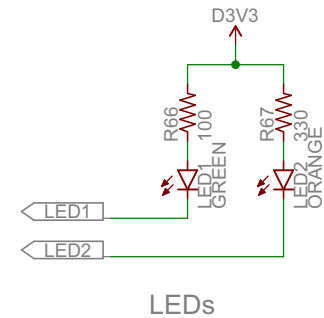
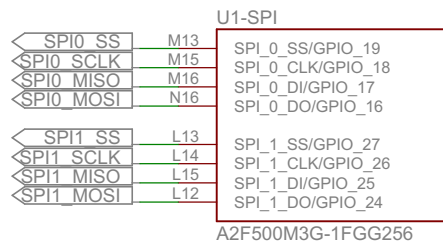
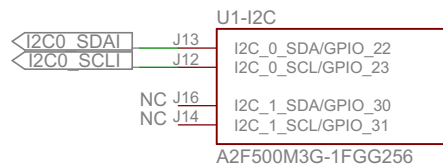
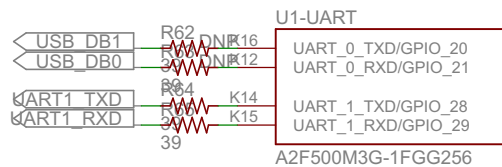
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EEPROM

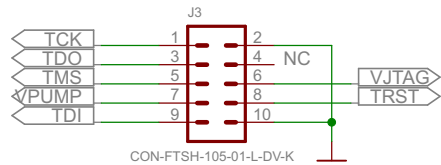
USB and AFE dig.

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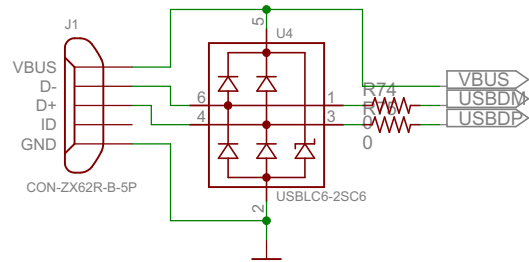


I/O signals

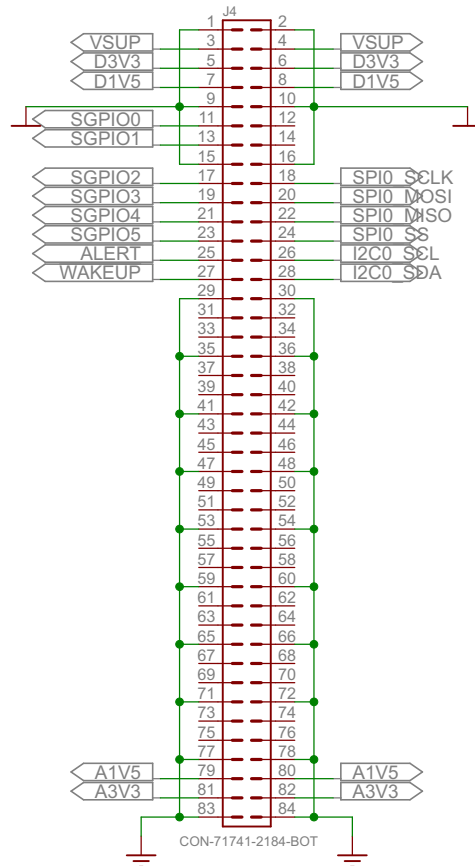
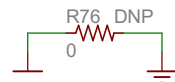
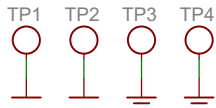
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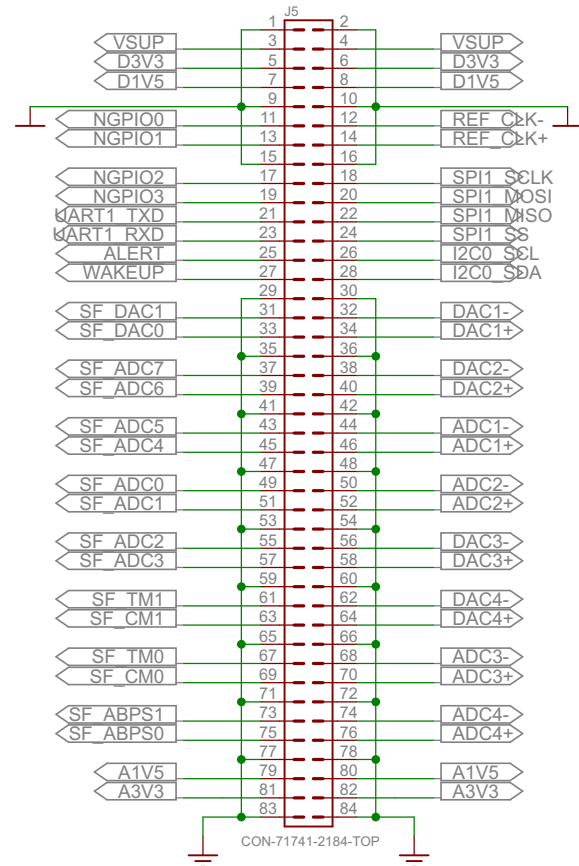
JTAG header



USB connector



South bridge



North bridge

Connectors

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