

N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

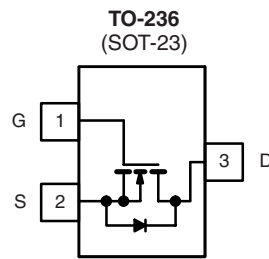
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ.)
30	0.047 at $V_{GS} = 10$ V	4.0	3.0
	0.065 at $V_{GS} = 4.5$ V	3.5	

FEATURES

- Halogen-free Option Available
- TrenchFET® Power MOSFET
- 100 % R_g Tested



RoHS
COMPLIANT



Top View
Si2306BDS (L6)*

* Marking Code

Ordering Information: Si2306BDS-T1-E3 (Lead (Pb)-free)
Si2306BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted

Parameter	Symbol	5 s	Steady State	Unit
Drain-Source Voltage	V_{DS}	30		V
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current ($T_J = 150$ °C) ^{a, b}	I_D	4.0	3.16	A
		3.5	2.7	
Pulsed Drain Current	I_{DM}	20		
Continuous Source Current (Diode Conduction) ^{a, b}	I_S	1.04	0.62	
Maximum Power Dissipation ^{a, b}	P_D	1.25	0.75	W
		0.8	0.48	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	80	100	°C/W
		130	166	
Maximum Junction-to-Foot (Drain)	R_{thJF}	60	75	

Notes:

- a. Surface Mounted on FR4 board, $t \leq 5$ s.
b. Pulse width limited by maximum junction temperature.
c. Surface Mounted on FR4 board.

For SPICE model information via the Worldwide Web: <http://www.vishay.com/www/product/spice.htm>

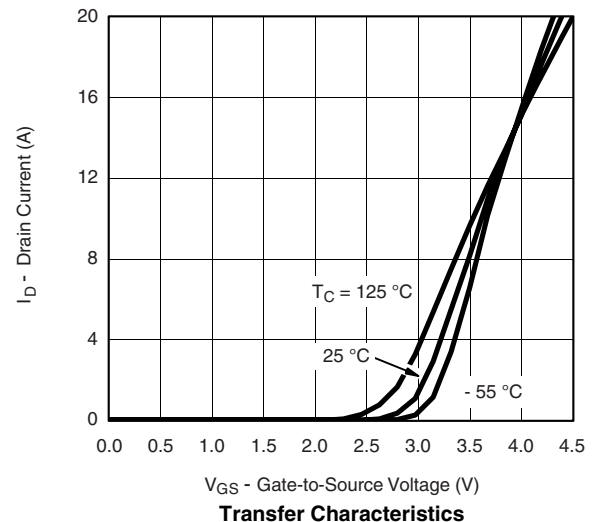
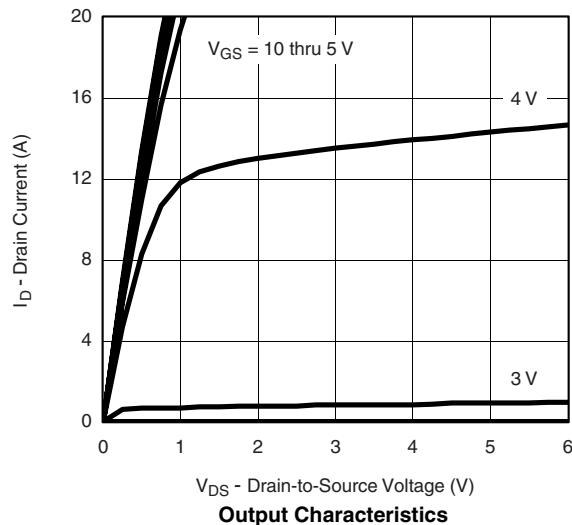
SPECIFICATIONS $T_A = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Limits			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA	30			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.0		3.0	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			0.5	μA
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 4.5 V, V _{GS} = 10 V	6			A
Drain-Source On-Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 3.5 A		0.038	0.047	Ω
		V _{GS} = 4.5 V, I _D = 2.8 A		0.052	0.065	
Forward Transconductance ^a	g _{fs}	V _{DS} = 4.5 V, I _D = 2.5 A		7.0		S
Diode Forward Voltage	V _{SD}	I _S = 1.25 A, V _{GS} = 0 V		0.8	1.2	V
Dynamic						
Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 5 V, I _D = 2.5 A		3.0	4.5	nC
Total Gate Charge	Q _{gt}			6	9	
Gate-Source Charge	Q _{gs}			1.6		
Gate-Drain Charge	Q _{gd}			0.6		
Gate Resistance	R _g	f = 1.0 MHz	2.5	5	7.5	Ω
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		305		pF
Output Capacitance	C _{oss}			65		
Reverse Transfer Capacitance	C _{rss}			29		
Switching						
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 15 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _g = 6 Ω		7	11	ns
Rise Time	t _r			12	18	
Turn-Off Delay Time	t _{d(off)}			14	25	
Fall Time	t _f			6	10	
Reverse Recovery Time	t _{rr}	I _F = 1.25 A, di/dt = 100 A/μs		14	21	nC
Body Diode Reverse Recovery Charge	Q _{rr}			6	10	

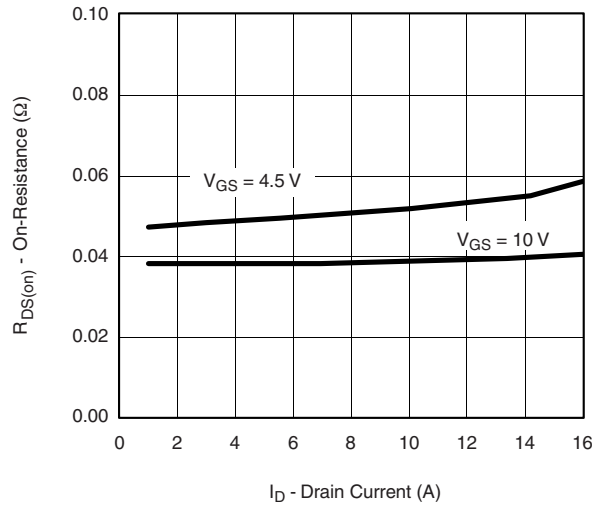
Notes:

a. Pulse test: Pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

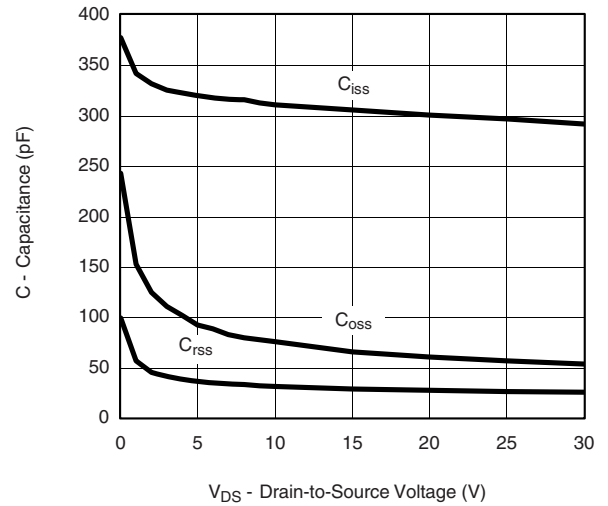
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25°C , unless otherwise noted

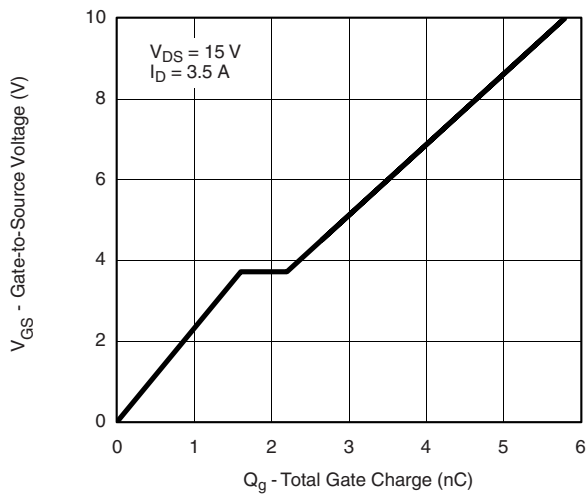
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



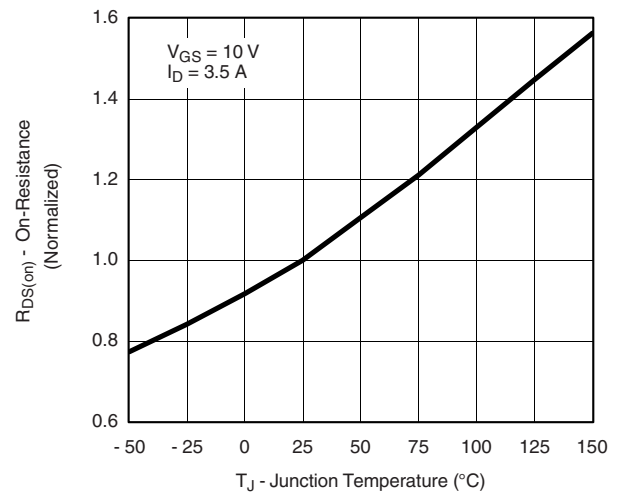
On-Resistance vs. Drain Current



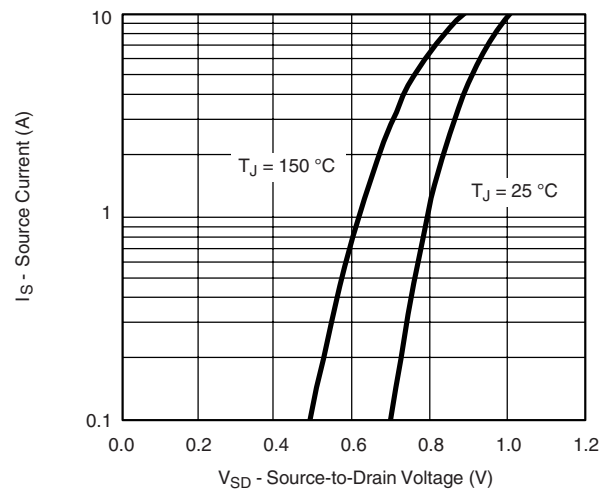
Capacitance



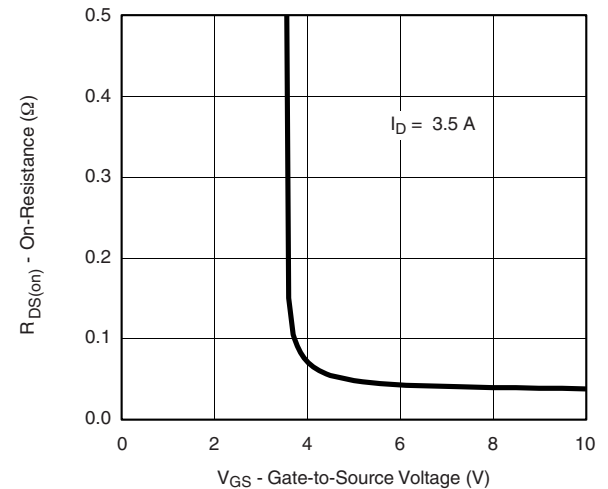
Gate Charge



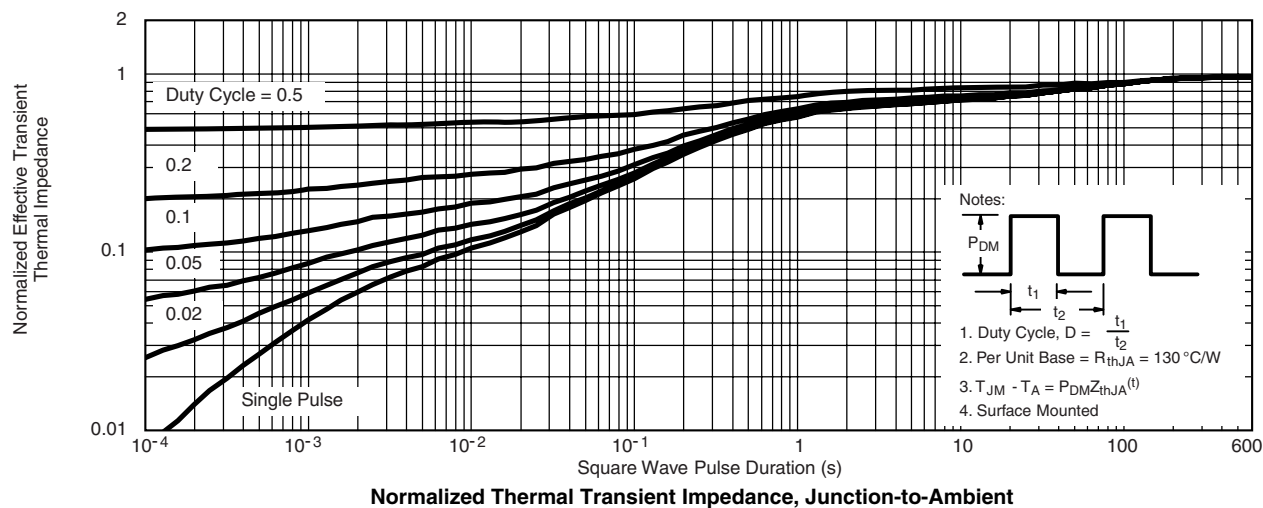
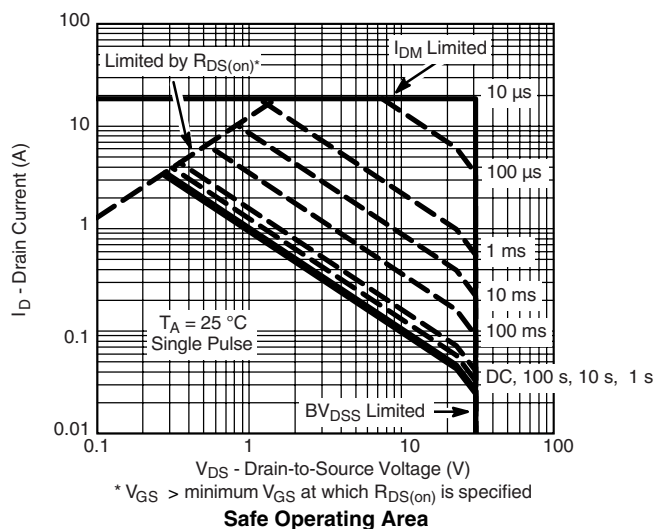
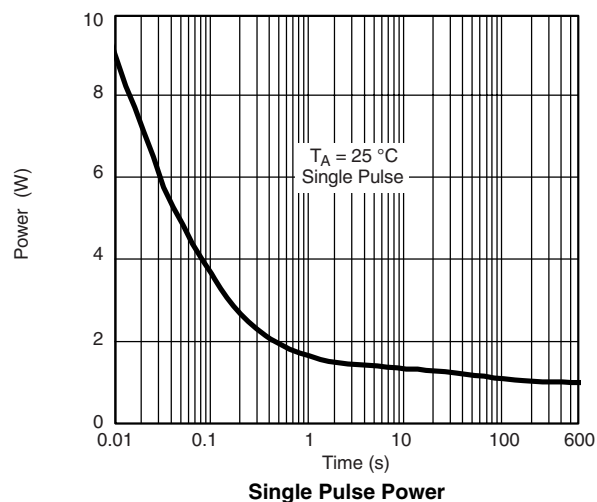
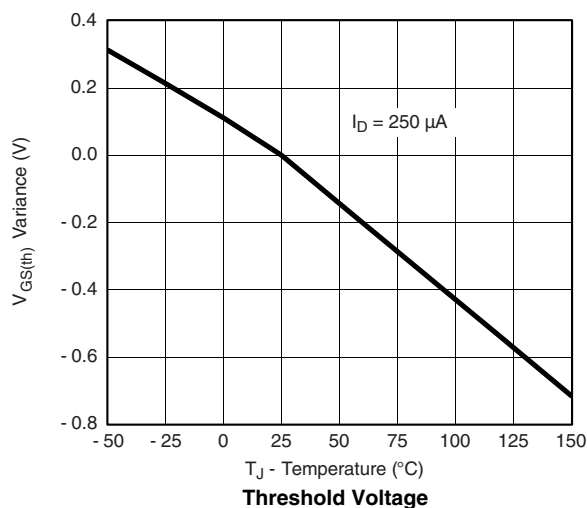
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

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