

# Teton

Mixed-signal computational module for the Marmote platform

<http://marmote.googlecode.com>



## Pages:

1 - Cover page	6 - External AFE #2
2 - SF supply pins	7 - Ethernet
3 - Clock, reset, JTAG	8 - USB and AFE dig.
4 - SF AFE	9 - I/O signals
5 - External AFE #1	10 - Connectors

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## Cover page

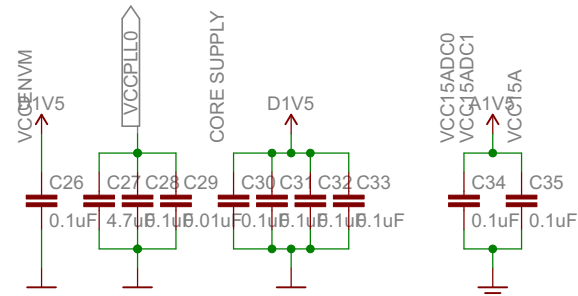
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Rev: A

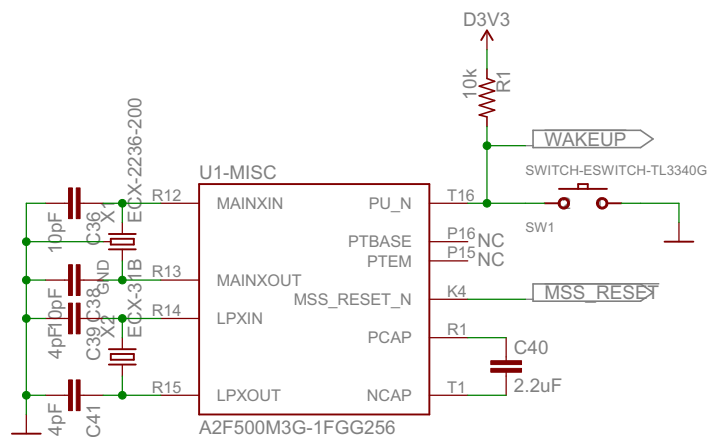
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Sheet: 1/10

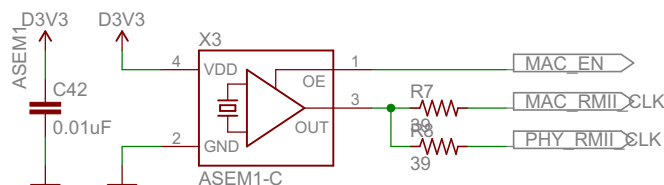
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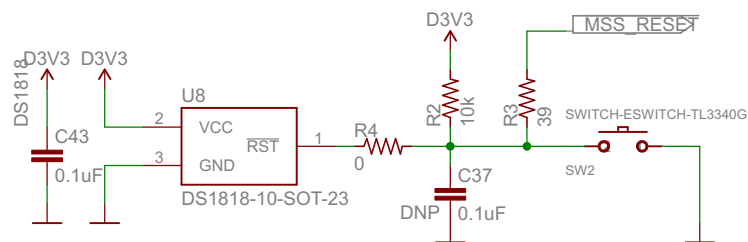
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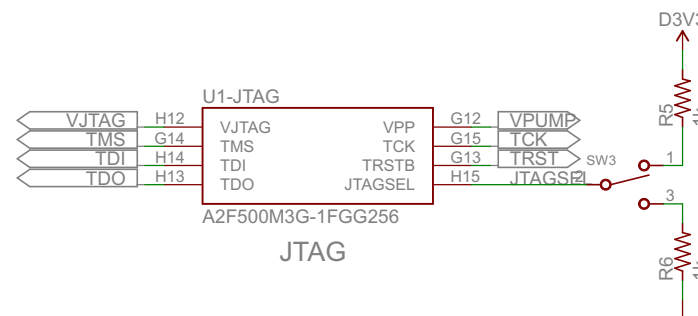
Internal oscillator - 32 kHz



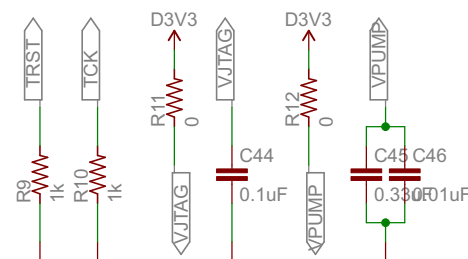
Ethernet oscillator - 50 MHz



Reset logic

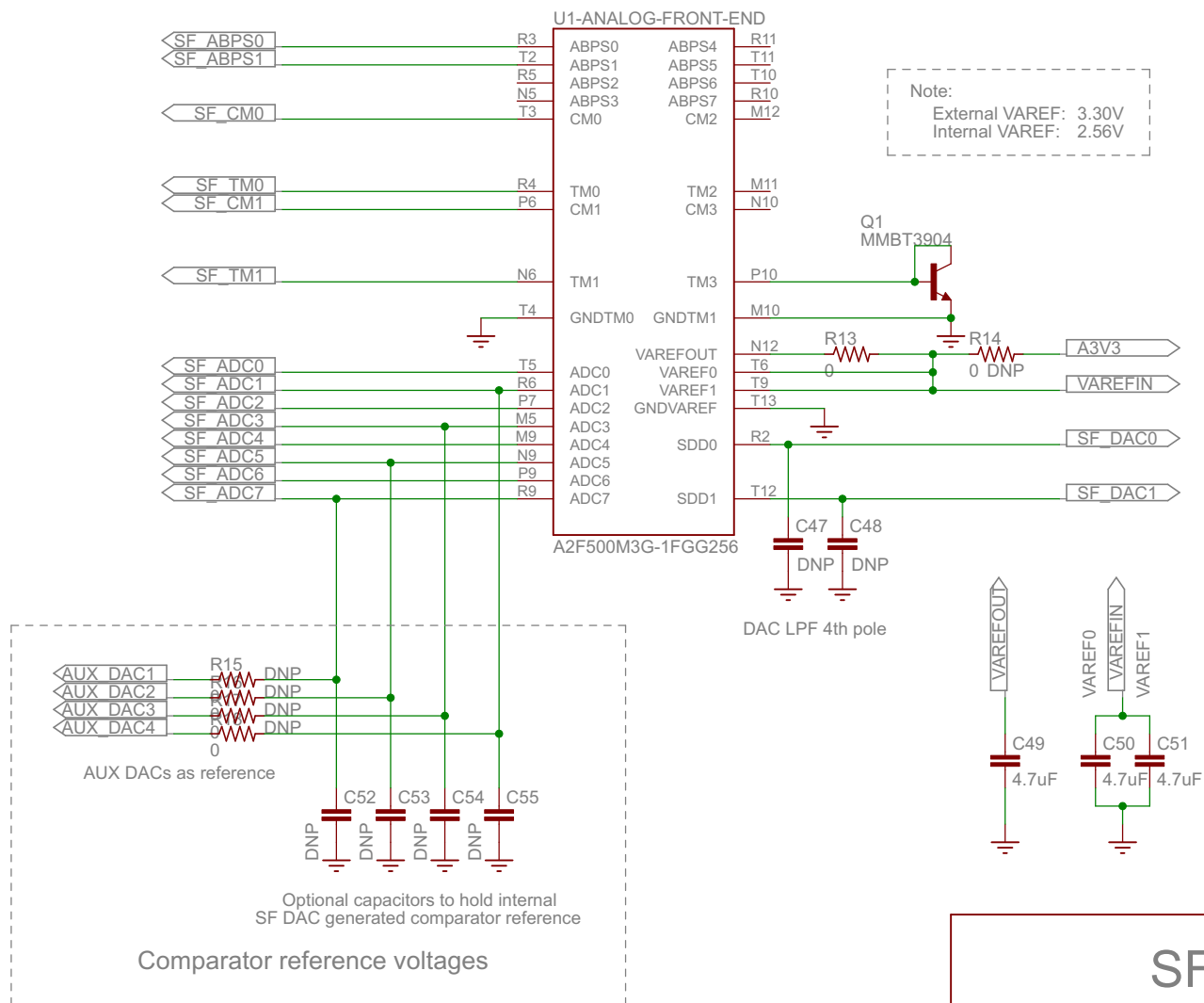


JTAG



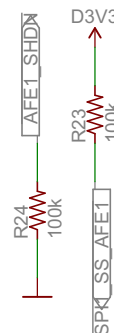
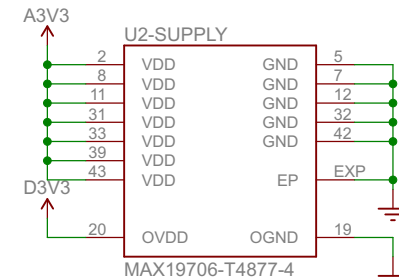
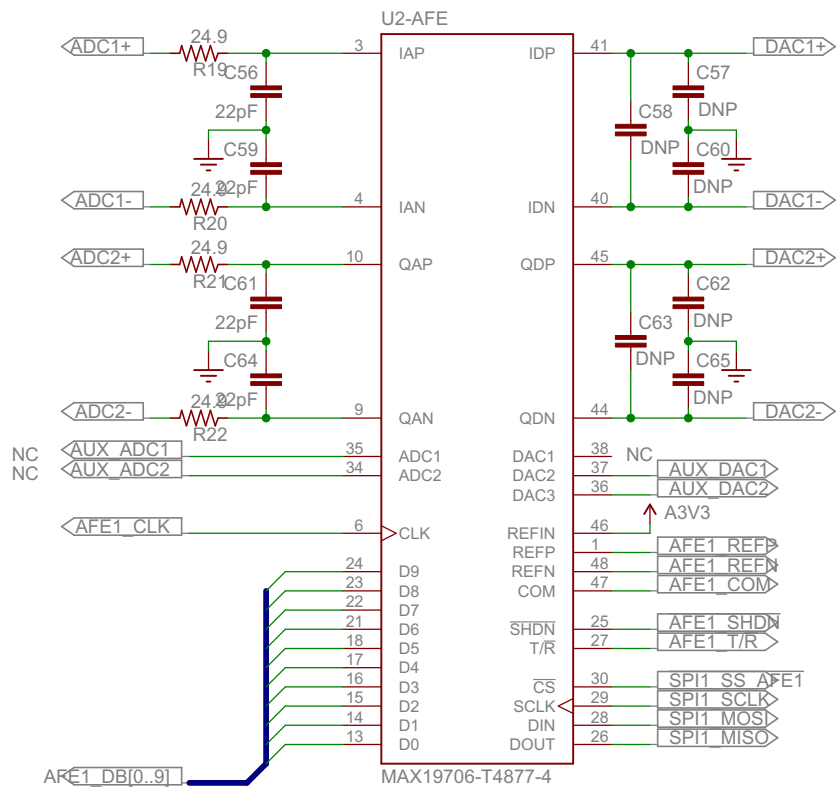
## Clock, reset, JTAG

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Date: 3/19/2012 10:31:08 AM	Sheet: 3/10
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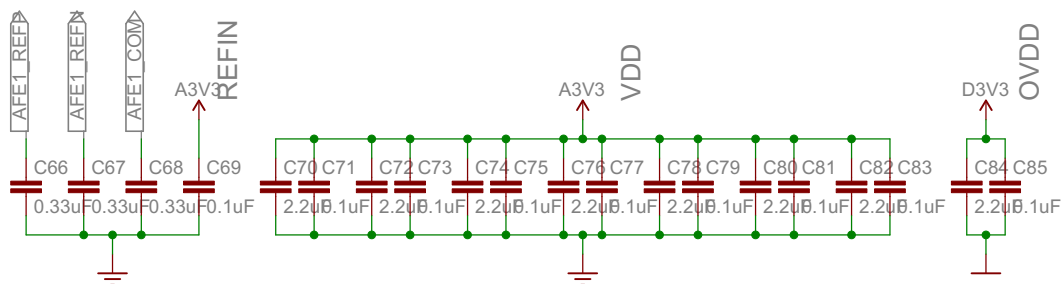
## SF AFE

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Date: 3/19/2012 10:31:08 AM	Sheet: 4/10
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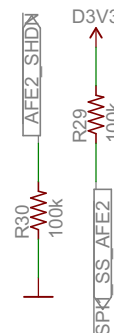
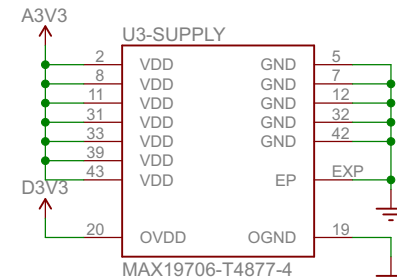
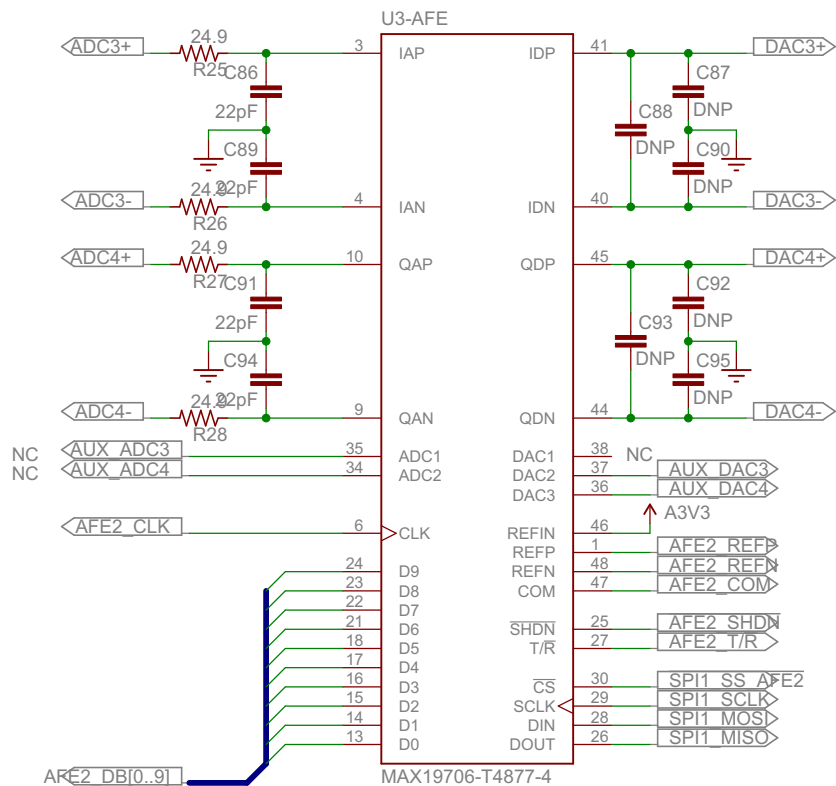
Note:

- AFEs are disabled by default
- AFE DB lines are pulled-up internally both in AFE and SmartFusion

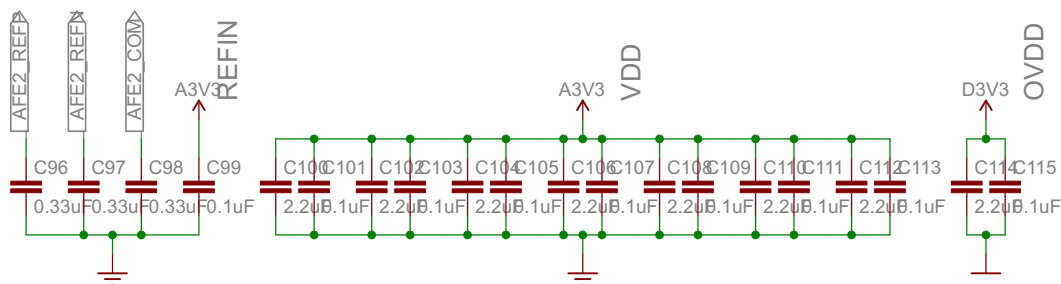


## External AFE #1

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Date: 3/19/2012 10:31:08 AM	Sheet: 5/10
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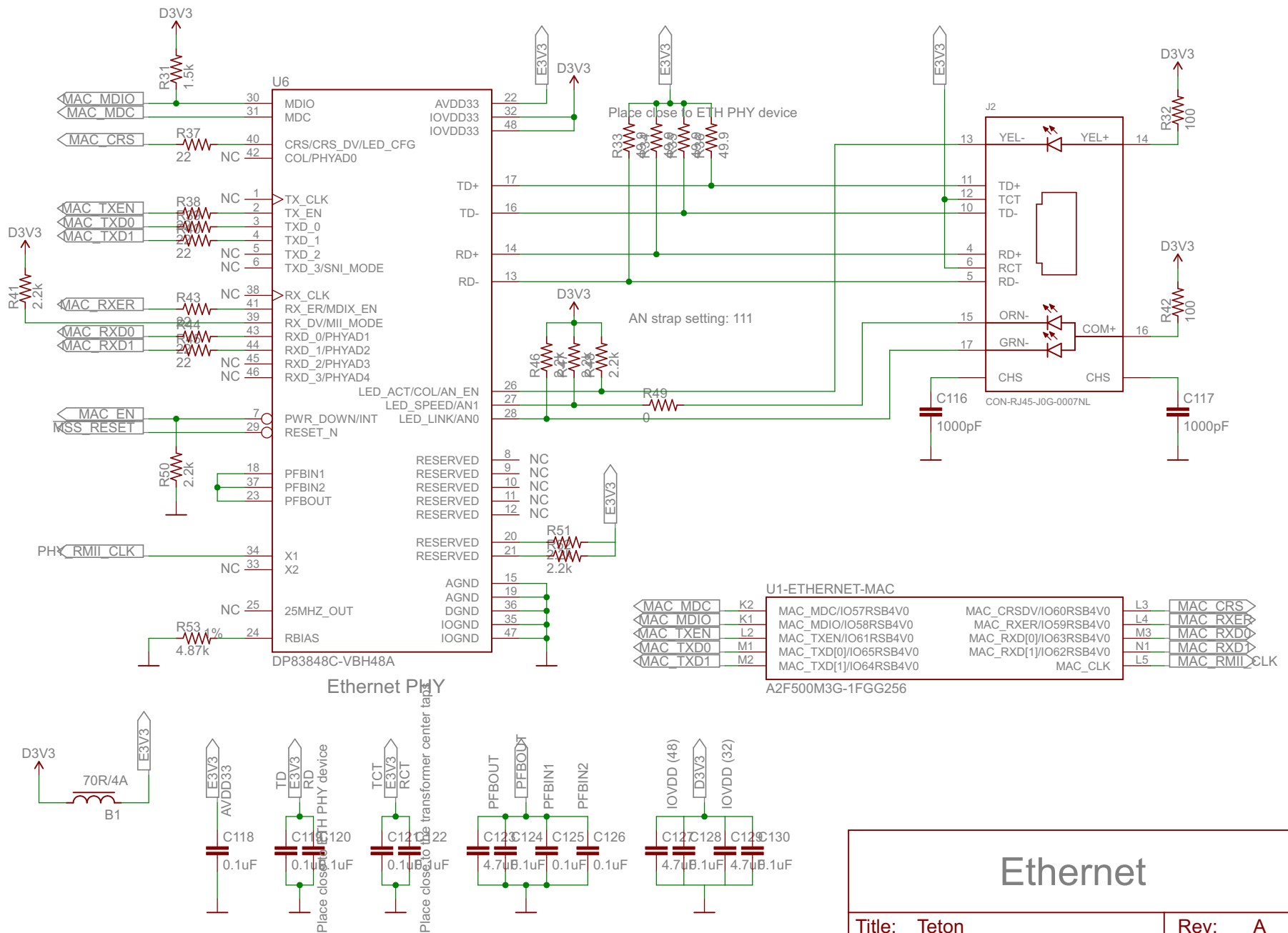


Note:  
 -AFEs are disabled by default  
 -AFE DB lines are pulled-up internally  
 both in AFE and SmartFusion



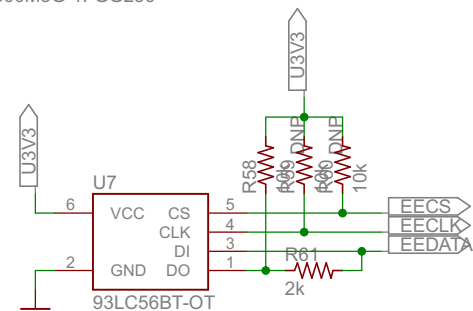
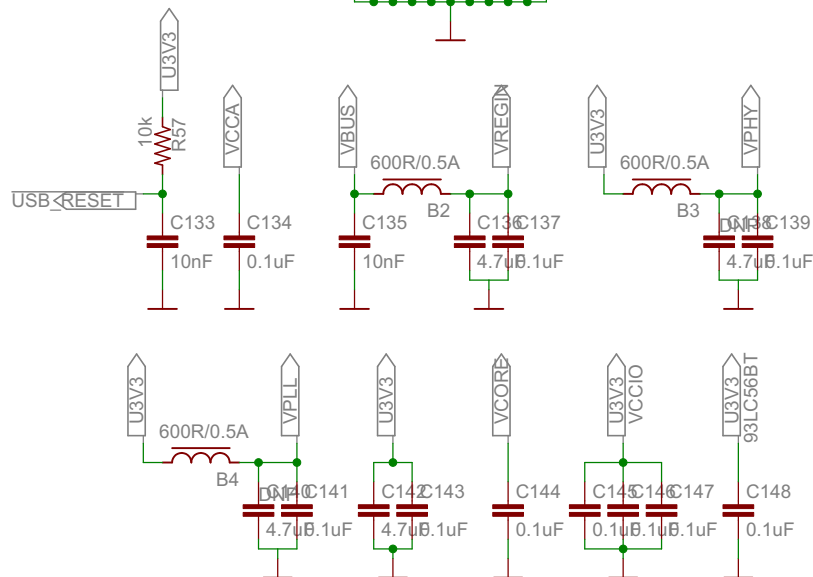
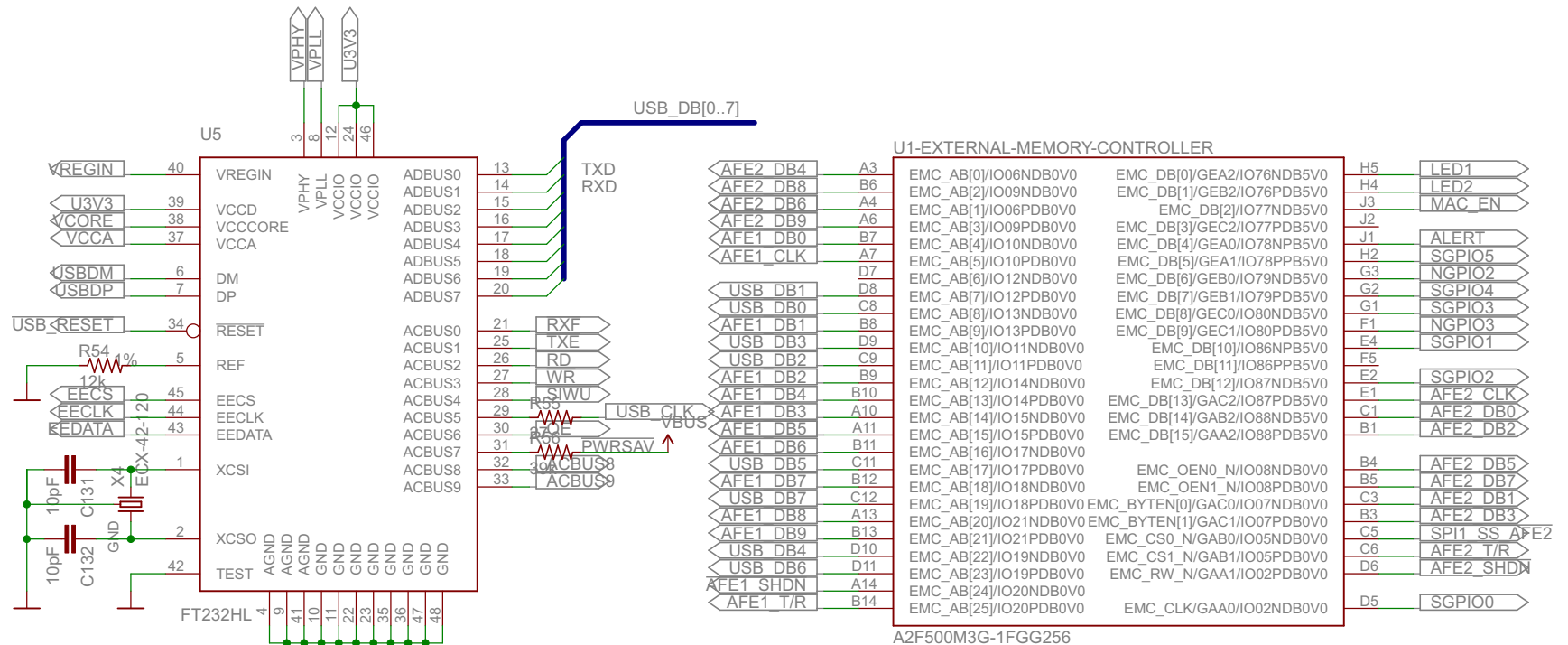
## External AFE #2

Title: Teton	Rev: A
Date: 3/19/2012 10:31:08 AM	Sheet: 6/10
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## Ethernet

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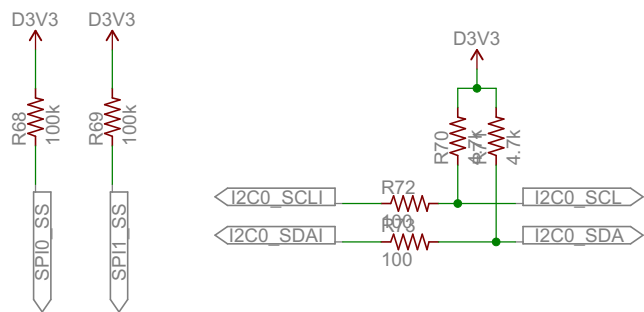
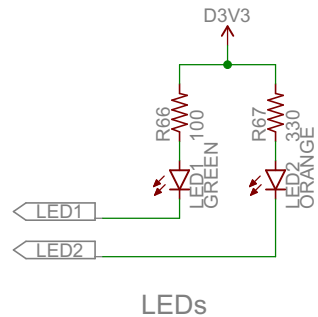
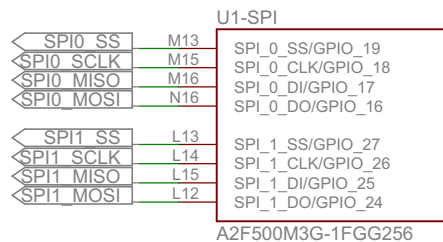
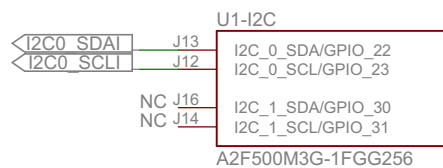
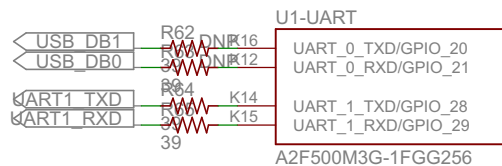


EEPROM

## USB and AFE dig.

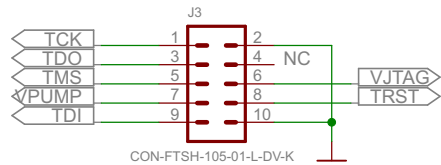
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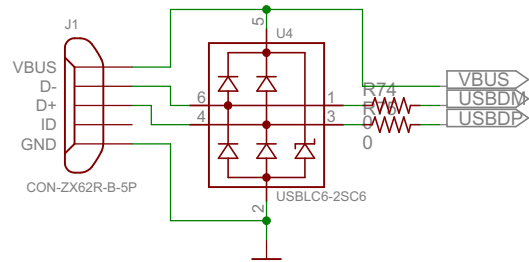


## I/O signals

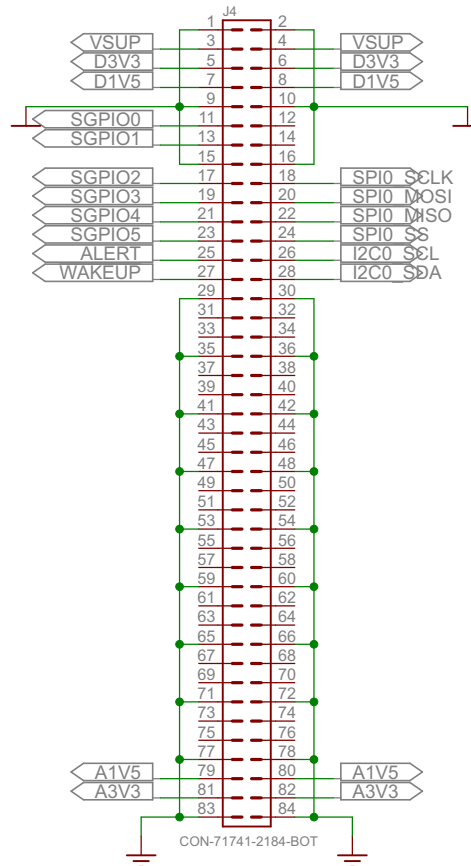
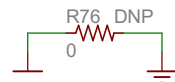
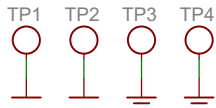
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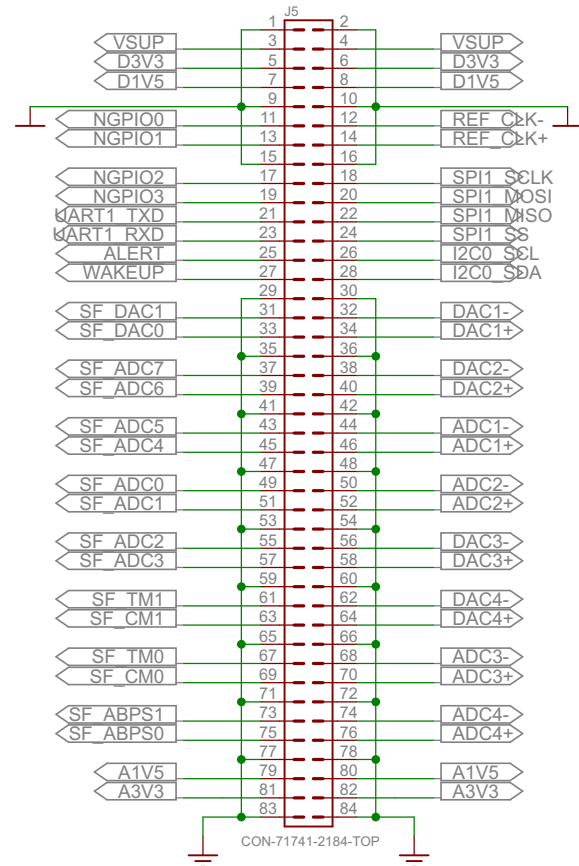
JTAG header



USB connector



South bridge



North bridge

## Connectors

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