



# P-Channel 20-V (D-S) MOSFET

MOSFET PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
- 20	0.039 at $V_{GS} = -4.5 \text{ V}$	- 6 <sup>e</sup>			
	0.050 at V <sub>GS</sub> = - 2.5 V	- 5.8	9 nC		
	0.063 at V <sub>GS</sub> = - 1.8 V	- 5.1			

### **FEATURES**

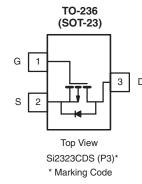
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

### **APPLICATIONS**

- Load Switch
- PA Switch
- DC/DC Converters



Ordering Information: Si2323CDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A =$	25 °C, unless oth	erwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		$V_{DS}$	- 20	V	
Gate-Source Voltage		$V_{GS}$	± 8	V	
	T <sub>C</sub> = 25 °C		- 6 <sup>e</sup>		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	- 5.2		
Continuous Diam Guitent (1) = 100 °C)	T <sub>A</sub> = 25 °C	טי	- 4.6 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		- 3.7 <sup>b, c</sup>	Α	
Pulsed Drain Current		I <sub>DM</sub>	- 20		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	la	- 2.1		
Continuous Source-Diam Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 1.0 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		2.5		
Mayimum Dawar Dissination	T <sub>C</sub> = 70 °C	D.	1.6	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.25 <sup>b, c</sup>	vv	
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	≤5 s	R <sub>thJA</sub>	75	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	40	50	]	

#### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 166 °C/W.
- e. Package limited.

## **Si2323CDS**

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MOSFET SPECIFICATIONS  Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	Oymboi	rest conditions		Typ.	IVIGA.	- Oilit	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>DS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 20			Ιv	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	D3 - 7 D 7		- 14		-	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		2.4		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 0.4		- 1	V	
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V			± 100	nA	
Zero Gate Voltage Drain Current	400	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1	иА	
	I <sub>DSS</sub>	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
	5(0.1)	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 4.6 A		0.032	0.039		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 4.1 A		0.041	0.050	Ω	
	20(0)	V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 3.6 A		0.050	0.063	1	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 4.6 A		2.0		S	
Dynamic <sup>b</sup>					l	1	
Input Capacitance	C <sub>iss</sub>			1090		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		155			
Reverse Transfer Capacitance	C <sub>rss</sub>			135			
Tabal Oaks Observe		V <sub>DS</sub> = - 10 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 4.6 A		16	25	nC	
Total Gate Charge	$Q_g$			9.3	15		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -10 \text{ V}, V_{GS} = -2.5 \text{ V}, I_{D} = -4.6 \text{ A}$		2.5			
Gate-Drain Charge	$Q_{gd}$			3.2			
Gate Resistance	$R_{g}$	f = 1 MHz	0.8	4.1	8.2	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			15	23		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 2.7 $\Omega$		23	35	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D = -3.7 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_G = 1 \Omega$		40	60		
Fall Time	t <sub>f</sub>			12	20	1	
<b>Drain-Source Body Diode Characteristi</b>	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 2.1	- A	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 20		
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = - 3.7 A		- 0.8	- 1.2	٧	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			30	45	ns	
Body Diode Reverse Recovery Charge	$Q_{rr}$	I <sub>F</sub> = - 3.7 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		20	40	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$_{1F} = -3.7 \text{ A}, \text{ ut/ut} = 100 \text{ A/}\mu\text{s}, \text{ I}_{J} = 25 ^{\circ}\text{C}$		17		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			13			

### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

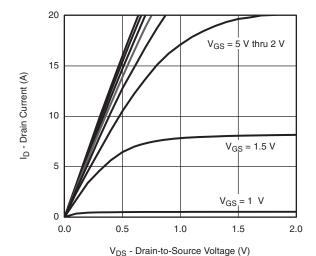
b. Guaranteed by design, not subject to production testing.



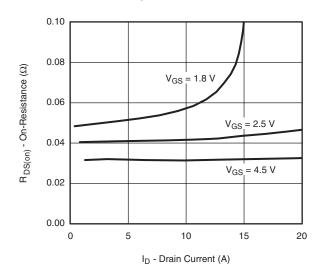




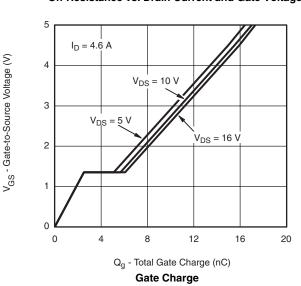
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

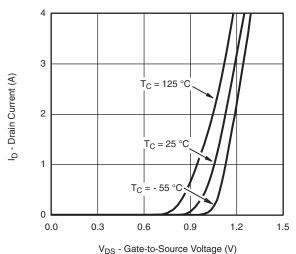


Output Characteristics

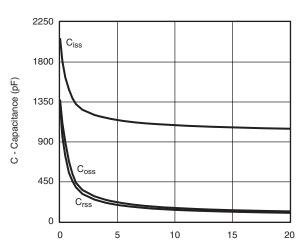


On-Resistance vs. Drain Current and Gate Voltage

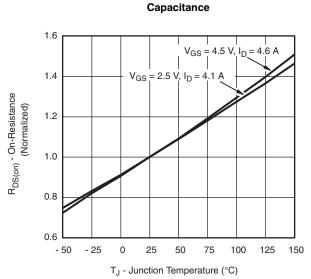




Transfer Characteristics



V<sub>DS</sub> - Drain-to-Source Voltage (V)

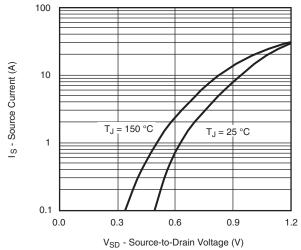


On-Resistance vs. Junction Temperature

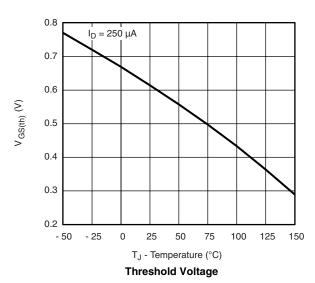
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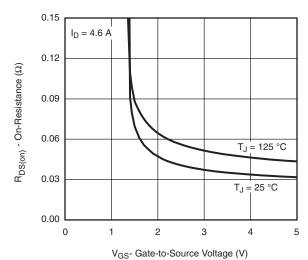
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

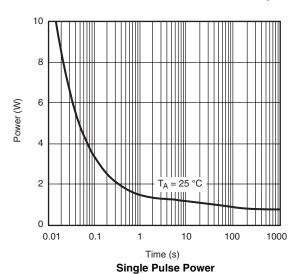


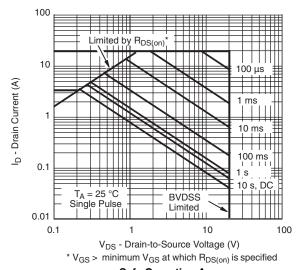
### Source-Drain Diode Forward Voltage





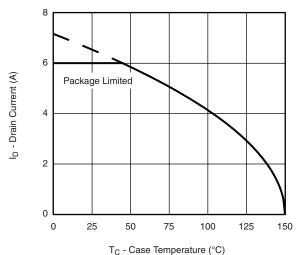
On-Resistance vs. Gate-to-Source Voltage



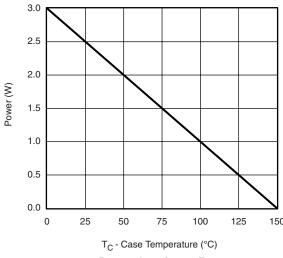


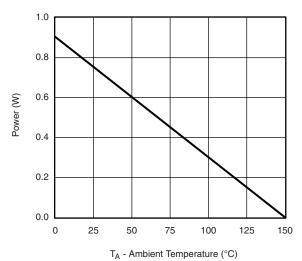


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Current Derating\*





Power, Junction-to-Foot

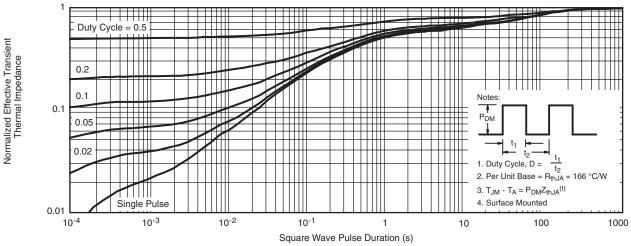
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

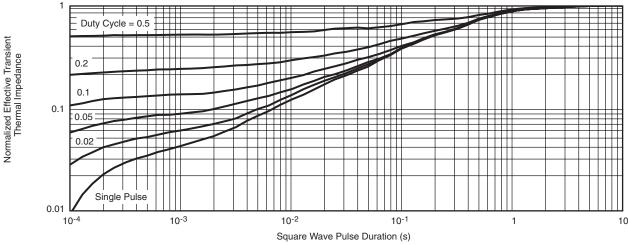
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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