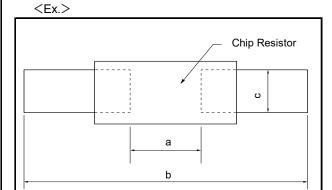


Recommended land pattern

• An example of a land pattern for the rectangular type is shown below.



High power (double-sided resistive elements structure) type

Part No.	Size	Dimensions			
Tartito.	(inch)	а	b	С	
ERJ2LW/2BW	1005 (0402)	0.52	1.4 to 1.6	0.4 to 0.6	
ERJ3LW/3BW	1608 (0603)	0.5 to 0.8	2.5 to 2.7	0.9 to 1.1	
ERJ6LW		0.6 to 0.8	3.2 to 3.8	1.1 to 1.4	
ERJ6BW		0.9	3.2 to 3.8	1.1 to 1.4	
ERJ6CW	2012	0.74- 0.0	0.0400	4 4 4 - 4 4	
(10 to 13 m Ω)	(0805)	0.7 to 0.9	3.2 to 3.8	1.1 to 1.4	
ERJ6CW		0.9 to 1.1	3.2 to 3.8	1.1 to 1.4	
(15 to 30 m Ω)		0.9 10 1.1	3.2 10 3.0	1.1 10 1.4	
ERJ8BW					
ERJ8CW		1.2	4.4 to 5.0	1.3 to 1.8	
(10 to 16 m Ω)	3216 (1206)				
ERJ8CW	(1200)	2.0 to 2.6	4.4 to 5.0	1.2 to 1.8	
(18 to 50 m Ω)		2.0 10 2.0	4.4 (0 5.0	1.2 (0 1.0	

High temperature type (ERJH)

High precision type (ERA)

Current sensing type (ERJ*L/B/C, ERJ*R, ERJL)

Small & high power type (ERJP, ERJT)

Anti-sulfurated type (ERJS, ERJU)

General purpose type (ERJ)

Wide terminal type (ERJA/B/Ds)

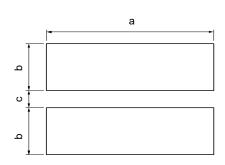
Unit : mm

Size	Dimensions				
mm/inch	а	b	С		
0402/01005	0.15 to 0.20	0.5 to 0.7	0.20 to 0.25		
0603/0201	0.3 to 0.4	0.8 to 0.9	0.25 to 0.35		
1005/0402	0.5 to 0.6	1.4 to 1.6	0.4 to 0.6		
1608/0603	0.7 to 0.9	2.0 to 2.2	0.8 to 1.0		
2012/0805	1.0 to 1.4	3.2 to 3.8	0.9 to 1.4		
3216/1206	2.0 to 2.4	4.4 to 5.0	1.2 to 1.8		
3225/1210	2.0 to 2.4	4.4 to 5.0	1.8 to 2.8		
4532/1812	3.3 to 3.7	5.7 to 6.5	2.3 to 3.5		
5025/2010	3.6 to 4.0	6.2 to 7.0	1.8 to 2.8		
6432/2512	5.0 to 5.4	7.6 to 8.6	2.3 to 3.5		
6432/2512*	3.6 to 4.0	7.6 to 8.6	2.3 to 3.5		

^{*} ERJL1W

• An example of a land pattern for high power chip resistors / Wide terminal type is shown below.

Unit : mm



Unit:mm					
Part No.	Dimensions				
i ait ivo.	а	b	С		
ERJA1	6.4	1.70	0.60		
ERJB1					
ERJC1 ^{*1}	5.0	1.30	0.75		
ERJD1*2					
ERJB2	3.2	0.95	0.70		
ERJD2*2	3.2	0.95	0.70		
ERJB3	2.0	0.80	0.60		

^{*1:} Anti-Sulfurated High power chip resistors / Wide terminal type

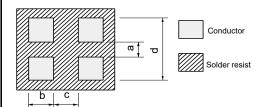
^{*2:} Low TCR High power chip resistors / Wide terminal type



Recommended land pattern

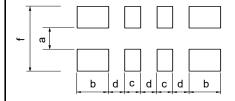
 An example of a land pattern for Chip Resistor Array, Anti-Sulfurated Chip Resistor Array and Chip Attenuator is shown below.

Jnit: mm



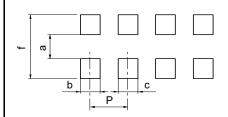
Part No.	Dimensions				
	а	b	С	d	
EXB14V EXB14A	0.30	0.30	0.30	0.80 to 0.90	
EXB24V EXBU24 EXB24A	0.5	0.35 to 0.40	0.30	1.4 to 1.5	

Unit : mm

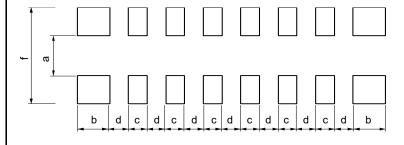


Part No.	Dimensions				
	а	b	С	d	f
EXB28V EXBU28	0.40	0.525	0.25	0.25	1.40
EXBN8V	0.45 to 0.50	0.35 to 0.38	0.25	0.25	1.40 to 2.00

Unit : mm



Part No.	Dimensions					
Part No.	а	b	С	f	Р	
EXB18V	0.20 to 0.30	0.15 to 0.20	0.15 to 0.20	0.80 to 0.90	0.40	
EXBV4V EXBV8V	0.7 to 0.9	0.4 to 0.45	0.4 to 0.45	2 to 2.4	0.80	
EXB34V EXB38V EXBU34 EXBU38	0.7 to 0.9	0.4 to 0.5	0.4 to 0.5	2.2 to 2.6	0.80	
EXBS8V	1 to 1.2	0.5 to 0.75	0.5 to 0.75	3.2 to 3.8	1.27	



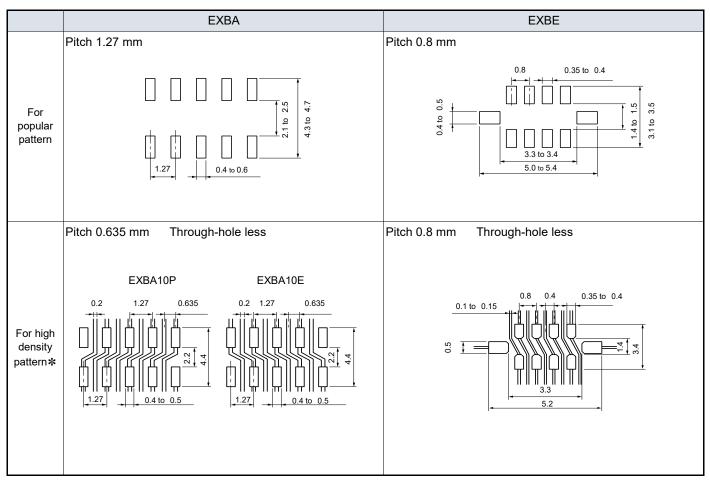
Unit : mm

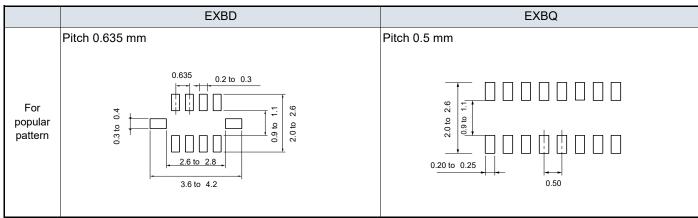
Part No.	Dimensions				
	а	b	С	d	f
EXB2HV EXBU2H	1.00	0.425	0.25	0.25	2.00



Recommended land pattern

• An example of a land pattern for Chip Resistor Networks is shown below.





* When designing high density land patterns, examine the reliability of isolation among the lines and adopt the chip resistor networks.