Lecturenotes in ttk 4145 Real-Time Programming spring 2015

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1 Course intro, starting the project, info on exercises

Feedback and questions from everybody welcome at any time!

We have students with different backgrounds here: If I use a term that you do not understand, please make me aware of this!

I am always eager to improve the lectures. Give me feedback!

- Quality of blackboard notes?
- More code examples?

Be also aware Sverres known weaknesses:

- Side Tracks
- Half sentences
- Unanswered questions.

Interrupt me if I am trailing off.

I am willing to put in extra effort. Walkthrough of old exam questions, disciussing project designs, "questions & answers" sessions before the exam etc. This happens on your initiative.

Anders' and Kyrres slides should be available at its learning. Introduction of project and exercises respectively.

Sverre talked about fault tolerance from the project perspective. Nothing that will not be repeated later in the course, but relevant as a preparation for hte project.

"There will be bugs in your project! Your system should work correctly even though there are bugs there!"

1.1 Practical Details

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Project is due week 17

Exam: Saturday 22/5 0900-1300

Non-kyb students must go to Unni to get access to lab.

Evaluation:

• Project 25%

• Exersises: Mandatory (approved on lab)

• Exam 75%

• No Midterm

1.2 Quality system

- Check https://innsida.ntnu.no/wiki/-/wiki/Norsk/Kvalitetssikring+av+utdanning
- We are having a questionaire to be answered by everybody.
- We are having a reference group (3+ meetings, pizza at the last one)

Quality System Action Points from 2014

- To motivate each lecture with relevant old exam questions
- Continually improve lecture notes.
- Try to set up a process for improvment of curriculum summary.
- Continually improve project execution (This year: make a better process on networking modules, change code quality evaluation?)

Note: Only students can enter items into quality system!



For reference: Action points from 2013 executed 2014:

- Lectures and exercises the first 4-5 weeks changed to support project startup better.
- Project milestones added.
- All (?) exercises reworked completely.
- Project slightly simplified.
- Curriculum somewhat reduced. Added curriculum on Code Quality, while reducing emphasis on a hard part: Asynch transfer of control.

1.3 Reference Group

Reference Group.

Erik Liland Mikal Edlund Patrik Levin Gunnar Homb Eirik Prestegårdshus Elektro

1.3.1 Its learning call for reference group members

I would very much like to have a representative for those who does not attend the lectures in the reference group, but you are a bit more difficult to recruit :-) Still you are a relevant part of the student group, and will be more dependent/critical on the quality of the other learning resources. Any voluntaries?

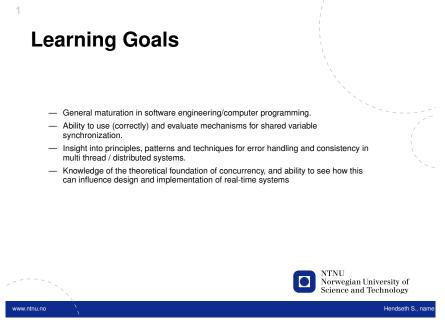
Other groups that could well be represented:

- Weak or low ambition students (What can we do for you, to increase outcome or reduce workload?)
- Girls

There will be pizza

Sverre

1.4 Learning Goals



Multicore important at the time: We **should** know how to do this! Will learn important basics here also.

Large parts of the industry does not know this - And need arise, as demands rise and multicore and wireless expands.

More detailed learning goals: Exist also for all parts of the course: Use them when preparing for the exam?

Question: What is the purpose of the course?

1.5 A special note on the programming languages.

1.5.1 The context of the project:

Each group selects freely the programming language for the project. Make the choice that makes the project easiest for you, or use the opportunity to learn a new language as you wish.

Three programming languages are "officially supported" by the student assistants on the lab:

- C (+ a reasonable subset of C++): This is a relevant programming language in the embedded setting, and most of you have some familiarity of it already.
- Python: Not so much an embedded language, but many of you know it, and the "fast and easy results" profile of python might be attractive. Who knows; maybe making network connections in python are simpler than in C? maybe there exist ready made transactional frameworks, distributed data managers, fault tolerance frameworks etc. that you can use?
- Go: The focus on Go at the lab stems from its support of the "communicating processes" way of designing paralell/concurrent software. If you design your system in terms of "servers" passing messages to each other, then Go is a nice choice.

If you choose another language we will still support you as best we can, but your ability to solve your own problems might be stressed slightly more. Google is your friend.

1.5.2 The context of the curriculum, lectures and the exam.

The standard design pattern in real-time systems is to distribute the responisbilities of the different timing demands to different threads. These threads then share resources (memory, hw, modules, libraries) and must **synchronize** their access to them.

Synchronization can be achieved by a large number of synchronization primitives, all with strengths and weaknesses, and it is a central goal of the course to learn to use and evaluate these.

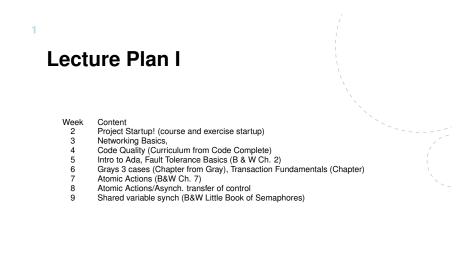
In addition to primitives of historical interest (suspend and resume, events,...) and unimplemented suggestions (conditional critical regions, the generic monitor) we are studying the synchronization primitives of C/POSIX, Java and Ada since these are particularly interesting and well thought through.

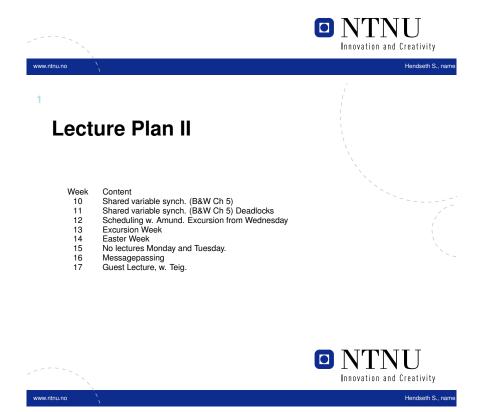
- For C you should know of the mutex and its interaction with the condition variables. (+thread canceling and signals)
- For Java; synchronized object methods with the calls to wait(), notify() notifyAll() and interrupt(). For RT Java; that asynch transfer of control (ATC) is integrated into the exceptionhandling mechanism.
- For Ada; On one hand the protected object (with functions, procedures and entries with guards, requeue). ATC is here integrated with the select mechanism.

The languages is not important in themselves. You should be able to read example code in each of them though. On the exam, if asked to write pseudo code in language X, it is not important that the code looks like X code, but the relevant synchronization primitives should be used correctly.

1.6 Lecture Plan, Real-Time Programming 2015

This will change, during the semester.





There is one week of teaching missing here - week 18 that compensates for the lost excursion week. Hopefully we can use the spare exercise lectures during the semester to avoid lectures and exercise in this week.

1.7 Project Q&A

We talked a lot about the project... If anyone made notes I'll happily include them here.

2 Networking basics

Hello, all

This week we will continue making the foundation for the project. Monday morning (I hope to see you all; give this week a good start :-)) Mladen Skelin will start giving his great "introduction to networking" lectures, giving you the necessary background for making the project communication work.

The network communication has traditionally been a stumbling block for many groups in the project, and I sincerely recommend attending.

Tuesday Mladen will keep his third and last lecture on this topic, and Anders will continue with one hour of "language speed-dating", giving you the foundation for making a sensible choice of programming language for the project.

Sverre

See Mladens slides at its learning.

3 Code Quality

3.1 Its Learning Call

The theme for tomorrow will be code quality. Not only do we go through what is expected of you in the project (1/3) of project evaluation is on code quality), and how to answer code quality related exam questions. Code quality is also the criteria for evaluating tools, language mechanisms, techniques, designs etc. in all parts of later lectures: Why are global variables bad? What are the downsides to using semaphores? Which is the best programming language for shared variable synchronization? When is shared variables better than message passing? - It all boils down to what yields the higher code quality.

Note that I have uploaded some chapters of a fabulous book: Code Complete for supporting the lectures tomorrow. Chapter 6: Skip the things about

inheritance, (read only 6.2 "Good class interfaces" and do a mental search-and-replace for "class" with "module")

If you want to prepare for tomorrow; Each chapter has some checklists and "key points" summary; look through them.

Finishhing the Code Quality section we will continue on basic fault tolerance.

The Real-time programing lectures monday morning is your opportunity to give your week a good start !! :-) See you!

Sverre

3.2 Learning goals

Learning goals; Code Quality

- Be able to write software following selected Code Complete Checklists for modules, functions, variables and comments
- Be able to critizise program code based on the same checklists



3.3 Motivational Questions

Exam question on Code Quality from Continuation 2014

As the program grows it turns out that the combination of printName() and printAddress() occurs many places in the program (when printing name tags, letterheads, business cards, envelopes,...), and we decide to make the function *printNameAndAddress* as a part of the module:

```
void printNameAndAddress(int i){
   sem_wait(personSem);
   printName(i);
   printAddress(i);
   sem_signal(personSem);
}
```

Look at the module interface; What do you think about this decision in a code quality perspective?



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Exam question on Code Quality from Continuation 2014

(2x) A slightly larger part of our module is shown here:

```
#ifndef PERSON_H
#define PERSON_H

typedef struct {
   char * firstName;
   char * lastName;
   char * street;
   int streetNumber;
} TPerson;

void reallocateArray(int newSize);

TPerson ** getArray();

void printName(int personNumber);
void printAddress(int personNumber);
void printNameAndAddress(int personNumber);
...
#endif
```

Criticize, from a code quality perspective, the inclusion of the type functions reallocateArray() and getArray()



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Exam question on Code Quality from Continuation 2014

In the listing under is a module interface (a C header file) from an old project deliverable. The

```
modules name is "cost". Criticize (concisely, with bullet points) the design.

#ifndef lift_cost_h
#define lift_cost_h
int calculateCost(int currentFloor, int direction, int orderedFloor, int orderedDirection);
int downCost[MAX_ELEVATORS][N_FLOORS];
int upCost[MAX_ELEVATORS][N_FLOORS];

void fillCostArrays();
void clearCosts(void);
int lowestCostFloor(int elevator);
int lowestCostFloor(int elevator);
int findBestElevator(int floor, int direction);

void designateElevators();
void clearDesignatedElevator();

int designatedElevator[N_FLOORS][2];

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#endif

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```

3.4 Intro SW Quality

"Reflection on SW quality is the answer to all exam questions:

- What is good and what is bad? What are strengths and weaknesses.
- Discuss goto, global variables, exceptions, semaphores, RT in general...

Brainstorms on:

- Why SW quality? (what are the goals?)
- What is SW quality?
- How do we achieve SW quality?

Sverres ultimate software quality metric: Maintainability.

• Sooner or later a system gets too large to maintain; delay this by increasing maintainability.

- Writing new SW is more fun than maintenance. (keep the maintenance ratio low) (Rule of thumb: Maintenance effort constitutes 20% of total development effort.)
- If we have maintainability then fixing bugs or adding features gets simple. (per definision)
- Law of nature: The quality of a program decreases over time.
- Maintenance cost increases more than linearly on project size.
- Measure maintainability expected effort to fix the next bug.

Also discuss: Code should do what is expected of it: The maintainers expectations must be made consistent with what the code in fact does. In an imperative language this is about goals and plans - steps to achieve the goals. "Causality".

How?

- 1. Modules!
- 2. Choose (Use or build) the right abstraction! The plan the steps should be concisely described. (Creative work, this! Choose the words to think with)
- 3. "Good design"?
- 4. "Intuition/Experience"?
- 5. Design Patterns -> Common Culture in project group.
- 6. Tools that generate code (or documentation)
- 7. Documentation. in appropriate amounts

... so if something supports these, then it is good.

Example: Does our languages let us express our timing demands concisely? Bad abstraction?

Ref. Chapter 8, Blooms criteria ("Expressive power and ease of use")

A module:

- A group of lines, a function, a file, an object, group of files, package, library, etc.
- You must be able to:
 - Maintain a module without knowing the rest of the system.
 - Use a module without knowing its internals.
- Composition: Building supermodules from submodules provides scalability!

3.5 Modules

3.5.1 Modules, Intro

- You must be able to use a module without knowing its internals.
- You must be able to maintain the module without knowing the rest of the system.
- Composition: You should be able to build supermodules from submodules.
- Coupling: Weak coupling between modules.
- Cohesion: The parts of a module should be well connected
- Interfaces should be complete and minimal.

3.5.2 The checklists

Module Interfaces Checklist: Encapsulation

- Does the module minimize accessibility to its data members?
- Does the module avoid exposing member data?
- Does the module hide its implementation details from other modules as much as the programming language permits?
- Does the module avoid making assumptions about its users, (including its derived classes?)
- Is the module independent of other modules? Is it loosely coupled?



Module Interfaces Checklist: Other Implementation Issues

- Does the module contain about seven data members or fewer?
- Does the module minimize direct and indirect routine calls to other modules?
- Does the module collaborate with other modules only to the extent absolutely necessary?
- Is all member data initialized appropriately?



Module Interfaces Checklist: Language-Specific Issues

— Have you investigated the language-specific issues for modules in your specific programming language?



3.5.3 Key Points on modules

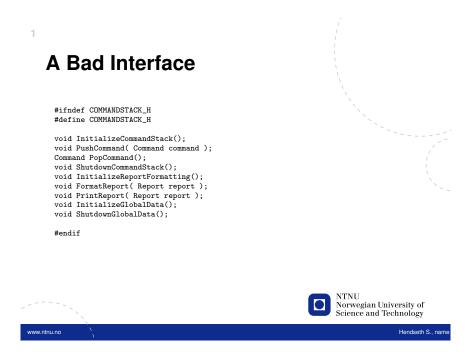
Module Interfaces (classes, chapter 6.2) - Key Points

- Class interfaces should provide a consistent abstraction.
- (An abstraction is the ability to view a complex operation in a simplified form. A module interface provides an abstraction of the implementation that is hidden behind the interface)
- (A module interface should hide something)
- Modules are your primary tool for managing complexity. Give their design as much attention as needed to accomplish that objective.
- Sverre: Building/choosing abstractions is the secondmost important tool!



- What is hidden by a get-function? Get-functions hurts abstraction!
- Think about abstraction also internally in the "module".

3.5.4 Bad Example: CommandStack



3.5.5 Bad Example2: CommandStack



(This inherited list...)

3.5.6 Other tips

Other Tips

- Interfaces should be complete and minimal
- Services often comes in pairs: put/get, start/stop, init/shutdown, on/off...
- If your interface ends up not a great abstraction: Redesign!
- Make assumptions explicit in the code ("programmatic" rather than "semantic")
- Beware of erosion of the abstraction over time (ref pt 3)



3.6 HighQuality Routines

3.6.1 HighQuality Routines

High-Quality Routines (Chapter 7)

- Good, describing, name
- Does one thing and does it well
- Readable; It is easy to see what it does.
- You can say that it works correctly only by reading it
- It is defensive; protects itself from bad usage and bad parameters (assumptions are programmatic).
- Uses all parameters and variables (but not reuse for other purpose)
- Less than seven parameters
- High cohesion



3.6.2 Routine Size

Routine Size

- How large can it be? (...to not represent a maintenance problem in itself...)
- How small can it be? (...and still represent useful abstraction, encapsulate something worth encapsulating, ...)



3.6.3 Routine Name

A good routine name

- describes all a routine does
- is spesific (avoid "handle", "perform", "do", "process" etc.)
- A function: describe the return value
- A procedure: A verb and object
- Standardize naming and abbreviations

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3.6.4 Reasons to Create a Routine

Reasons to Create a Routine

- Encapsulate/hide something
- Introduce abstraction
- Reduce complexity
- Avoid duplicate code
- Support subclassing
- Hiding sequences
- Hide pointer operations
- Improve portability
- simplify tests
- Improve performance
- Etc etc...



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3.6.5 Other tips

Other Tips

- Standardize parameter orders.
- Standardize naming conventions.



3.6.6 Checklists

Big-Picture Issues

- Is the reason for creating the routine sufficient?
- Have all parts of the routine that would benefit from being put into rou- tines of their own been put into routines of their own?
- Is the routine's name a strong, clear verb-plus-object name for a procedure or a description of the return value for a function?
- Does the routine's name describe everything the routine does?
- Have you established naming conventions for common operations?
- Does the routine have strong, functional cohesion-doing one and only one thing and doing it well?
- Do the routines have loose coupling-are the routine's connections to other routines small, intimate, visible, and flexible?
- Is the length of the routine determined naturally by its function and logic, rather than by an artificial coding standard?



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Parameter-Passing Issues

- Does the routine's parameter list, taken as a whole, present a consistent interface abstraction?
- Are the routine's parameters in a sensible order, including matching the order of parameters in similar routines?
- Are interface assumptions documented?
- Does the routine have seven or fewer parameters?
- Is each input parameter used?
- Is each output parameter used?
- Does the routine avoid using input parameters as working variables?
- If the routine is a function, does it return a valid value under all possible circumstances?



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3.6.7 Key Points

Hig-Quality routines; Key Points

- The most important reason for creating a routine is to improve the intellectual manageability of a program, and you can create a routine for many other good reasons. Saving space is a minor reason; improved readability, reliability, and modifiability are better reasons.
- Sometimes the operation that most benefits from being put into a routine of its own is a simple one.
- You can classify routines into various kinds of cohesion, but you can make most routines functionally cohesive, which is best.
- The name of a routine is an indication of its quality. If the name is bad and it's accurate, the routine might be poorly designed. If the name is bad and it's inac- curate, it's not telling you what the program does. Either way, a bad name means that the program needs to be changed.
- Functions should be used only when the primary purpose of the function is to return the specific value described by the function's name.

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- Careful programmers use macro routines with care আপতা তারি আছা আ

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3.7 The power of variable names

A good variable name

- describing and specific
- (Can be shorter if scope is small)
- indicate type and/or scope by naming convention?
- standardize abreviations
- Prefix enums with enum type?
- Differ between names for variables, routines(), TTypes, CONSTANTS, pPointers, etc...



General Naming Considerations

- Does the name fully and accurately describe what the variable represents?
- Does the name refer to the real-world problem rather than to the program- ming-language solution?
- Is the name long enough that you don't have to puzzle it out?
- Are computed-value qualifiers, if any, at the end of the name?
- Does the name use Count or Index instead of Num?



Naming Specific Kinds of Data

- Are loop index names meaningful (something other than i, j, or k if the loop is more than one or two lines long or is nested)?
- Have all "temporary" variables been renamed to something more mean- ingful?
- Are boolean variables named so that their meanings when they're true are clear?
- Do enumerated-type names include a prefix or suffix that indicates the category - for example, Color_for Color_Red, Color_Green, Color_Blue, and so on?
- Are named constants named for the abstract entities they represent rather than the numbers they refer to?



Naming Conventions

- Does the convention distinguish among local, class, and global data?
- Does the convention distinguish among type names, named constants, enumerated types, and variables?
- Does the convention identify input-only parameters to routines in languages that don't enforce them?
- Is the convention as compatible as possible with standard conventions for the language?
- Are names formatted for readability?



Short Names

- Does the code use long names (unless it's necessary to use short ones)?
- Does the code avoid abbreviations that save only one character?
- Are all words abbreviated consistently?
- Are the names pronounceable?
- Are names that could be misread or mispronounced avoided?
- Are short names documented in translation tables?



Common Naming Problems: Have You Avoided...

- ...names that are misleading?
- ...names with similar meanings?
- ...names that are different by only one or two characters?
- ...names that sound similar?
- ...names that use numerals?
- ...names intentionally misspelled to make them shorter?
- ...names that are commonly misspelled in English?
- ...names that conflict with standard library routine names or with predefined variable names?
- ...totally arbitrary names?
- ...hard-to-read characters?



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TPO Variable Names: Key Points

- Good variable names are a key element of program readability.
 Specific kinds of variables such as loop indexes and status variables require specific considerations.
- Names should be as specific as possible. Names that are vague_ enough or gen- eral enough to be used for more than one purpose are usually bad names.
- Naming conventions distinguish among local, class, and global data.
 They dis- tinguish among type names, named constants, enumerated types, and variables.
- Regardless of the kind of project you're working on, you should adopt a variable naming convention. The kind of convention you adopt depends on the size of your program and the number of people working on it.
- Abbreviations are rarely needed with modern programming languages. If you do use abbreviations, keep track of abbreviations in a project dictionary or use the standardized approach sity of
- Code is read far more times than it is written. De sure that The relatives

you about favor road time convenience ever write time

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3.8 Self-Documenting Code

Self-Documenting Code

- Rule 1: If something is not clear from the code: Improve Code!
- Choose better names, divide complex statements, move complex code into well-named functions; can you use layout or whitespace?
- Rule 2: If the code is still not clear: The code has a weakness! Improve it!
- Comments add to the size of the project comments must be maintained also.
- Use comments as headlines, to emphasize structure, give summaries or describe intent
- Make comments easy to maintain



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General

- Can someone pick up the code and immediately start to understand it?
- Do comments explain the code's intent or summarize what the code does, rather than just repeating the code?
- Is the Pseudocode Programming Process used to reduce commenting time?
- Has tricky code been rewritten rather than commented?
- Are comments up to date?
- Are comments clear and correct?
- Does the commenting style allow comments to be easily modified?



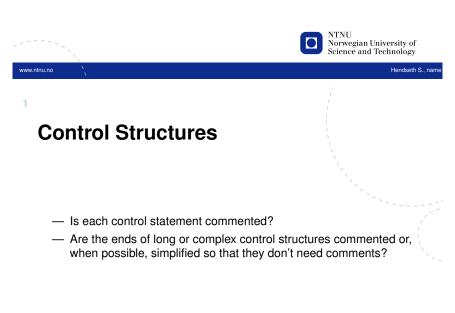
Statements and Paragraphs

- Does the code avoid endline comments?
- Do comments focus on why rather than how?
- Do comments prepare the reader for the code to follow?
- Does every comment count? Have redundant, extraneous, and self-indul- gent comments been removed or improved?
- Are surprises documented?
- Have abbreviations been avoided?
- Is the distinction between major and minor comments clear?
- Is code that works around an error or undocumented feature commented?



Data Declarations

- Are units on data declarations commented?
- Are the ranges of values on numeric data commented?
- Are coded meanings commented?
- Are limitations on input data commented?
- Are flags documented to the bit level?
- Has each global variable been commented where it is declared?
- Has each global variable been identified as such at each usage, by a naming convention, a comment, or both?
- Are magic numbers replaced with named constants or variables rather than just documented?



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Routines

- Is the purpose of each routine commented?
- Are other facts about each routine given in comments, when relevant, including input and output data, interface assumptions, limitations, error corrections, global effects, and sources of algorithms?



Files, Classes, and Programs

- Does the program have a short document, such as that described in the Book Paradigm, that gives an overall view of how the program is organized?
- Is the purpose of each file described?
- Are the author's name, e-mail address, and phone number in the listing?



Self-Documenting Code: Key Points

- The question of whether to comment is a legitimate one. Done poorly, commenting is a waste of time and sometimes harmful. Done well, commenting is worthwhile.
- The source code should contain most of the critical information about the program. As long as the program is running, the source code is more likely than any other resource to be kept current, and it's useful to have important information bundled with the code.
- Good code is its own best documentation. If the code is bad enough to require extensive comments, try first to improve the code so that it doesn't need extensive comments.
- Comments should say things about the code that the code can't say about itself - at the summary level or the intent level.
- Some commenting styles require a lot of tedious clerical work.
 Develop a style that's easy to maintain.



3.9 Answer to motivational questions

Answer to exam question on Code Quality from Continuation 2014

The interface is not minimal any more (which is a bad thing). Continuing this trend will lead to code duplication in the module. (More obscurely: We get dependencies between functions in the module which increases module complexity). But of course; we sometimes do make convenience functions, if the convenience is great enough:-)



Answer to exam question on Code Quality from Continuation 2014

- The type should not be a part of the module interface breaks the encapsulation!
- returning the list in getArray() is *really* terrible! breaks the encapsulation!
- reallocateArray() is inconsistent abstraction and breaks encapsulation revealing more
 of the implementation than should be necessary.



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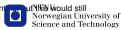
Answer to exam question on Code Quality from Continuation 2014

Any reasonable comment the student does should be rewarded. However, the "ideal solution" argues along the lines of the Code Complete checklists.

The main issue with this module is that this is not a module interface! It is (probably) a list of all functions in the module. including the variables!

- Ideally it should be clear from the interface exactly what the responsibility of the module is, and how it should be used correctly. It is not.
- There are (probably) nonobvious dependencies between the functions in the interface. (When do you need to call clearCosts for example?)
- The interface is not minimal. The functionalities of calculateCost, findBestElevator, lowestCostFloor and lowestCostDirection is overlapping. Also probably clearCosts and fillCostArrays.
- The abstraction is not consistent. The name "Cost" indicates that the module calculates or manages costs in some way. Either calculateCost or findBestElevator must be the main purpose of the module? But then there is the manipulation of some "costArrays" in addition and keeping track of a "designatedElevator"?
- The data members are not encapsulated.
- (Having non-external variables in the headerfile like this is a bug, in addition to the fact that exporting the module's data is bad form)

Possibly some of these thinks could have been mitigated by commer be a badly designed module interface.



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4 Ada

Kristoffer Gregertsen kept a 2 hour introduction to the ADA programming language.

4.1 Its learning call

Monday 26/1 Kristoffer Gregertsen will give an introduction to the Ada programming language. (I have guest lectures in another course at this time).

Ada is a large and carefully designed language, well suited for embedded software and some of the choices made on how to do timing and thread interaction are very interesting (and relevant for the exam :-) Check e.g. 1b, 1c, 1d, 1e from the main 2014 exam or 1j in the 2014 continuation exam). These mechanisms will be discussed explicitly in later lectures, but building familiarity with the language, enough for you to understand short pieces of Ada code, and to write small Ada programs (or "Ada pseudo code" on the exam) happens tomorrow.

Two of the exercises will be using Ada.

Do not miss the opportunity to give also this week a good start!

I will see you Tuesday, with the promised introduction to "Basic Fault Tolerance".

Sverre

5 Fault Tolerance Basics

5.1 Its Learning Call

Lecture Tomorrow

We start tomorrow at the proper walkthough of the fault tolerance part of the course. This is the part of the course that the project is primarily ment to challenge.

Think about this for tomorrow: Is it even possible to make software handle the unexpected errors that exist in it - the bugs that you assume is not there? How can this be done?

Btw. I put out the plan for the semester.

5.2 Learning goals

Learning goals; Fault Tolerance Basics

- Understand and use terms (like): Reliability. Failure vs fault vs error.
 Failure modes. Acceptance test. Fault prevention vs. tolerance.
 Redundancy, Static vs. Dynamic. Forward/ Backward error recovery.
- Understand, use and evaluate techniques (like): N-version programming. Recovery blocks. Error detection. Failure mode merging. Acceptance tests.



5.3 Intro: Classic bug handling

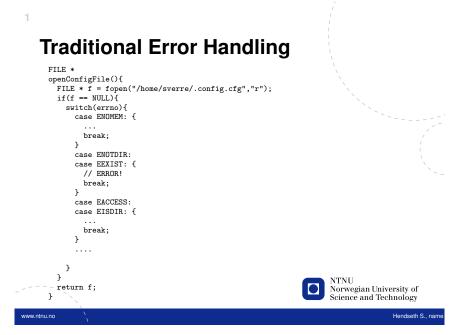
Bugs:

- Do all systems have them?
- Yes...
- "1bug/50 lines before testing"
- "1/500 at release"
- "1/550 after a year, and then constant"

The "normal" way of handling bugs:

- Make your program to fill the (functional) specification
- Run/Test the program
- \bullet Errors happen

- Find "cause" in code
 - erroneous code
 - missing handling of some situation
 - incomplete spec
- Add/Change Code: Fix code or Detect/Handle situation



This is how it sometimes ends up; One line of readable and intuitive code grows into something not signaling clearly the intent or causality of the code at all.

Are we adding these case's during debuging? Then we would never be certain that we handle all cases... And how do we maintain and test this code... (add even more code for error injection?)

Are we adding the case's systematicly from the list of what can happen?

Failure modes of fopen() call from clib:

On error NULL is returned, and the global variable errno is set: EINVAL The mode provided to fopen was invalid. The fopen function may also fail and set errno for any of the errors specified for the routine malloc(3). (ENOMEM) The fopen function may also fail and set errno for any of the errors specified for the routine open(2). (EEXIST, EISDIR, EACCES, ENAMETOOLONG, ENOENT, ENOTDIR, ENXIO, ENODEV, EROFS, ETXTBSY, EFAULT, ELOOP, ENOSPC, ENOMEM, EMFILE, ENFILE)



Many of these can not even happen... How to test?

Term: Failure modes:

- In which manners can the system fail?
- Which situations do we have to handle?

Failure modes example

This is practical, project-relevant:

• What are the failuremodes of a TCP connection?, UDP?, Message Queues?...

Example slide: The failure modes of fopen (See the nerdy joke here?) Welcome to the dark side!

- Maintainability & modules: Use without knowing internals?
- failure modes destroys module boundaries!!!

(Also Java: "which exceptions can this method throw list")

Testing is not good enough

- Can only show existence of errors
- Cannot find errors in spec.
- Is realistic testing possible?
- Cannot find race conditions
- World interface: can realistic testing be done (must simulate world...)
- Embedded systems may have higher demands of safety

Error handling destroys readability of code? Even worse: The error modes destroys a module's encapsulation: If the user of a module needs to know all the ways a module can fail, he cannot use the module without knowledge of its internals.

5.3.1 Causes of errors

"Unhandled situations" much more common than "software bugs". A software specification does seldomly handle all the unexpected situations that should be handled, and never (?) distinguishes between situations that should be handled and those that allows the program to fail.

5.3.2 This is not good enough!

Anyway: This is not good enough!

- We must handle all errors also the unexpected ones
- Since more threads are cooperating in the system, then sometimes they must also cooperate on error handling.

How can we handle unexpected errors, transient errors (Ref. Cosmic Rays?), and yet unfixed errors?

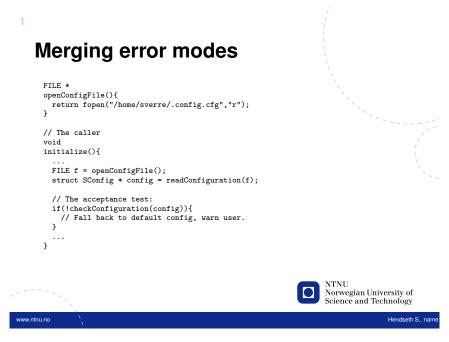
How can we handle the bugs that are still left in the system after testing?

Answer: Fault Tolerance! – The Chapter headline!

Answer: Acceptance tests & Redundancy.

Answer: Merging of error modes

- merge also with the unexpected errors
- And it works wonders for a modules interface.



Technique: Merging of Failure Modes:

- There are bugs here unknown bugs that we have to handle...
- What is the worst that can happen?
- We must handle this anyway, so group with easier ones (lots of these are known situations!) (Ref. Fopen example if we cannot open the configuration file, go with the default configuration)
- The cost of increased frequency (but it is still seldom, so...)
- We achieve:
 - A simpler system overall
 - Simpler error handling.
 - Better modules.
 - Handling of unknown errors.

- Examples:
 - All communication errors -> Lost message, resend
 - All SW bugs -> restart.
 - All exceptions -> ABORT/Try Again.

(Relevant for project again: When you are handling restart anyway...)

int checkConfiguration(struct SConfig * config)){ // check range of all variables if(config->nNodes < 1 && config->nNodes > 5) return 0; // Check any correlations if(config->maxOrders > config->nNodes * 4) return 0; // Possibly configuration have been extended with // parameters enabling better checks if(config->isSimpleSystem && config->nNodes > 4) return 0; ... return 1; }

Technique: Acceptance tests (a change of perspective)

- Not the traditional focus on
 - error situations
 - error returns
 - (These will never handle unexpected errors.)
- Rather: We put demands on current status to be ok (**try** to get a configuration from file. Is the current configuration reasonable? If not go with the default)

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• NB: Handles unexpected errors!

5.4 Fault Tolerance: Some terms

Terms:

- error: Extending the error term to include: unexpected situations + exceptions + situations that happen seldomly + other situations we must handle. (where the methods are the same)
- Reliability = consistency between system and spec.

Terms:

Failure	->	Fault	->	Error	->	Failure	->	Fault
Something		the bug		the thing		Consequence		Failure in subsystem
went wrong		(part of		that happened		compared to		represents a fault in
to insert		the system)				the spec		supersystem
a fault								

Bugfixing - Fault Prevention - removes faults. Fault Tolerance prevents errors becoming failures.

Term: Fault Prevention

- 1. Avoidance
 - Complete spec.
 - Good methodology
 - Good language/abstraction
 - Good tools.
 - (Basicly: Good work)
- 2. Removal
 - \bullet Reviews/Inspections
 - Verification
 - Testing

5.5 Fault tolerance: Some basic techniques

How to make an acceptance test

- Replication Checks
- Timing Checks
- Reversal Checks
- Coding Checks
- Reasonableness Checks
- Structural Checks
- Dynamic Reasonableness Checks

Technique: Recovery Points

• Storing consistent states so that we can reset sensibly after an error.

Technique: Redundancy

- static: All parts are active and errors is masked (n-version prog.) (What errors does this handle? SW errors?)
- dynamic: Detects & and handles (recovery blocks.)

Both increases the complexity of the system

5.6 N-Version Programming

Static Redundancy: "Classic redundancy", Learning from HW culture. Masks errors: Handles transient errors well, including unexpected errors (if systems are different).

Figs on blackboard: Failfast, (What do we get from these) (1! error mode!) pair & spare, 3vs 3 etc.

Failfast:

• Merging failuremodes into one!

- No wrong outputs
- suitable building block
- Also nplex, pair & spare
- NB: Fails more often than one module alone
- Easy to compute reliability figures.

Static redundancy Technique: N-version programming

Fig. 3 boxes and one voter

- Handles transient errors.
- Can handle unexpected errors (if systems are different).

Challenges:

- 4 parts increases complexity significantly. (Number of bugs increases more than linearly? Anyway maintenance is!)
- The driver must be correct.
- Price
- Must avoid the blocks have the same errors
 - N different versions, but
 - * common culture
 - * common spec

make independent errors difficult

- * difficult parts
- The Voting problem:
 - How to handle:
 - * discrete decitions
 - * More acceptable decitions

5.7 Recovery Blocks

(Dynamic redundancy: redundant in that we have more ways to do the same. Must have detection - acceptance tests.)

Dynamic redundance Technique: Recovery Blocks (Dynamic Redundancy, Backward Recovery)

- Program more ways to solve the problem
- Make a good acceptance test
- Store recoverypoints to go back to when tests fail
- Try again in other way.
- More threads? Hmmm.

Term: Levels of Fault Tolerance

- Full
- Graceful degradation
- Failsafe

5.8 SW dynamic redundancy

detect	Kode
confine/assess	Design!
recover	Kode
treat	routines

Design is necessary here! For "Traditional error handling" error handling is easily done afterwards. Not here!

- After having detected an error we must know the "error domain" so that the error can be compensated for.
- (With more participants) we must ensure that the consequences of an error is not spread!

2.5.1 Error detection: (talked about this already)

2.5.2 Confinement & Assessment

Question: What must be done to to handle an error?

We must design the program so that this question have an answer (<- Design)

- Be careful not to spread unsafe data.
- Mark or store safe, consistent states (recovery points). <- a technique
- Make modules
 - with clear interfaces
 - with their own consistency checks & error handling.
 - that can participate in creating recovery points.
 - etc...
- Atomic Actions is a confinement pattern...
- . . .

Ex. An order is wrongly deleted in the lift system...

2.5.3 Error Recovery: Two types:

Backward Error Recovery i.e. to a recovery point

Forward Error Recovery compensate for the error to get to a consistent state

Fig: Arrows, X back to recovery point or to some other consistent state. Generalizing to more participants:

• May lead to domino effect

Fig: Domino effect

(Learn to draw this figure :-))

2.5.4: Error Treatment

- send mail
- write to log
- sugges upgrade
- + routines for follow-up.

6 Gray: Fault Model & Software Fault Masking

6.1 Learning Goals

Learning goals; Fault model and software fault masking

- Understanding of the three cases in low level design for fault tolerance by redundancy: Storage, Computation and Communication.
- Understanding of the work method: 1) Find error model 2) detect errors and merge failure modes (+error injection for testing) 3) handling/masking with redundancy ...aiming for progression of fail fast, reliable and available systems
- Ability to Implement (simple) Process Pairs-like systems.



6.2 Its learning call

Tomorrow we will examine a systematic workflow for making fault tolerant software systems, building on top of last weeks techniques of "merging error modes" and "acceptance tests", utilizing redundancy.

We will cover the chapter (copy on its learning) 3.7 in Gray&Reuters book "Transaction processing" going through three fundamental cases: fault tolerant storage, communication and processing. The "process pair" approach to fault tolerant processing is especially relevant, both for how you think on the project and for the exam. Check the continuation exam 2014 task 3a, 3b, 3d and 3g. (3d on explaining process pairs could be emphasized:-))

Do not miss the opportunity to give also this week a good start!

6.3 Intro

What we have: Acceptance tests, Merging failure modes, Redundancy So, given these elements: How would a fault tolerant system in fact be built? This chapter describes 3 examples to illustrate.

Chapter title:

Fault Model Failure modes, probabilities and spec Software Fault Masking "Masking errors by redundancy"

Remember the Context here:

- To handle also enexpected errors -> Everything can fail -> Is everything hopeless?
- This kind of fault tolerance is a question of design
- But is it a global design? We would want composition: to be able to build fault tolerant systems from fault tolerant modules.
- This chapter: Ee are making *the* three basic modules (storage, communication and processing).

The underlying progression in the examples

- Failfast (all failures is detected immediately)
- Reliable (failfast + repair)
- Available (continuous operation)

Rough Process:

- 1. Find the failure modes.
- 2. Detect errors / simplify error model.
 - injection of errors for testing
- 3. Error handling by using redundancy -> reliable & available module

Error injection:

• lets us test error handling!

Brainstorm: How can we test the handling of unexpected errors?

- Yes, (if acceptance tests are good) inject failed acceptance tests
- Btw: This is also simplified by merging of failure modes.

6.4 Case 1: Storage

Assume unreliable functions: read & write. We are writing sectors to the HD, but imagine an array of data areas.

Step 1) Failure modes:

Write Read

Writes wrong data
Writes wrong place
Gives wrong data
Gives old data

Does not write Gives data from wrong place

Fails Fails

(Grays model also includes probabilities. -> We are reducing the chance of failure to less than some number here.)

Step 2) Detection, Merging of error modes and error injection.

- Writes also address, checksum, versionId, statusbit to the buffer. (version id isnt used yet, statusbit will be used for injection.)
- All errors -> fail
- Makes "decay" thread that runs in paralell and flips status bits (For testing).

Step 3) Handling w. redundancy

- More copies of buffer -> (The version id is used The newest is returned)
- All reads implies writeback on error.
- Repair thread reading regularly.

Possible to calculate probabilities for error of the total system.

store_read

```
/* Reads a block from storage, performs acceptance test and returns status */
bool store_read(group, address,&value){
  int result = read(group,address,value);
  if(result != 0 ||
    checksum fails ||
    stored address does not correspond to addr ||
    statusBit is set){
    return False;
}else{
    return True;
}
```



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The (error injection) Decay Thread

```
/* There is one store_decay process for each store in the system */
#define mttvf 7E5 /* mean time (sec) to a page fail, a few days */
#define mttsf 1E8 /* mean time(sec) to disc fail is a few years */
void store_decay(astore store){
   Ulong addr;
   Ulong page_fail = time() + mttvf*randf();
   Ulong store_fail = time() + mttsf*randf();
   while (TRUE){
        wait(min(page_fail,store_fail) - time());
        if(time() >= page_fail){
             addr = randf()*MAXSTORE;
             store.page[addr].status = FALSE;
             page_fail = time() - log(randf())*mttvf;
        }
        if (time() >= store_fail){
             store.status = FALSE;
             for (addr = 0; addr < MAXSTORE; addr++) store.page[addr].status = FALSE;
             store_fail = time() + log(randf())*mttsf;
        }
    }
}</pre>
```

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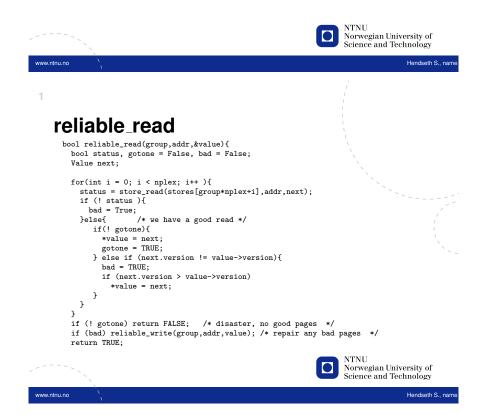
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Reliable Write

```
#define nplex 2 /* code works for n>2, but do duplex */
Boolean reliable_write(Ulong group, address addr, avalue value){
   Boolean status = FALSE;

for(int i = 0; i < nplex; i++ ){
   status = status ||
   store_write(stores[group*nplex+i],addr,value);
}
return status;</pre>
```



This can still fail silently! Under which circumastances?

The Repair Process

```
void store_repair(Ulong group){
  int i;
  avalue value;
  while(TRUE){
    for (i = 0; i <MAXSTORE; i++){
        wait(1);
        reliable_read(group,i,value);
    }
}</pre>
```



(These code snippets are taken from some presentation helt by Gray found on the internet.)

6.5 Case 2: Messages

Fault tolerant communication demands storage & processes in order.

Step 1) Failure modes Message: Lost, delayed, corrupted, duplicated, wrong recipient.

Step 2) Detection, Merging of error modes

- Session Id
- checksum
- ack
- sequence numbers
- All errors -> Lost message

Step 3) Handling w. redundancy

• Timeout & Retransmission.

6.6 Case 3: Processes/Calculations

Note: Does not require safe communication

Step 1) Error mode: Does not do the next correct side effect.

Step 2) **Detect&Simplify**: All failed acceptance tests -> PANIC/STOP (Failfast)

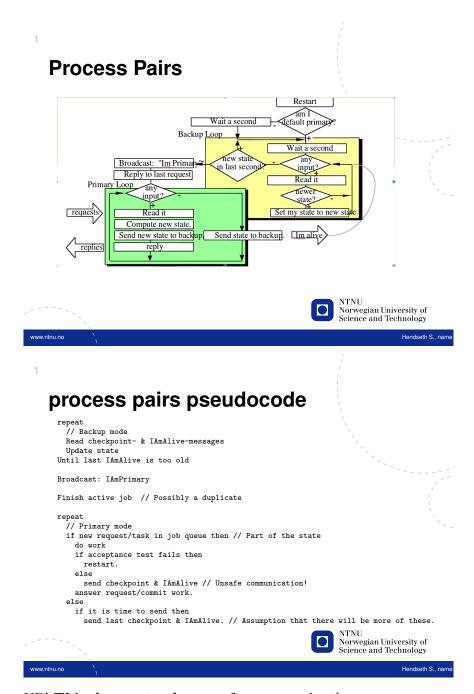
Step 3) Handling w. redundancy: 3 solutions:

Alt 1: 3a) Checkpoint-Restart (Reliable)

- Writes state to storage after each acceptance test, before each side effect
 - (Note this very important pattern.)
 - (Yes, we are very dependant on good acceptance tests!)
- This gives us error containment.

Alt 2: 3b) Process pairs (Gives us available)

- Two processes, Primary & Backup (primary does the work)
- Backup takes over then the primary fails
- IAmAlive messages from primary to backup
- Primary sends checkpoints to backup.



NB! This does not rely on safe communication

• Redundancy by resending: Masks communication errors.

- Note new master can answer request sent to old master (no sessions)
- (Communication queues part of state).

• . . .

Alt 3 3c) Persistent Processes

- Assumes a transactional infrastructure.
- All calculations are transactions ("atomic transformations from one consistent state to another").
- The processes becomes stateless All states are in database
- For such simple processes the OS can take care of take-over & restart!

In grays vision: **This** is the context of databases!

- But of course: Reliable/available Storage/Communication/Calculations is necessary for making this transactional infrastructure.
- So for us, where it is not, we need to do Alt 1 or 2.

7 Fault tolerance with more participants: Transaction fundamentals

7.1 Its learning call

Today we will discuss sw dynamic redundancy in systems with more participants (threads, processes, distributed systems). Since these participants cooperates towards the functionality of the total system, they also, sometimes, must cooperate in error handling.

"Atomic Actions" (the term used in the Burns & Wellings chapter) or "Transactions" (used in the "Transaction Fundamentals" chapter) are the standard techniques that provides the necessary framework error containment and handling.

We will be developing these frameworks step by step, focusing on the relevant design patterns on the way.

7.2 Learning Goals

Learning goals; Transaction Fundamentals

- Knowledge of eight "design patterns" (Locking, Two-Phase Commit, Transaction Manager, Resource Manager, Log, Checkpoints, Log Manager, Lock Manager), how they work and which problems they solve. Ability to utilize these patterns in highlevel design.
- Comprehension of terms: Optimistic Concurrency Control, Two-phase commit optimizations, Heuristic Transactions, Interposition.



7.3 Intro & Context

Context:

- we search for the "error assessment and confinement" design
- We have more participants (more threads, processes, servers,...)
- The system might be doing more things in paralell (not only like "threads", but also conceptual tasks of the kind that requires cooperation between participants)
- We are continuing from "sw dynamic redundancy" "how to avoid the domino effect"

Motivating examples:

• The process control plant with a lot of local controllers, supervisory tasks, monitoring, safety, optimizing. Operator interfaces with scripting abilities. Multiple modes of operation.

- The mobile sensor network (ships, drones, bouoes)
- The "open embedded infrastructure", imagining cooperation between the safety fire, burglar alarms, smartgrid, internet, home automation

So, how to make "error assessment and confinement" in such a setting?

- To avoid the domino effect we need to negociate the "starting point".
- and keep track of participants.
- The starting point of what? Enter the *action* the operation the thing we want to do.

Warning:

- The chapter is not written for us RT people: It is written for users of an transactional infrastructure that needs to know how it works. We do not have infrastructure; we must learn how to build it.
- Assumed implementation is RPC, but the principles stay the same.

8 Patterns:

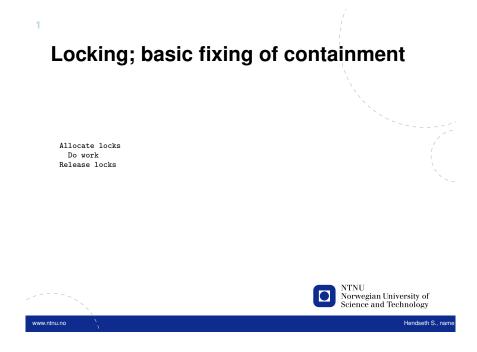
- How do they work?
- When are these used / which problems is solved?

7.4 Locking

We need features!!!

How do we ensure nonspreading of intermediate states?

- Locking
- explicit membership (if not static)
- Participants are only allowed to communicate with other participants.



7.5 Bacwards error recovery

We will be thinking backward error recovery for now; "Abort" is the common failure mode. It is a bit simpler, and when the framework is in place we will recheck the assumption...

- an action like this with backward error recovery is a transaction.
- Errors can not have no consequences outside of the action (error containment) and since no inconsistent, intermediate states are visible to non-participants we call them *atomic actions*.
- an action is an atomic transformation from one state of the system to another.

Backwards Error Recovery v1: Recovery Points

```
Allocate locks
Store variable values (recovery points)
Do work, jump to end: if problems
label end:
if(error){
  set variables back to recoverypoint
  status = FAIL;
}else{
  status = OK;
}
Release locks
  return status;
```



7.6 More things to do

What if there are more things to do or more participants?

```
status = OK;
if(doWorkX(...) == FAIL) status = FAIL;
if(doWorkY(...) == FAIL) status = FAIL;
if(doWorkZ(...) == FAIL) status = FAIL;
if(status == OK){
    commitWorkX(); commitWorkY(); commitWorkZ();
}else{
    abortWorkX(); abortWorkY(); abortWorkZ();
}
```



7.7 Generalizing

Introducing a Transaction Manager

```
TransactionId tid = tm_beginWork();
doWorkY(tid,...);
doWorkY(tid,...);
doWorkZ(tid,...);
result = tm_endWork(tid);
```



The Transaction Manager

- tm_beginWork(): Creates a transaction; generates uniques id, keeps track of members.
- tm_joinTransaction(participant): Adds participant to members
- tm_endWork(tid): Asks all participants for status (Status prepareToCommit(tid)), counts votes, commits(tid) or aborts(tid) and returns status.



The Resource Manager ≡ The transaction participant

- offers rm_doWork(tid,...) functionality
- calls tm_joinTransaction(me) if not already a member
- keeps track of locks associated with the transaction
- keeps track of recovery points
- participates in two phase commit protocol (prepareToCommit(), commit(), abort())
- Note: prepareToCommit(tid), commit(tid) and abort(tid) can be given reuseable forms
 works only on the RMs data structures!



Sidetrack: Deadlocks, starvation and timing problems.

- These are allowed to happen! (But not good for Real-Time)
- Standard solution: Give each transaction a deadline, and abort if it does not reach it. (A job for the TM)



Sidetrack II: Interaction with the world.

- An effect on the outside world cannot generally be aborted.
- The solution must be dependent on the application:
 - Keep such effects outside of the transaction framework.
 - Wait until commit to do the action.
 - Develop "intelligent" HW that can handle abort or even participate in transactions.



Checkpoints revisited

- What if the RM state is too big?
- What if there are more concurrent transactions?
- What about: Only storing the used/locked variables?



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Writing to the log

- Every participant (including the TM) writes to a log what it plans to do and waits until it is confirmed safe before doing it. (Not only side effects; every change of state)
- After restart a process can get back to the current state by "executing" all the logrecords in order.
 - Of course, when executing log records, only the ones belonging to committed transactions must be executed. (Commit and abort logrecords must also be written.
 - Some transactions may lack commit/abort logrecords. The TM has not decided, or the decition was made when we were down. We need to be able to ask the TM about these.
 - If asked by the TM about transactions that were active before the crash we should vote for abort.



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7.7.1 Log II

Writing to the log – But what if the TM is restarted ?

- After the votes are in, but before the results are sent out, the result is logged.
- $\ensuremath{\boldsymbol{--}}$ All transactions active when the TM restarts are aborted.



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7.7.2 Log III

Writing to the *log* – A genious extension!

- Also write before-state into logrecords.
 - Logrecords can now be executed backwards actions can be undone.
 - We get rid of the recoverypoints!



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7.7.3 Checkpoints

Writing to the *log* – But, will not the log get infinitely large?

- Yes; the solution is to write **Checkpoints** to the log.
- Once in a while the complete state, including a list of all active transactions the checkpoint - is written to the log.
 - Log that is older than the last checkpoint that does not contain any active transactions may be deleted.
 - NOTE: A checkpoint is not a consistent state alone.



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7.7.4 LogManager

Time to introduce a Log Manager?

- Yes, this can queue more logrecords and optimize disk access.
- ... and if it runs on another machine we may be satisfied with the reciept for received logrecord rather than waiting for the disk access.



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7.7.5 LockManager

... and a Lock Manager?

- Can "release all locks associated with transaction X".
- Can tidy up properly if we are restarted.
- Can handle resources common to more RMs
- Can be extended with deadlock detection or avoidance algorithms.



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7.7.6 Summary 2

Summary

- This is philosophy a way to think about error handling and consistency in distributed systems / systems with more threads/processes/participants.
- For us on the RT / embedded side (that does not have a ready-made transaction infrastructure) the challenge is to adapt this to our use.



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7.7.7 Cases

Some Cases

- High availability: We want redundancy in calculating power one computer should be ready to take over for the other if it is restarted.
- Scalability or load balancing: We should be able to put extra resources in at bottlenecks.
- Online upgrade: We need to be able to upgrade the system without loosing availability.
- Consistency: How ensure a consistent total system after a subsystem restart ?



7.8 Further Comments on the transaction chapter

Further comments to the chapter:

Optimistic Concurrency Control:

- Assume non-interference, check afterwards and handle as error (/abort)
- Comes out as an alternative to locking.

Two-phase commit optimizations:

- presumed abort (early abort) (Using e.g. ATC)
- \bullet one-phase (only one participant no use voting)
- read-only (commit/abort irrelevant) (Class challenge: do we really ned to lock when doing a read-only operation?)
- Last Resource Commit: **One** participant/role gets to wait until after vote to make his decition: He can be responsible for world interaction?

• (Irrelevant for us: Synchronizations (4-fase commit, inn og utsjekking av cache))

Heuristic Transactions:

- What if give a vote and then lose connection?
 - We have to do å local guess w. ad-hoc error handling after. The system might become inconsisten/require forward recovery.

Interposition:

- A TM's ability to play the role of en RM ==> Nested **server**-modules.
- A GREAT way of dividing a system into modules!

Nested Transactions:

- (Sverre: Meh, this does not work so well, it is no fruitful way of building super-modules out of submodules, but even the B&W book covers it...)
- Locks are inherited by the super-transaction
 - --> subtransactions can be rolled back even after commit (Sverre: Is this even relevant for us?)

Now all is covered, skip the rest of the considerations.

7.9 Repetition 8 patterns

7.9.1 Locking (w. growing/shrinking phases)

The standard way of getting side boundary. Gives us error containment / Atomicity.

7.9.2 Tophase Commit (The vote)

Gives us End Boundary: Coordinate acceptance tests to avoid the domino effect.

 $\operatorname{prepareToCommit}()$ and then either $\operatorname{commit}()$ or abort() when the votes are collected.

7.9.3 Transaction Manager (Action Controller)

Reuseable infrastructure. Keeps track of Membership (Gives us Start Boundary), administres voting. Client calls startWork/ EndWork.

7.9.4 Resource Manager (A new way of making modules: The participant of a (trans)action)

A module/server/class that can take part in a transaction. Supports doWork(tid) calls, will contact TM for membership (join), and supports preparetocommit(), commit() and abort().

7.9.5 Log

Useful for restart (An alternative to the recovery points in B&W) handles large states and parallell actions. Also useful for undoing logrecords/aborting actions.

Everybody must log: Remember how RM & TM recovers.

Failure recovery:

- RM fails (read log as usual, ask TM about the ones with onknown status)
- TM fails (read log as usual, all transactions active gets aborted)

(TODO: Write out some more detailed recovery scenarios for next year)

7.9.6 Checkpoints

Inconsistent snapshots of the state that lets us avoid the log becoming infinite. The log that is older than the last checkpoint where all actions are finished can be deleted. (- we know the outcome of each of these.)

7.9.7 Log Manager

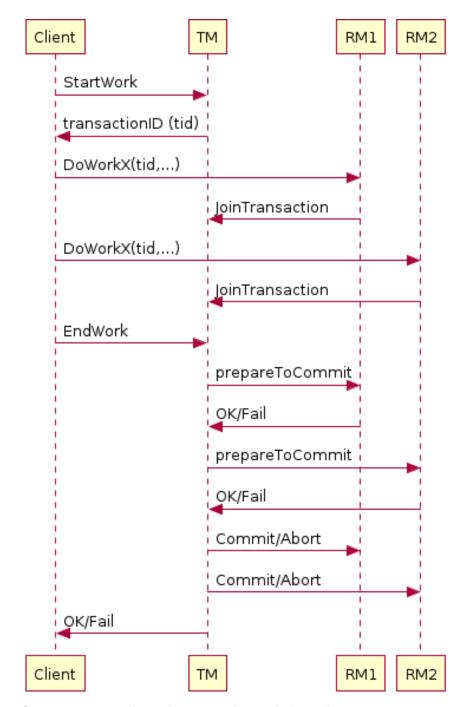
Yes, this can queue more logrecords and optimize disk access. ... and if it runs on another machine we may be satisfied with the reciept for received logrecord rather than waiting for the disk access.

7.9.8 Lock Manager

- Can "release all locks associated with transaction X".
- Can tidy up properly if we are restarted.
- Can handle resources common to more RMs
- Can be extended with deadlock detection or avoidance algorithms.

7.10 Sequence diagram for a transaction.

As a summary: Go through sequence diagram for transactions



Comment on Locking also, to explain side boundary.

8 Atomic Actions and forward error recovery.

8.1 Its Learning Call

This week we will be covering chapter 7 from Burns&Wellings on "Atomic Actions". We will continue from last weeks walk-through of how to construct a Transaction, on one hand being more specific on how it can be implemented in different languages. On the other hand we will generalize Transactions into Atomic Actions by allowing forward recovery.

Do not miss the opportunity to give also this week a good start!

— and for the Tuesday:

Today we will be seeing more code examples on how to implement Atomic Actions in Java and Ada. The examples are taken from the book chapter, are large, and not the easiest read. The relevant language features of Java and Ada (which are the reason the examples and choice of languages are relevant to start with) will be explained. (Java: synchronized methods, wait/notify/notifyAll. Ada: Protected Objects, functions, procedures, entries with guards)

Anders will be using the second hour for an exercise lecture.

— and for the next Monday:

We will finish the Fault Tolerance part of the course this week, ending with how forward error recovery can be embedded into the atomic actions framework.

Forward error recovery with more participants motivates the immediate signaling of error situations, and we will go through POSIX/C, Ada and Javas mechanisms for Asynchroneous Notification and Asynchroneous Transfer of Control.

Questions/answers as time allows. If destiny and computer skill are on my side a Kahoot session?

Tuesday will be an introduction to Go with an old Kyb Student Tor-Inge Johannessen Eriksen.

Do not miss the opportunity to give also this week a good start!

8.2 Learning Goals

Learning Goals: Atomic Actions

- A thorough understanding of the problems Atomic Actions are meant to solve and how these motivates the different aspects of Atomic Actions.
- Ability to use and implement Atomic Actions, including the mechanisms providing the start, siden and end boundaries.
- Understanding the motivation for using Asynchronous Notification in Atomic Actions
- Knowing how to use the mechanisms for Asynchronous Notification in C/Posix, ADA and Java, including understanding how the ADA version are used for forward recovery in atomic actions.



8.3 Atomic Actions Intro

Now Finally, Atomic Actions: AA Has been mentioned before:

- Error handling with more participants.
- Under "Sortware dynamic redundancy": How to do design for damage confinement & assessment
- How to avoid the dominoeffect.
- A way of dividing the system into modules: a transformation from one consistent state to the next
- In Grays vision for reliable & available computing. (Well, he called it "transactions")

An AA is:

• A design framework for Damage Confinement and Error Recovery.

Other, good, definisions:

- A transaction just without the backward recovery error mode as standard error mode. (Backward error recovery is not that good in a RT setting, remember...)
- indivisible & instantaneous seen from the outside
- A transformation from one consistent state to another.
- A modular computation.

Features of an AA/Transaction

- Explicit participation/membership
- Well defined starting points (**Start boundary**) (possibly w. recovery points in the case of backward error recovery?) (Not necessarily coordinated in time)
- No communication/sharing of info with non-participants. (Side boundary)
- A Clear end point (End boundary) (Coordinated in time!)

Fig: The Half Circles.

Comments:

• A recovery point, if used at all, does not need to be the complete state.

8.4 AA Standard implementation

Start boundary:

- Dynamic: startwork/joinTransaction
- Hardcoded? (How initiated?/what plays role of "client"?)

•

Side Boundary: Locking

- TwoPhase locking: growing & shrinking.
- The transition must be coordinated,
- No unlocking before safe state (== End Boundary!)

End Boundary:

- Some vote-counting (two-face commit?) at the end
- Presumed abort (quitting before viote)?
- Synchronization primitive "Barrier"

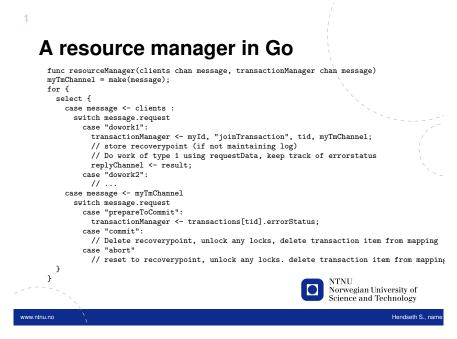
8.5 How can we implement a AA?

So, AA implementation?

- 1. Threads, interacting with each other? (like, in the domino-figure)
 - Participants supporting protocols
 - Go example?
 - Ada task
- 2. Server-processes that handles requests? (same, just structured: Gray)
 - Supporting protocols
- 3. A number of procedures that is called by participating threads? (the book, Focus on language features)
 - Action == Class or Package/module?
 - Thread interaction through synchronization and shared variables.
- 4. Threads that is created and terminated with the AA? (Cool, the way to get forward recovery in C)
 - select-then-abort in Ada
 - Asynch. exceptions in Java
 - pthread_{cancel} in POSIX
- 5. ...

This is a design framework

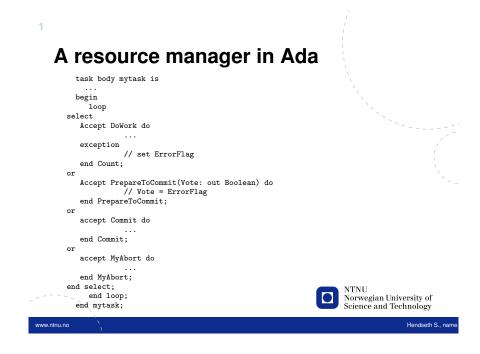
8.5.1 Messagebased rm in go



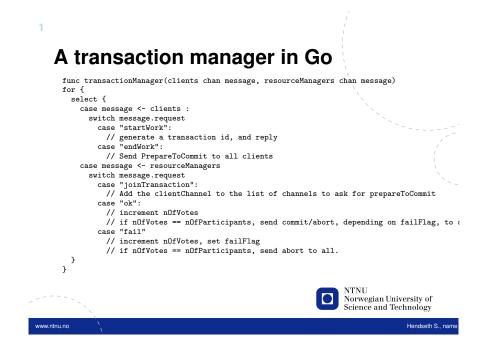
In the Go way of thinking there should be no shared resources. This means no need for locks? (Y/N)

No: One server thread may maintain its local data, but when one action have updated the data, other clients should not be able to access them. Access to a RM in itself might be shared.

8.5.2 Messagebased rm in Ada

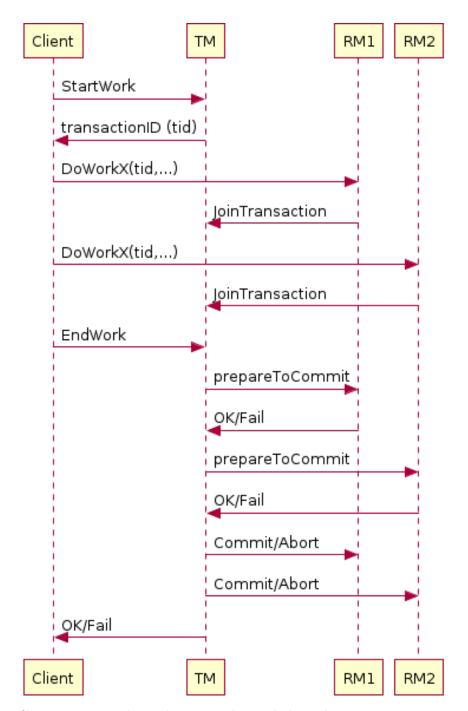


8.5.3 Messagebased tm in Go



8.5.4 Sequence diagram for a transaction.

As a summary: Go through sequence diagram for transactions



Comment on Locking also, to explain side boundary.

8.6 Sidestep: java monitors

Java Monitors:

- synchronized methods:
 - Only one thread can call one of those in an object at the same time. (need object lock) (Classes have locks also).
 - Almost like if all methods reserved a mutex when running.
- wait(): The thread is suspended and the lock released.
- notify()/notifyAll(): Awakens one/all threads
 - but they still need the lock, and cpu, to continue.

8.7 Sidestep: ADA Protected Objects

Note: Cannot block from the inside of a protected object!

ADA: Protected Objects.

- Modules (as with Monitors)
- Functions (readlocks)
- Procedures (writelocks)
- Entries w. Guards (not CondVar)
- Blocking from the inside is an error! (Waiting on other monitor is not blocking Waiting on guard or on task entry is...)
- Guards tests only private variables. -> reevaluation only when exiting entry&proc. // Hvorfor ikke funksjoner?

Term: Guard:

- A test integrated in the language that opens or closes for execution
- In our context: blocking
- Is not (necessarily, only) evaluated sequentially

8.8 Code Example: Making an AA framework in Ada.

Relevant slides from B&W: Chap. 7: 10-16

8.9 Code Example: Making an AA framework in Java (without error handling)

Chapter 10 (ThirdEdition of the books slides) Slide 20-26 Comments on example

- Start Boundary ok
- Nice Action == Class
- No error handling
- No side boundary
- static participants & actions.
- How is one of these actions initiated?
- reusable functionality in controller:
 - membership
 - voting (end boundary)
 - _

8.10 Adding backwards error recovery to these code examples

Note: The structure is quite static in the examples

- A module per Action!
- Predefined Roles!

We can simply

- maintain a module-flag for error
- Handle error recovery in the finish procedure.

8.11 Asynch Notification Motivation

Where we are - summary:

- We have: A SW structure that **enables** multithread error handling.
- A voting at the end of an AA that facilitates (backwards) error recovery
- But too late: Is it meaningful to wait for everybody to finish after an error have been detected?
 - (Well, we had the "presumed abort" commit protocol optimization, but still, how to abort?)
- AA & Backward Error Recovery == Transactions
- Backward error recovery is simple handle all errors in the same way

AA & Forward Error Recovery:

- We need a mechanism for distribution of error information:
- (1): Extend the commit protocol?
 - Would not cover all error modes (crashes, hangups)
 - Too late?
- (2) Error info distribution when it happens (Messages, global error variables, POSIX signals ->polling)
 - conseptually good (One error invalidates what is going on)
 - performance ok.
 - Would not cover all error modes
 - Still Too late?
- (3) "Solution": Asynch Transfer of Control (Kap 10 del 2) POSIX:
 - (Signals & setjmp/longjmp)
 - Kill the processes

ADA:

- structured variant of thread termination based on select.

Java:

- ability to throw exceptions in other thread.

Terms: Asynch Notification (AN) = Lowlevel thread interaction = Asynch Transfer of Control (ATC) (termination)

• Asynch Event Handling. (resumption)

Term: **Resumption vs. Termination mode**: (Important terms used also about signals and asynch transfer of control)

- If we continue where we was (after the throw)? -> Resumption
- If we continue somewhere else (i.e. terminating what we were doing)
 -> Termination

Resumption most often leads to polling.

Why avoid Asynch Transfer of Control?

- It leaves us in undefined state!
- But then; If we have...
 - Full control over changed state
 - A lock manager that can unlock on behalf of killed thread
- ...it might be doable.

8.12 POSIX: Signals and threadCancel

AN: POSIX signals (Resumption)

- Modeled after HW interrupt
- Can be sent to the correct thread
- Can be handled, ignored, blocked -> the domain can be controlled
- Often leads to polling

- Could rather skip the signal and poll a status variable or a message queue?
- Useless (?)

But: Threads can be killed

- Can make termination model by letting domain be a thread
- can still control domain by disabling 'cancelstate'

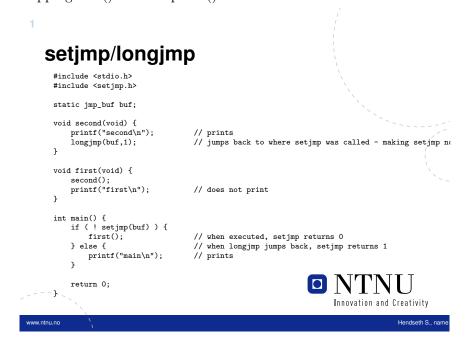
Why do we need to control the domain?

- Some operations are critical
- imagine the implementation of the lock-manager
- locking must be atomic.

8.13 C: setjmp/longjmp

Copied from the wikipedia: Simple Example:

The example below shows the basic idea of setjmp. There, main() calls first(), which in turn calls second(). Then, second() jumps back into main(), skipping first()'s call of printf().



When executed, the above program will output: second main

8.14 ATC Implementasjon Java

ATC in **Java**:

- (Before: suspend, resume, stop)
- Still have: interrupt, (+isInterrupted)
 - waking from wait state
- destroy.

ATC i **RT** Java

- Built on Java exceptions rather than select as in ADA
- An interruptible function must declare AIE
- Will never interrupt in any synchronized or in a finally block
- Can be blocked
- Each **thread** is assiciated with a generic AIE
- Interrupt() wil now throw the generic AIE asynchroneously.
- Must then test and clear on the generic
- B&W tries to make sense out of this. Let us not.

8.15 Select then abort in ADA

```
select
  delay 1.0 -- or any event.
then abort
  -- Do the real work
  -- Will be interrupted if it times our or the event happens.
end select;
```

Classroom challenge: How to make forward error recovery with this? Hint

- Use the controller to communicate with the participants
- Note: Cannot block from the inside of a protected object!

On the blackboard:

Reason := E;
end signalAbort;

```
AA med Forward Error Recovery i Ada:
Role1:
begin
  select
     controller.waitAbort(E1) // Event
     raise(E1)
  then abort
    begin
      // Useful work
    exception E2
      // Noe gikk galt - alle må få vite
      controller.signalAbort(E2) // Proc
// Gjør controller villig til å være med på waitAbort (N ganger).
    end
exception
  // Exceptions her er felles for alle trådene!
  when E3
    // handle error
    controller.Cleanup(ok/fail)
                                     // Proc: avgi stemme -> forbereder for backward e
    controller.WaitCleanup(Decition) // høyere-nivå feilhåndtering
    // Håndter evt. Decition==fail
end
procedure Controller.signalAbort(E)
begin
  Killed := True;
```

```
Entry Controller.WaitAbort(E: out ExceptionId) when Killed is
begin
    E := Reason;
    Informed := Informed + 1;
    if Informed = 3 then
        Killed := False;
        Informed := 0;
    end if;
end WaitAbort()
```

Drawing the arrows of execution flow in case of error.

9 Shared Variable Synchronization

9.1 Its learning call

Remember the "introduction to Go" lecture today.

It is not entirely clear to me whether it will take one or two hours. If there is significant time left when Tor-Inge has finished, I will motivate the second part of the course; Shared Variable Synchronization. Underlying, recommended Curriculum here will be the copied chapter five of the Burns and Wellings book, and "the little book of semaphores", which is freely available.

See you

The second part of the course, in addition to scheduling, of what I have called "classic real-time programming" is shared variable synchronization.

This is basicly how to avoid problems like the "i=i+1/i=i-1"-problem and is common to also other kinds of multithreaded programming.

We will be progressing through lowlevel primitives (like suspend/resume) through semaphores and preparing for the modern mechanisms we have in POSIX/Java and Ada today.

Knowledge of the strengths and limitations of these mechanisms is a very central part of the curriculum.

If you want to prepare, read any chapter of "the little book of semaphores" that is available on the internet. This illustrates much of the challenges in

this area, and trains you in the way of thinking when using shared variable synchronization.

See you tomorrow.

— Hi, there will be proper lectures tomorrow, to make up for the lectures lost during the excursion weeks.

The theme is still shared variable synchronization, continuing from the semaphores we arrived at Friday, and going through the more highlevel mechanisms leading up to the ones we find in POSIX/Java and Ada.

My quest here is (in addition to teaching you the language mechanisms with their strengths and weaknesses) to train your skill at recognizing race condisions. Bring pen and paper :-)

See you.

The second part of the course (after having finished "fault tolerance") will be what I have called "classic real-time programming". This is, in addition to "scheduling", "shared variable synchronization". We start tomorrow! A large part of the lectures tomorrow will be motivating the problems of shared variable synchronisation; "What is the problem?" and "Why is it hard?". Check the learning goals. Quite deep skills are required on this topic.

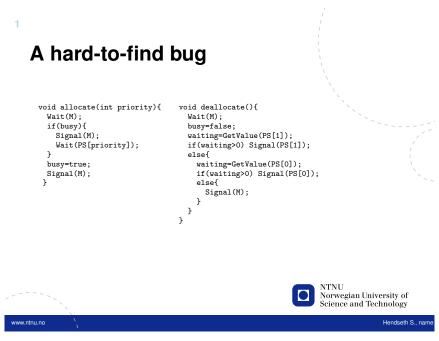
Shared variable synchronization starts with how to avoid problems like the "i=i+1/i=i-1"-problem, but, having solved this, grows into the pletoria of challenges on "deadlocks", "livelocks", "race condisions", "starvation", etc.

Curriculum on this part will be "the little book of semaphores" available on the internet and Burns and Wellings chapter 5.

Do not miss the opportunity to give also this week a good start!

After getting into the "detecting race-conditions" frame of mind yesterday, we will today take a few steps back and see how the development of a real-time kernel could happen, to put the different synchronization features into a "historical" and hardware perspective. The discussion spans the progression from non-preemptive systems to interrupts and preemption, spin locks, blocking and suspend/resume, events, locking, semaphores (finally!), conditional critical regions, monitors, and finally the modern synchronization mechanisms of Java/Ada and POSIX/C.

But testing yesterdays skills: The following code has a bug/ a race condition. Where? . . . and what is the symptom?



Having done our best to adapt to the "recognizing race condisions" way of studying code last week, and discussing non-preemptive scheduling, we will Monday 2/3 start by taking a few steps back and see how the development of a real-time kernel could happen, putting the different synchronization features into a "historical" and hardware perspective. The discussion spans the progression from interrupts and preemption, spin locks, blocking and suspend/resume, events, locking, semaphores (finally!), conditional critical regions, monitors, and finally the modern synchronization mechanisms of Java/Ada and POSIX/C.

Do not miss the opportunity to give also this week a good start!

Continuing our walkthrough of synchronization mechanisms and after finishing semaphores yesterday, we continue into the "Conditional Critical Region" and the "Monitor", before starting the walkthrough of state-of-the-art, real-world, synchronization primitives in POSIX, Java and Ada.

See you all.

Tomorrow morning we will go through Ada and Javas synchronization mechanisms in detail, and work through a number of examples.

Ada and Java are in this context just suitable example languages letting us carry our awareness of race conditions, deadlocks and starvation up from semaphores to the current abstractions of monitor-inspired conditional synchronization (POSIX/Java) and ConditionalCriticalRegion-inspired avoidance synchronization (Ada).

The semantics are not that difficult to learn; We are trying to build your skills here.

(This is the final part of the shared variable synchronization part of the course. Two themes remain: Scheduling/schedulability proofs, which will be kept by Amund and my quite theoretical introduction to communicating systems.)

Do not miss the opportunity to give also this week a good start!

9.2 Learning Goals.

Learning Goals: Shared Variable Synchronization

- Ability to create (error free) multi thread programs with shared variable synchronization.
- Thorough understanding of pitfalls, patterns, and standard applications of shared variable synchronization.
- Understanding of synchronization mechanisms in the context of the kernel/HW.
- Ability to correctly use the synchronization mechanisms in POSIX,
 ADA (incl. knowledge of requeue and entry families) and Java.



9.3 Standard problems

Ref. i=i+1 problem: (find the slide)

Solved with semaphores... Is everything ok now?

Some standard problems:

- Deadlock: system blocked in circular wait
- Livelock: system locked in a subset of states (like deadlock but we use CPU)
- Starvation: A thread does "by accident" not get necessary resources. Ex: Unfair scheduling. (Ref discussion on whether you should make assumptions on how the sceduling works. Ref. Go vs. Occam: & Who is waked by a signal?)
- Race Condision: A bug that surfaces by unfortunate timing or order of events.

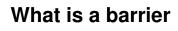
Class task: (You should know this) Classical example of race condision:

```
t1(){
   if(!ready) suspend();
     ready = 1;
     resume(t1);
}
```

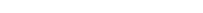
9.4 Making a barrier

Introducing the little book of semaphores: Read a few random cases to get into the way of thinking.

Sequence of slides:



Rendezvous, Critical point, AA exit point, two-phase commit protocol. All threads wait for all. Class task: How to make it with semaphores?





Each thread have their initialization to do - and have to wait for the others.

Classroom task: Try!

Hints:

- Assume N threads, N known, count them as they arrive.
- One semaphore protecting N, one being the barrier.

Failed attempt 1

```
// rendezvous
mutex.wait()
    count = count + 1
mutex.signal()

if count == n: barrier.signal()

barrier.wait()
// critical point
```



Quesions:

- (Easy) Why does this not work?
- (Hard) It might work sometimes... How?

One-time barrier Solution

```
// rendezvous
mutex.wait()
    count = count + 1
mutex.signal()

if count == n: barrier.signal()

barrier.wait()
barrier.signal()

// critical point
```



This is the "turnstile" semaphore pattern.

Questions:

- In which state is barrier after all threads have been through?
- If we were to fix this... How?

Now we want a reusable barrier: Imagine a while loop around the code.

Failed Attempt 2

```
while(1){
   // rendezvous
   mutex.wait()
   count = count + 1
   if count == n: barrier.signal()
   barrier.wait()
   barrier.signal()
   mutex.signal()

// critical point
}
```



Question:

• (Easy) What about this? Does it work?

Failed Attempt 3

```
while(1){
    // rendezvous
    mutex.wait()
        count += 1
    mutex.signal()
    if count == n: turnstile.signal()
    turnstile.wait()
    turnstile.signal()
    // critical point

mutex.wait()
    count -= 1
    mutex.signal()
    if count == 0: turnstile.wait()
}
```



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Questions

- How does this not work?
 - One obvious by now: Too many signals.

Finally, Solving problem 1, but...

```
while(1){
   // rendezvous
   mutex.wait()
   count += 1
   if count == n: turnstile.signal()
   mutex.signal()

   turnstile.wait()
   turnstile.signal()

// critical point

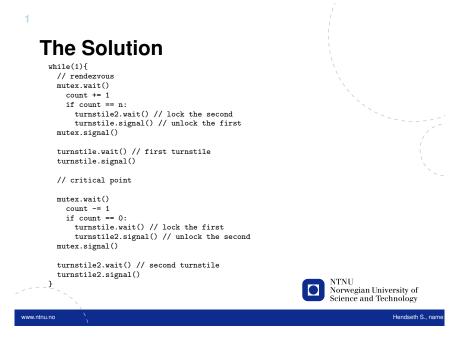
mutex.wait()
   count == 1
   if count == 0: turnstile.wait()
   mutex.signal()
}
```



Questions:

• So what is the problem now?

We need two turnstiles! Each unlocking the other.



This works:

Question:

• Are you convinced? If you were to bet your life?

9.5 The cigarette-smokers problem exam task

 \dots from the v2014 exam. Just walk through of 1e-1g

9.6 Standard applications

Some standard applications of synchronization

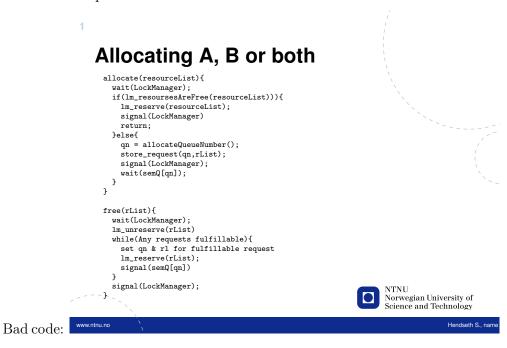
- Critical Section Code that must not be interrupted
- Mutual Exclusion More pieces of code that must not interrupt each other
- Bounded Buffer. Buffer with full/empty synchronization
- Read/Write Locks (Concurrent readers are ok)

- Condision Synchronization Blocking on event or status. (Guards e.g.)
- Resource allocation (More than mutual exclusion! Ref. the "lock manager")
- rendezvouz/barriere synchronization point (Ref. AA end boundary)
- Communication -
- Broadcast -
- Lock manager (reserve A, B or both)

Now take the hard-to-find bug.

9.7 Some harder problems

Classroom task: Lock Manager: If we want A, B, or both; How can we do this with semaphores.



Questions:

• Where is the problem?

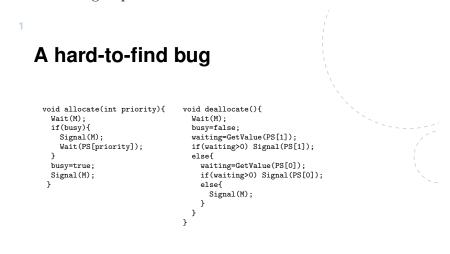
• Answer: Might wake the wrong client if it is not yet waiting...

Possible problem: Protect a number of functions, where some of tehm calls each other. Possible problem: Lock to more than one user a'la AA/Transactions.

These are difficult problems to solve with semaphores; Often leads to race condisions.

Another hard-to-find bug:

The setting is; We have two priorities for getting the resource. We want to wait in the right queue...





Questions:

- Where is the bug?
- What are the consequences?
- Can you solve it?

This is from B&W!

9.8 Pros and cons of preemptive and nonpreemptive scheduling

NonPreemptive Scheduling Preemptive Scheduling (Cyclic Executive)

- Simple, intuitive, predicatble
- No Kernel
- Threads must cooperate <- a form of dependency breaking module boundaries
- Heavy threads must be divided
- blocking library functions?
- Unrobust to errors
- Unrobust to (heavy) Error handling
- tuning at end of project

Preemptive Scheduling

- No problems with heavy threads
- Works well with competing, non-interacting threads
- Concurrent Access-problem: $t1()\{t2()\{i=i+1;i=i-1;\}\}$ Not only memory & CPU: All kinds of resources are shared.
- predictability
- race conditions
- tuning at end of project

9.9 Making a basic r-t kernel

Perspective: We are making a preemptive kernel!

Feature 1 (preemption): Switch between threads

- handle timer interrupt
- Store all registers (including ip & sp)
- organize queue of processes (Round Robin e.g.)
- Can synchronize by: while(!ready); (busy waiting, "spin locks")

Question: Can we get mutual exclusion with spin locks?

Solution:

```
flag1 = 1
turn = 2;
while(flag2 == 1 && turn == 2){}
    // Vi may run
flag1 = down;
```

Feature 2: Priorities

• Sort the process queue (or have more queues)

Feature 3 (synchronization):

- Queue of blocked processes
- suspend & resume.
- fixes the bad performance of spin locks.

(Home)Question: Mutual Exclusion, with suspend & resume?

• Fortsatt komplisert å unngå race conditions

Examples of bad solutions with race condisions

```
t1()
  while(busy == 1) suspend();
  // We own resource
  busy = 0;
  resume t2 // No
t1()
  while(TestNSet(busy,1) == 1) suspend();
  // We own resource
  busy = 0;
  resume t2 // No
Classical example:
t1(){
                            t2(){
  if(!ready) suspend();
                              ready = 1;
                              resume(t1);
}
```

Feature 4: (necessary when publishing suspend&resume or another sync. mechanism) protection of the schedulers data structures.

- Disable Interrupt. (not for large functions!)
- test&set instructions (or swap)
- And still: spin locks

(spin locks still relevant for some multicore mechanisms)

Feature 5: "Better" synchronization mechanisms.

- Events (wait(e), signal(e))
 - Just like anonymous suspend&resume
 - Separate queues for each event
 - still race condisions.
- Semaphores! (Usable!)

Recommend: "The little book of semaphores".

9.10 Semaphores

9.10.1 Presenting semaphores

Semaphores:

- Solves Mutual Exclusion
- Solves Condisional Synchronization
- Solves basic resource allocation

Classroom Task: Bounded buffer w. semaphors: Make the put and get functions.

Hints:

- You have the "enterIntoBuffer" and "getFromBuffer" functions, and the Element type.
- How many reasons for blocking are there?
 - Use one semaphore for each

Bounded buffer with semaphores

```
SEMAPHORE NInBuffer(0),NFree(N),Mutex(1);
put(e){
    wait(NFree);
    wait(Mutex);
    // enter into buffer
    signal(Mutex);
    signal(NInBuffer);
    signal(NInBuffer);
    signal(NFree);
```



Semaphore drawbacks: (Continuing feature list)

- Breaks modules (both ways)
 - does not scale!
- Deadlocks
 - global analysis -> does not scale
- Cannot release "temporarily"

9.11 Conditional Critical Regions

Conditional Critical Regions

- (Imagined) language support for mutual exclusion + guards.
- Named blocks are regions
- Regions of same name mutually excludes
- Regions have guards
- It is more structured, but still not a proper module
- Difficult to avoid bad performance when evaluating guards.
- (make the regions entries (if guards) or functions/procs (if not) in protected objects, restrict guards to private variables (and add requeue) and we have ADA)

9.11.1 Sidestep: Guards

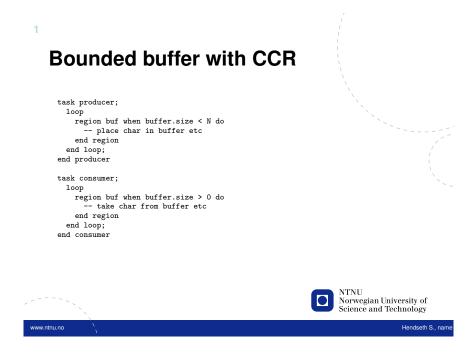
Term: Guard:

- A test integrated in the language that opens or closes for execution
- In our context: blocking
- Is not (necessarily, only) evaluated sequentially

Classroom questions:

• When are guards (re)evaluated?

• How do ADA make it?



9.12 Monitors

Monitors

- The region is a collection of procedures & local variables.
 - a module, great!
- No guards, but condition variables
 - Like events "named suspend/resume" but safe since they are only accessed from inside of the monitor
 - (Anonymize and we have java)
- Blocking on condition variable releases monitor temporarily
- Awakened process must wait for monitor lock (and cpu)
- Signal() awakens **one** process.

• Waiting on a condition variable must be done in a safe place, but it turns out that this is relatively easy.

Bad Code:

```
monitor M
unsafe(){
    t = i;
    wait(C);
    i = t+1;
}
```

Bounded buffers with monitors

```
procedure put(e);
begin
  if NumberInBuffer = size then
    wait(spaceavailable);
end if;
// Enter e into buffer
    signal(itemavailable)
end append;

Element procedure get();
begin
  if NumberInBuffer = 0 then
    wait(itemavailable);
end if;
// get an element from the buffer and return it.
    signal(spaceavailable);
end take;
```



9.13 POSIX: Mutexes & Condition variables

POSIX: Almost like monitors: Mutex & Condition variable

- Make the module yourself
- signal() awakens all

Bounded buffers with posix

```
void put( int e ) {
   pthread_mutex_lock( &mutex );
   while ( numberInBuffer == size )
        pthread_cond_wait( &spaceAvailable, &mutex );
   // do work
   pthread_cond_signal( &itemAvailable );
   pthread_mutex_unlock( &mutex );
}

int get() {
   pthread_mutex_lock( &mutex );
   while ( numberInBuffer == 0 )
        pthread_cond_wait( &itemAvailable, &mutex );
   // do work
   pthread_cond_signal( &spaceAvailable );
   pthread_mutex_unlock( &mutex );
   return /* ... */;
}
```



9.14 Critisism of monitors

• Locking in nested call does not release outer monitor(s)

Classroom task: Why? (Answer: These are not necessarily the safe places)

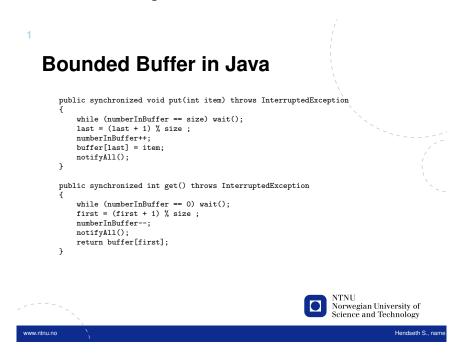
- -> Destroys Composition we cannot build supermodules from modules.
- Deadlocks & Race Condisions as with semaphores (Just for slightly larger systems)
- What if we need other synchronization than Condition variables?
- Conflict: High vs. low level of abstraction?
- More complex than semaphores...

9.15 Java: Synchronized Objects

Java:

- \bullet Object as monitor (one lock per object/class)
- synchronized methods locks
 - may have also non-synchronized methods
 - But this is very different from r/w locks
- calls (also from the outside!): o.synchronized(){...} also locks (terrible! breaks encapsulation completely. Use either or!)
- No explisit Condition variable (more as suspend & resume again, but safe(r) this time, since wait is always called from a protected region)
- wait() releases only the inner lock
- notify() Awakens **one** process.
- notifyAll() Awakens all waiting processes
- May also be awakened by t.interrupt() -> interruptedException

9.16 Java Example: Bounded buffer



9.17 Java discussion points:

Notify vs NotifyAll

- Why notifyAll & while loops around wait?
- (Ref. Monitors awakeniging one, but POSIX awakeing all.)
- "Conditions" are nameless in java, if there are more conditions for blocking, we would not know that notify wakes the right one.
- Conclusion: "Always" use notifyAll and while loops!

Inheritance Anomaly:

- What a sweet and obvious thought to integrate the monitor into the object.
- Unfortunately inheritance works surprisingly badly.

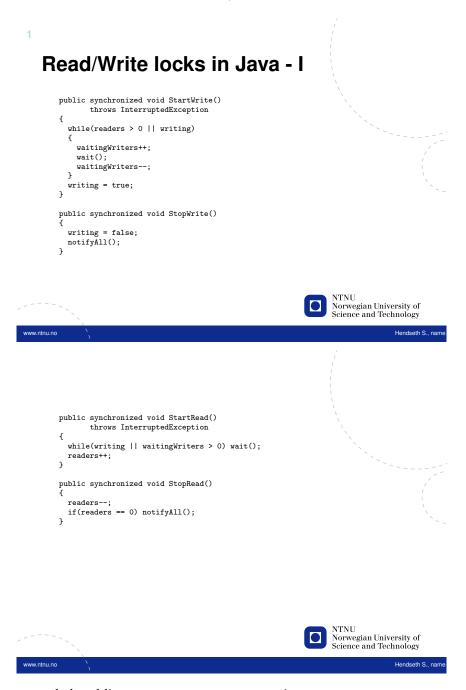
- synchronization and inheritance
- http://wisnesky.net/anomaly.pdf
- http://eprints.soton.ac.uk/262297/1/anomalySurvey.pdf
- Rule of thumb: do not use notify()
 - you have no control on who is awakened.
 - * -> Possible deadlock if more than one **type** of waiting?
 - we do now know because of inheritance/extensions/maintenance
- Design-pattern: while(!cond) wait(); % monitor forhindrer race condision. & notifyAll();
- (This is more flexible than condition variables...?)
- Inheritance Anomaly
 - Can be solved by prev point, but not always (eks p269-270)

(Book has also example of R/W locks without notifyAll...)

From http://wisnesky.net/anomaly.pdf Simply put, the anomaly is a failure of inheritance and concurrency to work well with each other, negating the usefulness of inheritance as a mechanism for code-reuse in a concurrent setting.

Sverre: The union of **classes** and **monitors** is not perfect: Inheritance and reuse means something for classes but not for monitors!

9.18 Java Example: Read/Write locks in Java



... and the obligatory comment on starvation...

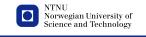
9.19 Ada: Protected Objects

ADA: Protected Objects.

- Modules (as with Monitors)
- Functions (readlocks)
- Procedures (writelocks)
- Entries w. Guards (as with CCR)
- Requeue (know semantics abition here; no pseudocode)
- Entry Families (know semantics abition here; no pseudocode)

Read-Write locks in Ada

protected body Shared_Data_Item is
 function Read return Data_Item is
 begin
 return The_Data;
 end Read;
 procedure Write (New_Value : in Data_Item) is
 begin
 The_Data := New_Value;
 end Write;
end Shared_Data_Item;



R/W-locks in ada are trivial: but then:

- We kind of assume that writers should have priority, that reading is more common than writing?
- (B&W slides also say that this cannot be used from another protected object because of blocking???)

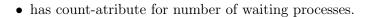
- Blocking from the inside is an error! (Waiting on other monitor is not blocking Waiting on guard is...)
- Guards tests only private variables. -> reevaluation only when exiting entry&proc.

Bounded Buffer in Ada

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Bounded Buffer i Ada (1 slide! quite elegant!)

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Classroom question: What is the type of First and Last?

BB in Ada - the Bounded Buffer type

```
Buffer_Size : constant Integer :=10;
type Index is mod Buffer_Size;
subtype Count is Natural range 0 .. Buffer_Size;
type Buffer is array (Index) of Data_Item;

protected type Bounded_Buffer is
entry Get (Item : out Data_Item);
entry Put (Item : in Data_Item);
private
First : Index := Index'First;
Last : Index := Index'Last;
Num : Count := 0;
Buf : Buffer;
end Bounded_Buffer;
```



Classroom question: How is it with composition? Can we have Protected objects using other protected objects?

- Waiting from inside is a runtime error: An exception will be thrown (the third alternative to "releasing/not releasing the outer monitor")
- But more elegant than catching the exception is to select on the entry!

A barrier in Ada

```
protected body Blocker is
  entry Proceed when
     Proceed'Count = 5 or
     Release is
begin
  if Proceed'Count = 0 then
     Release := False;
  else
     Release := True;
  end if;
end Proceed;
end Blocker;
```



A barrier in Ada?

Classroom challenge: Does it work?

- It is one-time.
- has "Requeue" for "temporal release" "Give up and retry, possibly in another queue"
- has "entry families" parameterized entries.

Slides on this are alternatives:

- Entry Families 63-64
- Readers/Writers problem with priorities to writers: 74-78

9.20 Ada Example: Update/Modify/Lock

Discussion: In Ada, with a lot of readers (calling functions/entries) and writers (calling procedures). How is the queue organized?

Sverres answer: FIFO. Why? To avoid starvation.

But if we want something else? Two functions with other priorities?

Here: Update should have priority over Modify. (For good measure; add lock)

Update/Modify/Lock in Ada - I

```
protected Resource_Manager is
  entry Update(...);
  entry Modify(...);
  procedure Lock;
  procedure Unlock;
  private
  Manager_Locked : Boolean := False;
  ...
end resource_manager;
```



(Note: The lock part of this is really "server state")

Update/Modify/Lock in Ada - II

```
protected body Resource_Manager is
entry Update(...) when not Manager_Locked is
begin ... end Update;
entry Modify(...) when not Manager_Locked and
Update'Count = 0 is
begin ... end Modify;
procedure Lock is
begin Manager_Locked := True; end Lock;
procedure Unlock is
begin Manager_Locked := False; end Unlock;
end Resource_Manager;
```

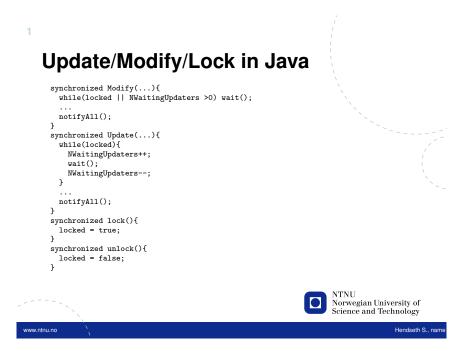


(So, the possible weaknesses of the default R/W locks of Ada can be easily compensated for.)

Classroom task:

• Would it be significally different if given allocate_{update} & allocate_{modify} form? (Answer: No, add a busy flag, and the apropriate guards.)

9.21 Java Example: Update/Modify/Lock - Make it in Java



Class challenge:

- What is best Ada or Java?
- How is the lock calls queued? Do we dare making lock and unlock not syncronized?
- For POSIX what would be the difference (from Java)?

9.22 Java example: Request Parameters

Handling request parameters in Java

```
public class ResourceManager
{
   private final int maxResources = ...;
   private int resourcesFree;

   public ResourceManager() { resourcesFree = maxResources; }

   public synchronized void allocate(int size)
   {
      while(size > resourcesFree) wait();
      resourcesFree = resourcesFree - size;
   }

   public synchronized void free(int size)
   {
      resourcesFree = resourcesFree + size;
      notifyAll();
   }
}
```



Class Question:

- Out of many requests; Who will get the allocation done?
- How to avoid starvation...

9.23 Ada: Request parameters

ADA: Not so easy: Cannot test on parameters in guards.

- Option 1: "Double Interaction", Two calls: The first nonblocking announcement, the second blocking
 - Not atomic: makes error recovery difficult, destroys side boundary of AA.
 - Does this even work at all in Ada? B&W does not give convincing example.
- Option 2: Requeue

- Requeue helps slightly (Only server side sees more states.).
- Option 3: Entry families instead of parameters?

Requeue:

- Leaves the calling code there and then
- Can be another entry (must have the same parameters)
- Can be in another object
- Abortable while blocked? Can be controlled "with abort"

Entry families:

• An indexed "array" of entries.

9.24 Ada example with requeue I

Request parameters in Ada with Requeue - I

```
type Request_Range is range 1 .. Max;
type Resource ...;
type Resource si array(Request_Range range <>) of Resource;

protected Resource_Controller is
   entry Request(R : out Resources; Amount: Request_Range);
   procedure Release(R : Resources; Amount: Request_Range);
private
   entry Assign(R : out Resources; Amount: Request_Range);
Free : Request_Range := Request_Range'Last;
New_Resources_Released : Boolean := False;
To_Try : Natural := 0;
end Resource_Controller;
```



Request parameters in Ada with Requeue - II

```
protected body Resource_Controller is
entry Request(R : out Resources; Amount: Request_Range)
    when Free > 0 is
begin
    if Amount <= Free then Free := Free - Amount;
else requeue Assign; end if;
end Request;

procedure Release(R : Resources; Amount: Request_Range) is
begin
    Free := Free + Amount;
-- free resources
    if Assign'Count > 0 then
        To_Try := Assign'Count;
        New_Resources_Released := True;
end if;
end Release;
```

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Request parameters in Ada with Requeue - III



Class challenge:

• Who is awoken?

- There are some ugly assumptions making this code work: Which?
 - it assumes FIFO queuing!
 - it assumes no new requests while handling a round of assigns!

(Both may be true, but...)

9.25 Ada example with requeue II

Request parameters in Ada with Requeue - 2-I



Request parameters in Ada with Requeue - 2-II



Request parameters in Ada with Requeue - 2-III

```
procedure Release (...) is
begin
   Free := Free + Amount;
   Queued := 0;
end Release;
end Resource_Controller;
```



Class challenge:

• Who, now, gets the resource?

• Which assumptions are made this time?

9.26 Ada example with entry families

Request parameters in Ada with Entry Families - I

```
package Resource_Manager is
   Max_Resources: constant Integer := 100;
   type Resource_Range is new Integer range
1..Max_Resources;
   subtype Instances_Of_Resource is
Resource_Range range 1..50;

   procedure Allocate(Size: Instances_Of_Resource);
   procedure Free(Size: Instances_Of_Resource);
end Resource_Manager;
```



Request parameters in Ada with Entry Families - II

```
package body Resource_Manager is

task Manager is
  entry Sign_In(Size : Instances_Of_Resource);
  entry Allocate(Instances_Of_Resource); -- family
  entry Free(Size : Instances_Of_Resource);
end Manager;

procedure Allocate(Size : Instances_Of_Resource) is
begin
  Manager.Sign_In(Size); -- size is a parameter
  Manager.Allocate(Size); -- size is an index
end Allocate;

procedure Free(Size : Instances_Of_Resource) is
begin
  Manager.Free(Size);
end Free:
```



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Request parameters in Ada with Entry Families - III

```
task body Manager is
  Pending : array(Instances_Of_Resource) of
  Natural := (others => 0);
  Resource_Free : Resource_Range := Max_Resources;
  Allocated : Boolean;
  begin
  loop
   select -- wait for first request
   accept Sign_In(Size : Instances_Of_Resource) do
        Pending(Size) := Pending(Size) + 1;
        end Sign_In;
   or
   accept Free(Size : Instances_Of_Resource) do
        resource_free := resource_free + size;
   end Free;
   end select;
```



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Request parameters in Ada with Entry Families - IV



Request parameters in Ada with Entry

Families - IV

```
-- now service largest request
   Allocated := False;
   for Request in reverse Instances_Of_Resource loop
   if Pending(Request) > 0 and
   Resource_Free >= Request then
        accept Allocate(Request);
        Pending(Request) := Pending(Request) - 1;
        Resource_Free := Resource_Free - Request;
        Allocated := True;
        exit; --loop to accept new sign-ins
        end if;
        end loop;
        exit when not Allocated;
        end loop;
        end loop;
        end Manager;
        end Resource_Manager;
```



9.27 Java example: LIFO Scheduling

- Scheduler does FIFO or "arbitrary"
- This is reasonable to avoid starvation
- Other orders of requests? We can make the queue ourselves

Order of Requests in Java

```
synchronous allocate(){
  if(busy){
    queue.insertFirst(myThreadId);
    while(busy || queue.getFirst() != myThreadId) wait();
    queue.removeFirst();
  }
  busy = true;
  // notifyAll() //? If more can be allocated at the same time.
}

synchronous free(){
  busy = false;
  notifyAll();
```



This one is a keeper - can implement any kind of orders and priorities by changing queue sorting.

- All RC problems can be transformed to server state.
 - We do not **have** to use the schedulers queue
 - Solution: Make our process queue the server state
 - ADA sometimes demands "double interaction" to get the same result.

9.28 Summary

Classroom challenge:

- So, What do you like the most? (Compare bounded buffers? Sverre: I like the ADA approach to composition, disallowing nested blocking)
- What would be the perfect synchronization mechanism? (I will propose an answer to this in the last lecture.)

10 Deadlocks

10.1 Learning Goals

Deadlocks

Understanding deadlocks, conditions for deadlocks, how they can be prevented, avoided and detected/handled.

10.2 Deadlocks

Necessary Conditions:

- 1. Mutual Exclusion
- 2. hold & Wait
- 3. No Preemption
- 4. Circular Wait

Deadlock **Prevention** (removing one of the conditions)

- 1. Optimistic concurrency control
- 2. allocate all resources at once.
 - efficiency?
 - starvation?
- 3. Preemption

- 4. Global allocation order
- 5. In addition: Global analysis prove absence of deadlocks

Deadlock **Avoidance**:

- Resource allocation (Bankers algorithm) (Can block on free resources!) (Assumes known allocation patterns!)
- Scheduling algorithms (Priority Ceiling)
 - efficiency?

Deadlock detection & Recovery:

- Sometimes it is impossible to avoid the chance of deadlock:
 - The user have scripting abilities (like in a large process control plant)
 - The operations are generated at runtime (travel agency)
 - The participants are dynamic (moving agents)
 - ...

Detection:

- Who owns and asks for what (detect circles)
- Timeout/Watchdog

Recovery:

- Breaking mutual exclusion (-> Inconsistent system)
- Preemption (Ex. -> Forward Error Recovery)
- Abort of thread or Atomic Action (-> backward E.R.)
- remember fairness

11 Modeling of concurrent programs.

11.1 Its learning call

The the lectures are almost over; Two-Three small subjects are left; This weeks lectures will be on formal modeling of concurrent systems, leading to both an understanding on how systems can be analyzed for deadlocks and race-conditions, and motivating thread interaction by synchronous communication.

Then after easter Amund Skavhaug will present a basic introduction to scheduling. (Remember that a predictable scheduler is a foundation for making real-time systems).

If there is some spare time anywhere I will spend it on looking a bit closer at deadlocks.

I really recomend attending these lectures:

- The subject matter is challenging: We are attacking thread interaction from the formal modelling angle, and we will be using a modelling language which is new to most of you and that you need to solve the exercise.
- Formal modelling is tough stuff, there is not much written curriculum on this matter; What you need on the exam is basicly defined by the exercise (!)

Check exam tasks 2 from last spring, and 4 from the continuation exam.

After easter Amund Skavhaug will be talking about Scheduling. I have also a very short part going a bit deeper on how to handle deadlocks.

Do not miss the opportunity to give also this week a good start!

11.1.1 Last years call

Out of all the lectures this spring these are the ones I would most strongly recommend you not to miss. These three hours represents, together with the guest lecture next Friday and the exercise, the "3rd part" of the course and there is not much written curriculum available. What we are after is the skills necessary to do the exercise.

However, the subject matter is challenging: We are attacking thread interaction from the formal modelling angle, and we will be using a modelling language which is new to most of you and that you need to solve the exercise.

The lecture tomorrow will be my last one. Next Friday will be a guest lecture. Unfortunately the last exercise will be scheduled in the week after easter. (but can be done on any PC, so feel free to work with it earlier :-)) See you!

11.2 Learning goals

Learning Goals, Modelling of concurrent programs

• An understanding of how using messagebased synchronization leads to a very different design than shared variable synchronization.

Ability to:

- Model, in FSP and by drawing transition diagrams, simple programs (semaphore-based or messagepassing).
- Draw very simple compound (for paralell processes) transition diagrams
- Sketch simple messagepassing programs
- An understanding of how using messagebased synchronization leads to fewer transitiondiagram states than shared variable synchronization.
- Understanding the terms deadlock and livelock in context of transition diagrams.

11.3 Motivation

(Ref. shared variable synchronization): Great, this is real-time programming.

Back to the beginning: we were making a RT kernel. We introduced preemption and published the Suspend/resume calls, and since then we have been struggling. What happened?

- We had RT demands on our system.
- Divided our system into one thread per RT demand (under preemption!)
- (Great, Modules)
- The threads were (by definision) cooperating -> Interaction was necessary.
- We chose spin locks -> suspend/resume -> Semaphores -> CCR -> Monitors...
- We got:
 - A system that was difficult to analyze/maintain
 - That had seldomly occurring and hard-to-find bugs
 - Bad composition/scalability (Neither threads nor monitors compose and deadlock and timing analysis is always global)

Have we chosen a good abstraction?

We have

- Questionable transformation from Timing demands -> Priorities
- Timing considerations always global -> does not scale
- Synchronization mechanisms are kernel mechanisms: more or less global entities -> global analysis necessary (like deadlock) -> does not scale.
- Monitors, threads and Atomic actions are ways of dividing a system into modules that do not compose well.

```
t1(){
   i=i+1;   i=i-1;
}
```

What does this program **do**?

Class challenge: We have been talking about thread interaction like a fact of life, because og the threads cooperating rather than competing.

• Give example of possible interactions (as seen from the conceptual side.)

Is communication closer to what we need than synchronization?

B&W Chapter 6 is "Messagebased synchronization and communication". Is there a fundamental misunderstanding here? What is messagebased synchronization?

What does the word "priority" mean?

- Transforming timing demands into "priorities"
- We would like to transform back to prove schedulability...

How can we do better?

How can we find out how to do better?

Rather than discussing maintainability (this time), expressive power, abstractions, we will briefly look at formal modeling.

11.4 How to model a concurrent program

So, what to do: Start from completely new angle (as different from just exploring message-based systems.)

Sverres Hypothesis:

- Systems that are simple to model yields simple models will be simple to maintain.
- How model (wrt. deadlock, livelock, liveness, safety, progression, etc. race condisions.)?
- How program such that models become simple. (Sverre hopes for maintainability)

(The program itself is, of course a model. Too complex though.)

Example: The classical deadlock:

```
T1:
   while(1){
     Wait(A)
     Wait(B)
     ...
     Signal(A)
     Signal(B)
}
```

Modelling as processes that participates in events.

(Oops, the semaphores are modeled as processes... What?)

Drawing the transition diagrams.

Terms:

- CSP, Communicating Sequential Processes: "Branch of mathematics" av Hoare,
 - We can model a system and prove properties

FSP, Finite State Process: Subset of CSP LTSA, Labeled Transition System Analyzer: Java Applet that reads and analyses FSP-modeller.

FSP/CSP syntaks:

- Processes that participants in events
- events are global!
- prefixing "->": event -> Process

- rekursjon
- Choise "|"
- Paralelle prosesser "||"

Yield here but leave it on the blackboard.

- Partial processes ","
- indexes
- Parameters "P(N=5)": Just a constant
- if & guards (when)
- Labeling: a:SEM: Exchanges all event names (wait -> a.wait)
- Sharing: {a,b}::SEM : Both a.wait and b.wait becomes possible
- Relabeling "/" change name of one event

•

- constants, sets
- properties: a partial model that the system is checked against
- Trace: En mulig sekvens av hendelser
- Transisjonsdiagrammer

Terms:

- Deadlock: A state we cannot leave (STOP)
- Livelock: A subsett of states we cannot leave
- progression: the absense of livelocks
- liveness: what should happen happens sooner or later.
- safety: Something bad never happens

• Model checking: does the model of the system correspond to the model of the specification.

New example: The bounded buffer (view the slide) Class challenge: Make a model of the bounded buffer.

11.5 The modelchecking example:

```
| when n > 0 get \rightarrow BUF[n-1]).
||SAFETY = (BUF(4) ||SYSTEM(3)).
```

The number of states

Bufferlength	#States
2	23
3	38
4	53
5	68

Conceptual model of a bounded buffer:

The number of states: N+1

Demonstrate modelchecking.

Class challenge:

- Why is there too many states in the semaphore solution?
 - Something wrong with how to model?
 - The modelling abstraction level?
 - The way the bounded buffer was implemented...
- How should the BB be implemented to yield N+1 states?
- Which language primitives would we use?

OCCAM story, occams razor, OCCAM language made to make 1-to-1 models. Solves multithread programming. (like Go - if not using shared variables)

- So: How do we program so that the models get simple? (We whish for **events** that more threads participates in)
 - Ada entries (synch tow-way comm++)

- Synchroneous communication!
- Barriers (as the end boundary of an Atomic Action)

A new way of defining a process follows:

- Process: Participates in events
- event: sunch communication
- Composition: More processes constitutes a process
 - Internal states disappear
 - Internal communication disappears
 - =>Very nice scalability
- The primitive processes are tiny
 - => non preemptive scheduling becomes ok (and by non-preemptive scheduling a semaphore is just a flag)
 - =>small overhead and few demands on scheduler.

Solves the problems introduced by preepmtion. NB: Does not solve problems like deadlocks, but they do get far easier to spot since we have fewer states.

Occam-like implementation

```
process
boundedBuffer(channel put,get){
  while(1)
    select{
     n<N: c = read put; store c; n++;
     n>0: read get; retrieve c; reply with c; n--;
    }
}
```

How many states? N+1

The i=i+1-example, message based:

```
server(){
   i = 0;
   while(1){
      select()
        read ch1: i = i+1;
      read ch2: i = i-1;
   }
}
```

 ${f No}$ doubt that rest of the program cannot make this inconsistent: If t1 sends 10000 messages on ch1 and t2 sends 10000 messages on ch2 i will be 0 at the end.

NB: This module can be maintained!

How could we implement synch comm with semaphores?

OCCAM SHORTHAND

- ? reads
- ! writes
- The semaphores and the variable: A channel

Now "process diagrams" with process circles and channel arrows. Great modularity!. Ref. Øyvind Teigs relation to deadlocks.

The challenge for you:

- Step 1: Think about building program with server threads and messages
 - "While(true) select()..."
 - Use lots of such processes.

- prosesses is a better way than objects for dividing the system into modules.
- Step 2: Skip buffered communication (Block on write!)
 - Buffers should be used when you need them, conceptually.
 - In other situation you just consume ram to store old data rather than doing useful work. Creating slanted arrows in the sequence diagrams.
- Step 3: Choose fine-granular processes (& preemptive scheduling)

But unfortunaltely:

- Performance, closeness to hw/kernel
 - publishing suspend/resume calls was tempting for a reason!
- Need some messagepassing infrastructure library or similar. (and these make most often buffered communication "send and forget" is assumed simpler to understand???)
- Real-time: No schedulability proofs exist.

12 Scheduling

12.1 Learning Goals

The student should:

- Be able to prove schedulability using the utilization test and the response time analysis for simple task sets.
- Know and evaluate the assumptions underlying these proofs and what is proven.
- Understand the bounded and unbounded priority inversion problems.
- Understand how the ceiling and inheritance protocols solves the unbounded priority inversion problem.
- Understand how the ceiling protocol avoids deadlocks.

12.2 Its learning call

This week Amund Skavhaug will cover "Scheduling". Having more threads with responsibilities for their own timing demands will work only if the threads gets to run when they need to, and knowing how the scheduler works is necessary both for the programmer designing the system and for the final argumentation that the system works - the "schedulability proof".

Amund will be using the lecture slots both Monday and Tuesday.

Do not miss the opportunity to give also this week a good start!

13 Guest lecture Teig and course ending.

An old colleague of mine, Øyvind Teig from Autronica, will be lecturing tomorrow on his experiences from a long career as a embedded/industrial systems software developer. Øyvind has always been an advocate of the message passing style of multithread programming, derived from the theory of communicating sequential processes that we talked about before Easter.

Tuesday will be the course ending. No special agenda; you can propose topics to revisit and/or ask questions.

I hope you all can make it to the last week of lectures in the course. See you!

13.1 Øyvinds slides.

http://www.teigfam.net/oyvind/pub/pub.html