Proceso	Llegada	CPU	I/O (rec,ins,dur)	Prioridad	0	1	2	3
P1	0	9	(R1, 4, 2) (R2, 6, 3) (R1, 8, 3)	1	>1	2	3	4
P2	1	5	(R3, 3, 2) (R3, 4, 2)	2		^		
Р3	2	5	(R1, 4, 1)	3			^	
P4	3	7	(R2, 1, 2) (R2, 5, 3)	2				>
P5	5	5	(R1, 2, 3) (R3, 4, 3)	1				
TV	Q=3			R Qeue 1	1	1	5	1
				R Qeue 2	2	4	2	4
				R Qeue 3	3	3	3	

No Apropiativo

R1 Qeue	1	5	1	3
R2 Qeue	1	4	4	
R3 Qeue	2	5	2	

Proceso	Llegada	CPU	I/O (rec,ins,dur)	Prioridad	0	1	2	3
P1	0	9	(R1, 4, 2) (R2, 6, 3) (R1, 8, 3)	1	>1	2	3	4
P2	1	5	(R3, 3, 2) (R3, 4, 2)	2		^		
Р3	2	5	(R1, 4, 1)	3			>	
P4	3	7	(R2, 1, 2) (R2, 5, 3)	2				>
P5	5	5	(R1, 2, 3) (R3, 4, 3)	1				
TV	Q=3			R Qeue 1	1	1	5	1
				R Qeue 2	2	4	2	4
				R Qeue 3	3	3		

Apropiativo

4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
R1	R1				5	6	R2	R2	R2	7	8	R1	R1	R1			9<
1	2	3	R3	R3								4	R3	R3			
							1			R2	R2			2	3	4	
	>		1	2	R1	R1	R1	3	4	R3	R3	R3	5<				
5	1	5	1														
2	4	4															

4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21
R1	R1		5	6	R2	R2	R2	7	8	R1	R1	R1	9<				
1										2				3	R3	R3	
					1			R2	R2			2			3	4	5
	>1	2	R1	R1	R1	3	4	R3	R3	R3	5<						
5	1	5	1														
2	4	2	2	4													

22	23	24	25	26	27	28	29	30		TR	TE
										22	13
5<										22	17
		1	2	3			4	R1	5<	30	25
	5	R2	R2	R2	6	7<				26	19
										13	8
										22.6	16.4

22	23	24	25	26	27	28	29	30	31	TR	TE
										18	9
4	R3	R3	5<							25	20
	1	2				3	4	R1	5<	30	25
R2	R2	R2		6	7<					25	18
										11	6
										21.8	15.6