Proceso	Llegada	CPU	Prioridad	I/O (rec,ins,dur)	0	1	2	3	4	5	6
P1	0	4		(R1, 2, 1)	>1	2	R1			3	4<
P2	2	6		(R2, 3, 1) (R2, 5, 2)			>1	2	3	R2	
Р3	3	4						^			
P4	6	5		(R3, 1, 2) (R3, 3, 1)							>
P5	8	2									
FCFS			R Qeue		1	2	1	3	2	4	5

CPU oseosa por un instante

Proceso	Llegada	CPU	Prioridad	I/O (rec,ins,dur)	0	1	2	3	4	5	6
P1	0	4		(R1, 2, 1)	>1	2	R1		3	4<	
P2	2	6		(R2, 3, 1) (R2, 5, 2)			>1	2			
Р3	3	4						۸			1
P4	6	5		(R3, 1, 2) (R3, 3, 1)							>
P5	8	2									
RR TV	Q=2		R Qeue		1	2	1	3	2	4	3

b)

Proceso	Llegada	CPU	Prioridad	I/O (rec,ins,dur)	0	1	2	3	4	5	6
P1	0	4		(R1, 2, 3) (R1, 3, 2)	>1	2	R1	R1	R1		
P2	2	6		(R2, 3, 2)			>1	2	3	R2	R2
Р3	3	4		(R2, 2, 3)				^		1	2
P4	6	5		(R1, 1, 2)							>
P5	8	2									
FCFS			R Qeue		1	2	3	1	4	2	5
			R1 Qeue		1	1	4				
			R2 Qeue		2	3					

Proceso	Llegada	CPU	Prioridad	I/O (rec,ins,dur)	0	1	2	3	4	5	6
P1	0	4		(R1, 2, 3) (R1, 3, 2)	>1	2	R1	R1	R1		
P2	2	6		(R2, 3, 2)			>1	2			3
Р3	3	4		(R2, 2, 3)				>	1	2	R2
P4	6	5		(R1, 1, 2)							>
P5	8	2									
RR TV	Q=2		R Qeue		1	2	ᢋ	2	1	4	5
			R1 Qeue		1	1	4				
			R2 Qeue		3	2					

7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	TR	TE
															7	3
				4	5	R2	R2		6<						15	9
1	2	3	4<												8	4
						1	R3	R3		2	3	R3	4	5<	16	11
	>						1	2<							8	6
2	4														10.8	6.6

7	8	9	10	11	12	13	14	15	16	17	18	19	20	TR	TE
														6	2
	3	R2					4	5	R2	R2	6<			17	11
2			3	4<										9	5
		1	R3	R3					2	3	R3	4	5<	15	10
	>				1	2<								6	4
5	2	4	2	4										10.6	6.4

7	8	9	10	11	12	13	14	15	16	17	18	19	20	TR	TE
3	R1	R1					4<							15	11
		4	5	6<										10	4
R2	R2	R2						3	4<					14	10
	1		R1	R1						2	3	4	5<	15	10
	^				1	2<								6	4
1	3	4												12	7.8

7	8	9	10	11	12	13	14	15	16	17	18	19	20	TR	TE
3	R1	R1				4<								14	10
		R2	R2				4	5			6<			17	11
R2	R2			3	4<									10	6
	1		R1	R1					2	3		4	5<	15	10
	^	1	2<											3	1
3	1	2	4	2	4									11.8	7.6