

# VHDL

Boas referências da linguagem:

Livros (bibliografia):

- [DESCHAMPS, 2012] DESCHAMPS, J. P.; SUTTER, G. D.; CANTÓ E. Guide to FPGA implementation of arithmetic functions. Springer Science & Business Media; 2012, Apr 5.
- [CHU, 2011] CHU, PONG P. Embedded SoPC design with Nios II processor and VHDL examples. John Wiley & Sons, 2011.
- [SIMPSON, 2015] Simpson, Philip Andrew; FPGA Design: Best Practices for Team-based Reuse 2<sup>nd</sup> ed. Springer, 2015 Edition.

Sites:

- <http://esd.cs.ucr.edu/labs/tutorial/>
- <http://github.com/insper/Z01.1/wiki>