Intermediate Code

 ${\bf x}$ and ${\bf y}$ can either be variables or constants, used as operands to instructions. ${\bf t}$ is a destination variable.

t = x	Simple Assignment
t = load from x	Load from and store to memory
store x to addr y	
declare var t	Declare a new variable t.
allocate x bytes for var y	Allocate memory for y (used for allocating aggregate data
	structures e.g. arrays).
reference var x	A non-executable instruction used internally to mark a
	variable as live at this program point.
t = &x	Address-of operator
t = x	Bitwise NOT
t = !x	Logical noт
t = x * y	Multiplication
t = x / y	Division
t = x % y	Modulus
t = x + y	Addition
t = x - y	Subtraction
$t = x \ll y$	Left-shift
$t = x \gg y$	Right-shift (signed-extending for signed x, zero-filling for
	unsigned x)
t = x & y	Bitwise and
$t = x \mid y$	Bitwise or
$t = x \wedge y$	Bitwise xor
t = x && y	Logical and
t = x y	Logical or
t = x < y	Less-than comparison
t = x > y	Greater-than comparison
t = x <= y	Less-than or equal comparison
t = x >= y	Greater-than or equal comparison
t = x == y	Equality comparison
t = x != y	Not equal comparison
$t = call f(p_1, p_2, \ldots)$	Call function f with parameters p _i (either variables or
	constants)

tail-call $f(p_1, p_2,)$ return [x]	Call function f and return the result from the current function Return from the current function. The return value x is optional.
label <1> br <1>	Attach a label to the current program point (immediately before the next instruction). Unconditional branch Conditional branch executed if operands are equal.
<pre>br <1> if x == y br <1> if x != y</pre>	Conditional branch; executed if operands are equal. Conditional branch; executed if operands are not equal.
t = & <sid></sid>	Static address of the string literal with id <sid></sid>
$t = (i8 \rightarrow i16) x$ $t = (i8 \rightarrow u16) x$ $t = (u8 \rightarrow u16) x$ $t = (u8 \rightarrow u16) x$	Char promotions
$t = (i16 \rightarrow i32) x$ $t = (u16 \rightarrow i32) x$	Promotions to signed integer
$t = (i16 \rightarrow u32) x$ $t = (u16 \rightarrow u32) x$ $t = (i32 \rightarrow u32) x$	Promotions to unsigned integer
$t = (i32 \rightarrow i64) x$ $t = (u32 \rightarrow i64) x$	Promotions to signed long
$t = (i32 \rightarrow u64) x$ $t = (u32 \rightarrow u64) x$ $t = (i64 \rightarrow u64) x$	Promotions to unsigned long
$t = (u32 \rightarrow f32) x$ $t = (i32 \rightarrow f32) x$ $t = (u64 \rightarrow f32) x$ $t = (i64 \rightarrow f32) x$	Integer to float conversions
$t = (u32 \rightarrow f64) x$ $t = (i32 \rightarrow f64) x$ $t = (u64 \rightarrow f64) x$ $t = (i64 \rightarrow f64) x$	Integer to double conversions
$t = (f32 \rightarrow f64) x$	Float to double promotion
$t = (f64 \rightarrow i32) x$	Double to int conversion
$t = (i32 \to i8) x$ $t = (u32 \to i8) x$ $t = (i64 \to i8) x$ $t = (u64 \to i8) x$ $t = (i32 \to u8) x$ $t = (u32 \to u8) x$ $t = (u34 \to u8) x$	Integer truncation

$t = (u64 \rightarrow u8) x$ $t = (i64 \rightarrow i32) x$ $t = (u64 \rightarrow i32) x$	
$t = (u32 \rightarrow *) x$ $t = (i32 \rightarrow *) x$ $t = (* \rightarrow i32) x$	Conversions between integer and pointer
nop	No-op
<pre>break <loop_block_id> continue <loop_block_id> end handled <multiple_block_id></multiple_block_id></loop_block_id></loop_block_id></pre>	Control-flow instructions inserted by the Relooper algorithm as it processes branch instructions.
<pre>if x == y {} else {} if x != y {} else {}</pre>	Conditional control flow instructions with nested instructions for each branch. These are only inserted by the Relooper algorithm, to replace a conditional branch with conditionally setting the label variable and then branching.