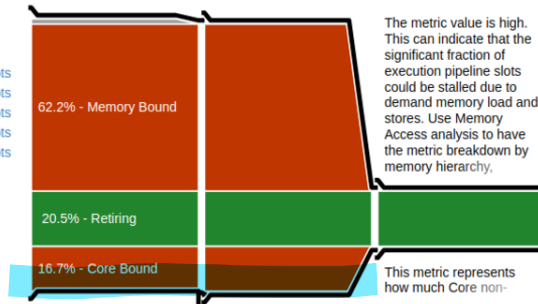


Elapsed Time : 8.118s

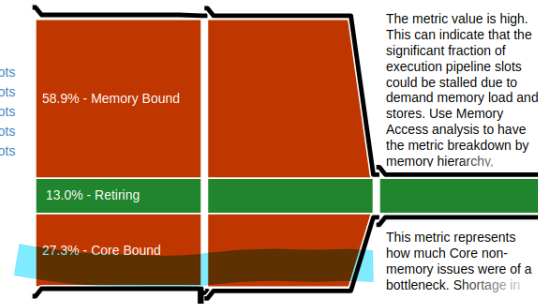
Clockticks:	18,414,000,000
Instructions Retired:	14,182,500,000
CPI Rate :	1.298
Retiring :	20.5% of Pipeline Slots
Front-End Bound :	1.9% of Pipeline Slots
Bad Speculation :	0.0% of Pipeline Slots
Back-End Bound :	78.8% of Pipeline Slots
Memory Bound :	62.2% of Pipeline Slots
L1 Bound :	12.3% of Clockticks
DTLB Overhead :	0.0% of Clockticks
Loads Blocked by Store Forwarding :	0.0% of Clockticks
Lock Latency :	0.0% of Clockticks
Split Loads :	12.5% of Clockticks
4K Aliasing :	0.8% of Clockticks
FB Full :	100.0% of Clockticks
L2 Bound :	2.6% of Clockticks
L3 Bound :	3.1% of Clockticks
DRAM Bound :	37.4% of Clockticks
Memory Bandwidth :	60.0% of Clockticks
Memory Latency :	20.0% of Clockticks
Local Memory :	51.2% of Clockticks
Remote Memory :	0.0% of Clockticks
Remote Cache :	0.0% of Clockticks
Store Bound :	0.0% of Clockticks
Core Bound :	16.7% of Pipeline Slots
Divider :	0.0% of Clockticks
Port Utilization :	14.8% of Clockticks
Cycles of 0 Ports Utilized :	23.7% of Clockticks
Cycles of 1 Port Utilized :	5.8% of Clockticks
Cycles of 2 Ports Utilized :	6.4% of Clockticks
Cycles of 3+ Ports Utilized :	7.7% of Clockticks
Vector Capacity Usage (FPU) :	50.0%
Average CPU Frequency :	2.3 GHz
Total Thread Count:	1
Paused Time :	0s



This diagram represents inefficiencies in CPU usage. Treat it as a pipe with an output flow equal to the "pipe efficiency" ratio: (Actual Instructions Retired)/(Maximum Possible Instruction Retired). If there are pipeline stalls decreasing the pipe efficiency, the pipe shape gets more narrow.

Elapsed Time : 37.575s

Clockticks:	85,451,500,000
Instructions Retired:	52,297,000,000
CPI Rate :	1.634
Retiring :	13.0% of Pipeline Slots
Front-End Bound :	0.3% of Pipeline Slots
Bad Speculation :	0.5% of Pipeline Slots
Back-End Bound :	86.1% of Pipeline Slots
Memory Bound :	58.9% of Pipeline Slots
L1 Bound :	15.6% of Clockticks
DTLB Overhead :	100.0% of Clockticks
Loads Blocked by Store Forwarding :	0.0% of Clockticks
Lock Latency :	0.0% of Clockticks
Split Loads :	0.0% of Clockticks
4K Aliasing :	1.1% of Clockticks
FB Full :	100.0% of Clockticks
L2 Bound :	2.0% of Clockticks
L3 Bound :	3.2% of Clockticks
DRAM Bound :	28.8% of Clockticks
Memory Bandwidth :	18.7% of Clockticks
Memory Latency :	42.2% of Clockticks
Local Memory :	65.0% of Clockticks
Remote Memory :	0.0% of Clockticks
Remote Cache :	0.0% of Clockticks
Store Bound :	17.5% of Clockticks
Core Bound :	27.3% of Pipeline Slots
Divider :	0.0% of Clockticks
Port Utilization :	31.0% of Clockticks
Cycles of 0 Ports Utilized :	29.2% of Clockticks
Cycles of 1 Port Utilized :	4.5% of Clockticks
Cycles of 2 Ports Utilized :	4.0% of Clockticks
Cycles of 3+ Ports Utilized :	4.9% of Clockticks
Vector Capacity Usage (FPU) :	25.0%
Average CPU Frequency :	2.3 GHz
Total Thread Count:	1
Paused Time :	0s



This diagram represents inefficiencies in CPU usage. Treat it as a pipe with an output flow equal to the "pipe efficiency" ratio: (Actual Instructions Retired)/(Maximum Possible Instruction Retired). If there are pipeline stalls decreasing the pipe efficiency, the pipe shape gets more narrow.