Left Panel

Elapsed Time ⊘ : 8.043s

CPU Time ⊘ :	7.993s
Memory Bound ⊘ :	**60.7%**⚑ **of Pipeline Slots**
L1 Bound ⊘ :	9.7%⚑ of Clockticks
L2 Bound ⊘ :	2.9% of Clockticks
L3 Bound ⊘ :	3.2% of Clockticks
DRAM Bound ⊘ :	**39.0%**⚑ **of Clockticks**
Store Bound ⊘ :	0.8% of Clockticks
NUMA: % of Remote Accesses ⊘ :	0.0%
UPI Utilization Bound ⊘ :	0.0% of Elapsed Time
Loads:	5,196,547,840
Stores:	775,053,300
LLC Miss Count ⊘ :	**14,763,510**
Average Latency (cycles) ⊘ :	46
Total Thread Count :	1
Paused Time ⊘ :	0s

Platform Diagram

DRAM 0.1% → SOCKET 0 — Average Physical Core Utilization ⊘ : 0.0% (0.000 out of 24)
NUMA: % of Remote Accesses ⊘ : 0.0%
UPI 2.1%
SOCKET 1 — Average Physical Core Utilization ⊘ : 4.8% (1.143 out of 24)
NUMA: % of Remote Accesses ⊘ : 0.0%
9.7% DRAM

Right Panel
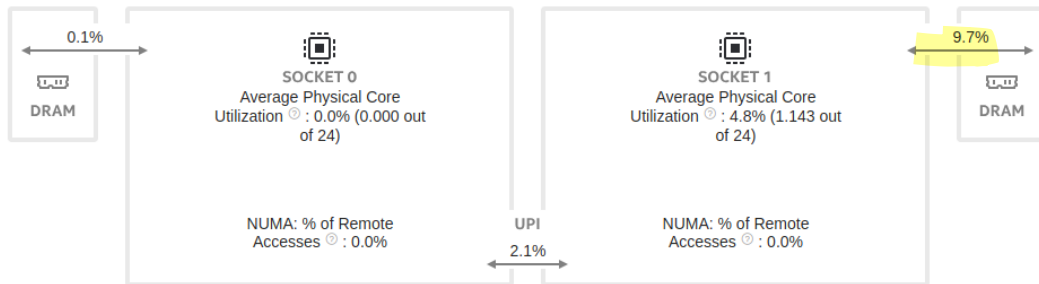
Elapsed Time ⊘ : 37.684s

CPU Time ⊘ :	37.408s
Memory Bound ⊘ :	**58.5%** **of Pipeline Slots**
L1 Bound ⊘ :	13.9%⚑ of Clockticks
L2 Bound ⊘ :	1.6% of Clockticks
L3 Bound ⊘ :	3.5% of Clockticks
DRAM Bound ⊘ :	**28.9%**⚑ **of Clockticks**
Store Bound ⊘ :	20.0%⚑ of Clockticks
NUMA: % of Remote Accesses ⊘ :	0.0%
UPI Utilization Bound ⊘ :	0.0% of Elapsed Time
Loads:	14,917,179,544
Stores:	4,492,490,656
LLC Miss Count ⊘ :	**286,445,444**
Average Latency (cycles) ⊘ :	54
Total Thread Count :	1
Paused Time ⊘ :	0s

Platform Diagram

DRAM 0.1% → SOCKET 0 — Average Physical Core Utilization ⊘ : 0.0% (0.000 out of 24)
NUMA: % of Remote Accesses ⊘ : 0.0%
UPI 0.3%
SOCKET 1 — Average Physical Core Utilization ⊘ : 4.8% (1.156 out of 24)
NUMA: % of Remote Accesses ⊘ : 0.0%
2.4% DRAM