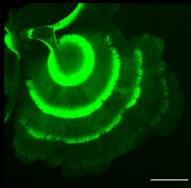


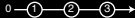
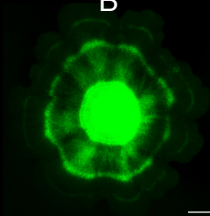
Full circuit



A



B



C

