

GATE input voltage is LOW then P-channel MOSFET connected in the circuit will be turned ON letting VBAT flow to LDO

The diagram illustrates a power management circuit. A P-channel MOSFET (Q2, Si2301CDS) acts as a switch between +VBUS and the LDO input. The MOSFET's source is connected to +VBUS, and its drain is connected to +VIN through a diode (D1). The gate of the MOSFET is controlled by a voltage divider consisting of a 10K resistor (R10) connected to +VBUS and a 1N5817 diode (D5) connected to GND. The LDO (U2, PWR_FLMCP1700-3302E_SOT23) is configured with its input (VI) connected to +VIN, its ground (GND) connected to GND, and its output (VO) connected to +3V3. Two 10uF capacitors (C1 and C2) are used for input and output filtering. A diode (D2) is also shown connected from +3V3 to the LDO input.

[illegible]

Diagram showing the pin connections for CN1:

- SWDIO (Orange) connects to Pin 1
- SWCLK (Orange) connects to Pin 3
- SWO ?? (Orange) connects to Pin 5
- MCU_RST (Green) connects to Pin 9
- +3V3 (Red) connects to Pin 2
- GND (Blue) connects to Pin 10

Board: HX JN1.27-2X5 TP H4.9

[illegible]

The image displays three circuit diagrams, each showing a different method for connecting a 10k resistor (R2, R21, or R4) to a microcontroller (MCU_RST) and a 3V3 supply. Each diagram includes a switch (SW1, SW2) and a ground connection.

- Top Diagram:** Shows a 10k resistor (R21) connected to a 3V3 supply. The other end of the resistor is connected to the MCU_RST pin. A switch (SW3) is connected between the 3V3 supply and the resistor. The switch has three additional pins (3, 4, 5) connected to ground (GND).
- Bottom Left Diagram:** Shows a 10k resistor (R2) connected to ground (GND). The other end of the resistor is connected to the MCU_RST pin. A switch (SW2) is connected between the MCU_RST pin and the resistor. The switch has three additional pins (3, 4, 5) connected to ground (GND).
- Bottom Right Diagram:** Shows a 10k resistor (R4) connected to ground (GND). The other end of the resistor is connected to the MCU_RST pin. A switch (SW1) is connected between the MCU_RST pin and the resistor. The switch has three additional pins (3, 4, 5) connected to ground (GND).

SM04B-SRSS-TB_1F_SN