

GATE input voltage is LOW then P-channel MOSFET connected in the circuit will be turned ON letting VBAT flow to LDO

The diagram illustrates a P-channel MOSFET (Q2, Si2301CDS) used as a switch. The gate of the MOSFET is connected to +VBUS through a 1N5817 diode (D5) and to +VBAT through a 10K resistor (R10). The source of the MOSFET is connected to +VBAT, and the drain is connected to the input of the LDO (U2, PWR\_FLMCP1700-3302E\_SOT23). The LDO output is +3V3. The LDO ground is connected to the MOSFET source. Input capacitors C1 and C2 are 10uF.

Diagram illustrating the pin connections for the HX JN1.27-2X5 TP H4,9 module (labeled CN1).

The module has 10 pins. The connections are as follows:

- Pin 1: SWDIO
- Pin 3: SWCLK
- Pin 9: MCU\_RST
- Pin 2: +3V3
- Pin 10: GND
- Pins 4, 6, 8, and 10 are also connected to GND.

The module is labeled HX JN1.27-2X5 TP H4,9.

The diagram illustrates the USB\_C\_Replacement\_USB2.0 module. It features a yellow rectangular component with various pins and connections. On the left side, pins A4 (+VBUS), A5 (CC1), B5 (CC2), A7 (D-), B7 (D-), A6 (D+), B6 (D+), A8 (SBU1), and B8 (SBU2) are labeled. On the bottom side, pins A1 (GND) and B1 (SHIELD) are labeled. Two 5.1k resistors, R1 and R20, are connected between pins A5 and A6 to ground (GND). The module is labeled 'USB\_C\_Replacement\_USB2.0' on the right. A USB symbol and a shield symbol are also present on the module's face.

[illegible][illegible]

SM04B-SR5S-TB\_LF\_\_SN\_

Diagram of a 7-pin connector J3 (Conn\_01x07) with the following pin assignments:

- Pin 1: P1.00
- Pin 2: P0.22
- Pin 3: P0.20
- Pin 4: P0.26
- Pin 5: P0.13
- Pin 6: +3V3
- Pin 7: GND