

GATE input voltage is LOW then P-channel MOSFET connected in the circuit will be turned ON letting VBAT flow to LDO

The diagram illustrates a P-channel MOSFET controlled LDO circuit. The MOSFET (Q2, Si2301CDS) is used to switch the input voltage (+VIN) to the LDO (U2, PWR_FLMCP1700-3302E_SOT23). The MOSFET's source is connected to +VBUS, and its gate is connected to +VIN through a 10K resistor (R10). The drain of the MOSFET is connected to the input of the LDO. The LDO output is +3V3. The MOSFET's gate is also connected to a battery cell (BT1) through a diode (D5, 1N5817) to ground. The LDO's ground is connected to a 10uF capacitor (C2) and the MOSFET's source is connected to a 10uF capacitor (C1).

U1
E73-2G4M08S1C

Pin connections:

- 1: +3V3
- 2: SDA
- 3: SCL
- 4: EPD_RST
- 5: GND
- 6: GND
- 7: GND
- 8: GND
- 9: GND
- 10: GND
- 11: GND
- 12: GND
- 13: GND
- 14: GND
- 15: GND
- 16: GND
- 17: GND
- 18: GND
- 19: GND
- 20: GND
- 21: GND
- 22: GND
- 23: DCH
- 24: Y2
- 25: RTC_Crystal_32MHz
- 26: RTC_Crystal_32MHz
- 27: MCU_RST
- 28: +VBUS
- 29: +V3V3
- 30: +V3V3
- 31: +V3V3
- 32: +V3V3
- 33: +V3V3
- 34: +V3V3
- 35: +V3V3
- 36: +V3V3
- 37: +V3V3
- 38: +V3V3
- 39: SW1
- 40: SW2
- 41: SWCLK
- 42: SWDIO
- 43: SWDIO

Diagram illustrating the pinout for the HX JN1.27-2X5 TP H4.9 connector. The connector has 10 pins, numbered 1 through 10. The connections are as follows:

- Pin 1: SWDIO
- Pin 3: SWCLK
- Pin 5: SWO ??
- Pin 9: MCU_RST
- Pin 2: +3V3
- Pin 10: GND

Pins 4, 6, 8, and 9 are marked with an 'X' and are not connected.

The diagram shows the USB_C_Replacement_USB2.0 module. It features a yellow rectangular footprint with a USB symbol and a shield icon. The module has the following pins and connections:

- Top Pins:**
 - A4: VBUS (connected to +VBUS supply)
 - A5: CC1
 - B5: CC2
- Left Pins:**
 - A7: D- (connected to USB_D-)
 - B7: D- (connected to USB_D-)
 - A6: D+ (connected to USB_D+)
 - B6: D+ (connected to USB_D+)
- Bottom Pins:**
 - A8: SBU1 (marked with an 'X')
 - B8: SBU2 (marked with an 'X')
- Internal Connections:**
 - Resistor R1 (5.1k) connects A4 to GND.
 - Resistor R20 (5.1k) connects B5 to GND.
- Connector J1:**
 - A1: Connected to GND.
 - S1: Connected to GND.

[illegible]

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