

GATE input voltage is LOW then P-channel MOSFET connected in the circuit will be turned ON letting VBAT flow to LDO

The diagram illustrates a circuit for switching an LDO. A P-channel MOSFET (Q2, Si2301CDS) is used to control the LDO's input. The MOSFET's gate is connected to +5V through a 10K resistor (R10). The MOSFET's source is connected to +VIN, and its drain is connected to the LDO input (VI) through a diode (D5, 1N5817). The LDO (U2, PWR_FLMCXP1700-3302E_SOT23) has its gate connected to +VIN and its output (V0) connected to +3V3. The LDO input is also connected to +VIN through a diode (D5, 1N5817). The LDO output is connected to +3V3 through a diode (D5, 1N5817). The LDO input is also connected to +VIN through a diode (D5, 1N5817). The LDO output is connected to +3V3 through a diode (D5, 1N5817). The LDO input is also connected to +VIN through a diode (D5, 1N5817). The LDO output is connected to +3V3 through a diode (D5, 1N5817).

[illegible]

Diagram showing the pin connections for CN1:

- SWDIO (Orange) connects to Pin 1
- SWCLK (Orange) connects to Pin 3
- SWO ?? (Orange) connects to Pin 5
- MCU_RST (Green) connects to Pin 9
- +3V3 (Red) connects to Pin 2
- GND (Blue) connects to Pin 10

Board: HX JN1.27-2X5 TP H4.9

The schematic diagram illustrates the EPD driver circuit for the AFC24-S24FIC-00 module. The module's pins are connected to various components as follows:

- Pin 24:** Connected to GDR.
- Pin 22:** Connected to RESE.
- Pin 21:** Connected to VGL.
- Pin 20:** Connected to VGH.
- Pin 19:** Connected to GND.
- Pin 18:** Connected to GND.
- Pin 17:** Connected to GND.
- Pin 16:** Connected to EPD_BUSY.
- Pin 15:** Connected to EPD_RST.
- Pin 14:** Connected to EPD_DC.
- Pin 13:** Connected to EPD_CS.
- Pin 12:** Connected to EPD_CLK.
- Pin 11:** Connected to EPD_MOSI.
- Pin 10:** Connected to VDDIO.
- Pin 9:** Connected to VCI.
- Pin 8:** Connected to VSS.
- Pin 7:** Connected to VDD.
- Pin 6:** Connected to VPP.
- Pin 5:** Connected to VSH.
- Pin 4:** Connected to PREVGH.
- Pin 3:** Connected to PREVGH.
- Pin 2:** Connected to PREVGL.
- Pin 1:** Connected to VCOM.

The circuit includes the following components:

- Resistors:** R7 (10K), R8 (0.47R), R3 (10k).
- Capacitors:** C6 (4.7uF), C7 (4.7uF), C9 (1uF), C10 (1uF), C11 (4.7uF), C12 (1uF), C13 (4.7uF), C14 (4.7uF), C15 (4.7uF), C16 (4.7uF 50V), C17 (4.7uF).
- Inductor:** L3 (10uH).
- Diodes:** D1, D3 (MBR0530).
- MOSFET:** Q1 (Si1308).

The circuit is powered by +3V3 and includes an EPD_RST Pull-up resistor (R3).

SM04B-SRSS-TB_1F_SN