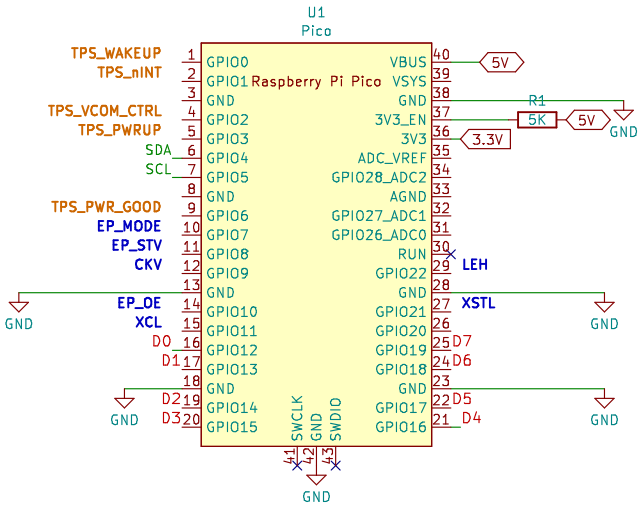


1st version of raw Schematic for PIC02 Adapter  
PCB to be defined only when Rev 1.0 is reviewed

RIGHT IOs for this signals to Gate & Source EPD

Gate: Selects ROW to write pixels  
**EP\_MODE**  
**EP\_STV**  
**CKV**  
Source: Gives signal to FETs in row to apply  
+15 or -15 to pixels  
**EP\_OE**  
**XCL**  
**LEH**  
**XSTL**

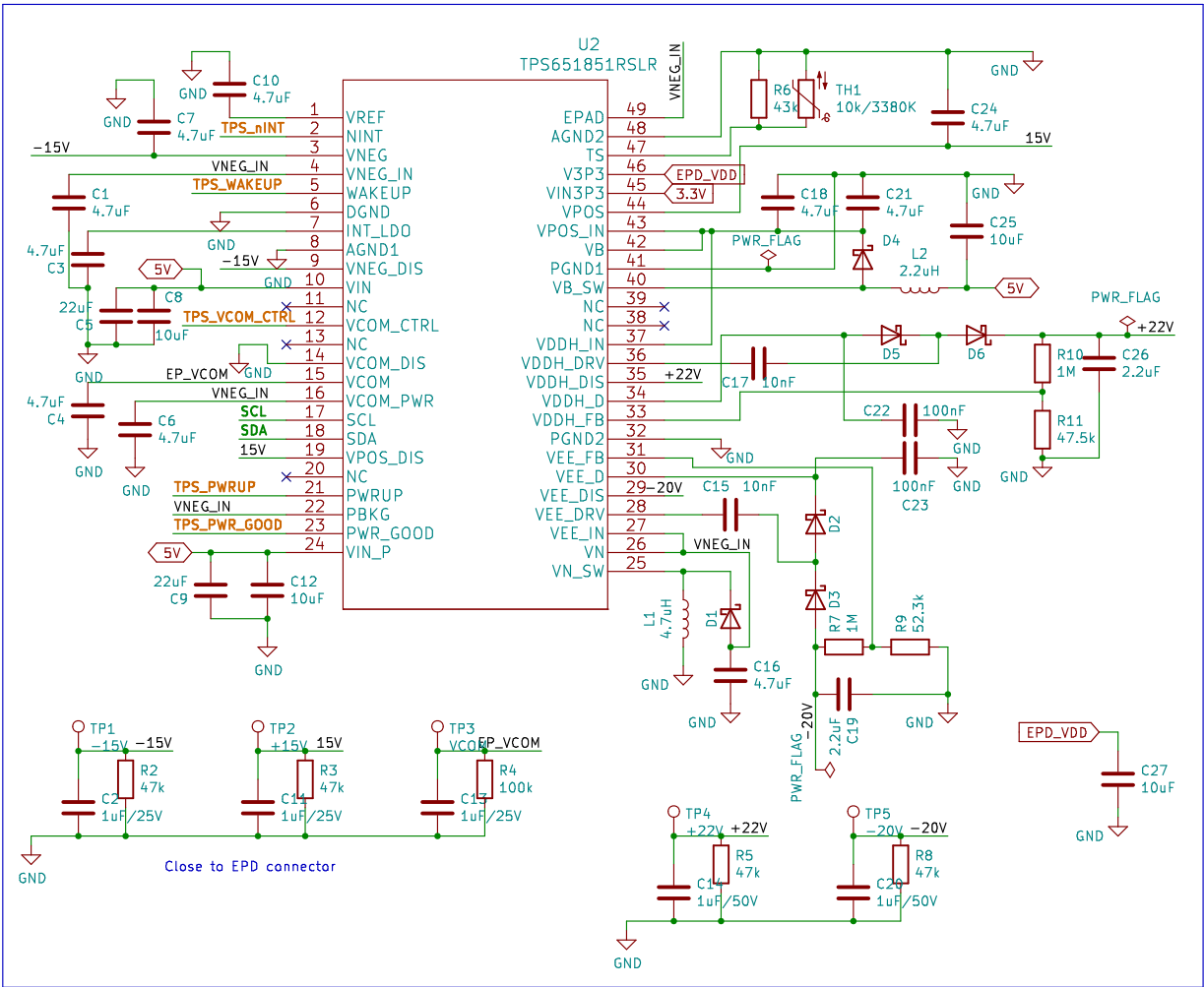
SIGNALS according to:  
<http://essentialscrap.com/eink/waveforms.html>



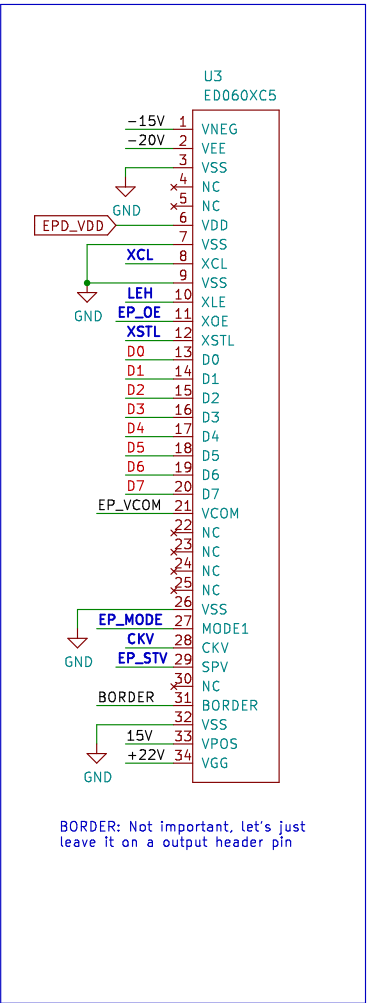
RIGHT IOs for this signals from P-Mic

**TPS\_WAKEUP** OUT Wake up pin. Pull this pin high to wake up from sleep mode.  
**TPS\_nINT** INPUT Open drain interrupt pin (active low)  
**TPS\_VCOM\_CTRL** OUT VCOM enable. Pull this pin high to enable the VCOM amplifier.  
**TPS\_PWRUP** OUT Power-up pin. Pull this pin high to power up all output rails.  
**TPS\_PWR\_GOOD** INPUT Open-drain power good output pin. HIGH when all power rails OK

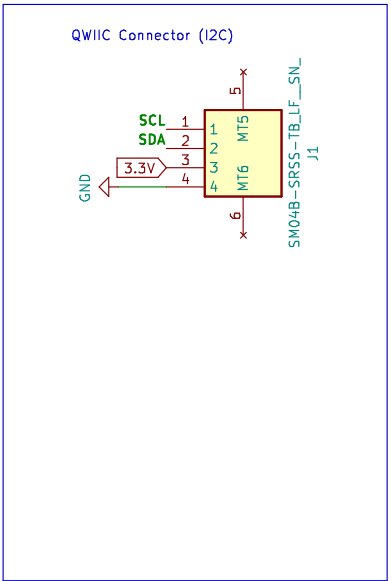
### EPD Power



### EPD 6" Connector 34pin, 8bit



### External connectors



This board aims to be a test-board to connect  
an EINK panel (6" & 9.7") and test the signals+voltages  
**FASANI CORP.**

Sheet: /  
File: pico-eink.kicad\_sch

**Title: ALPHA Pico HAT board to test EINK signals**

Size: A3 Date: 2024-08-18  
KiCad E.D.A. kicad 7.0.11-7.0.11-ubuntu22.04.1

Rev: 1.0  
Id: 1/1