

RIGHT IOs for this signals to Gate & Source EPD

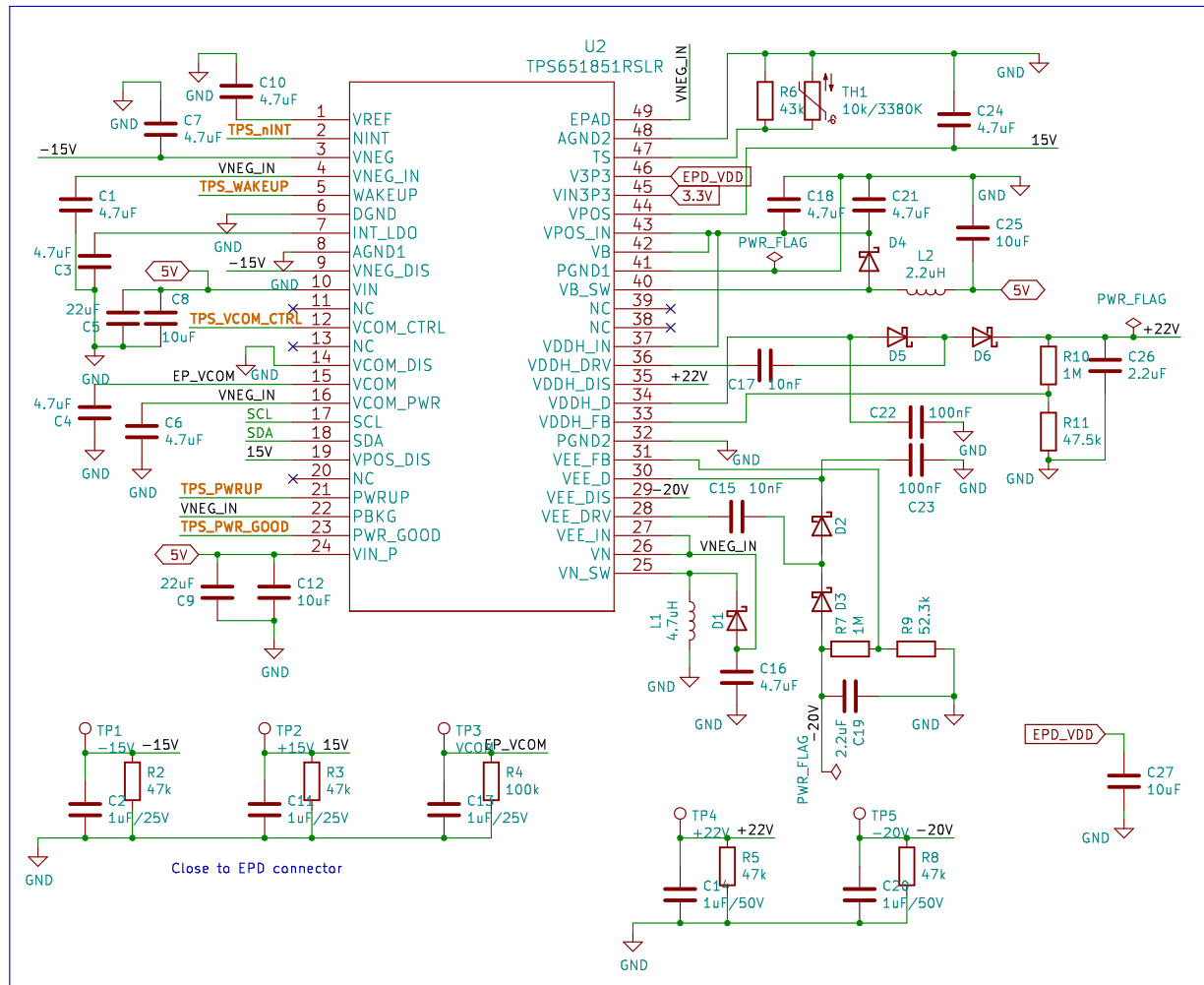
Gate: Selects ROW to write pixels
EP_MODE
EP_STV
CKV
Source: Gives signal to FETs in row to apply
+15 or -15 to pixels
EP_OE
XCL
LEH
XSTL

SIGNALS according to:
<http://essentialscrap.com/eink/waveforms.html>

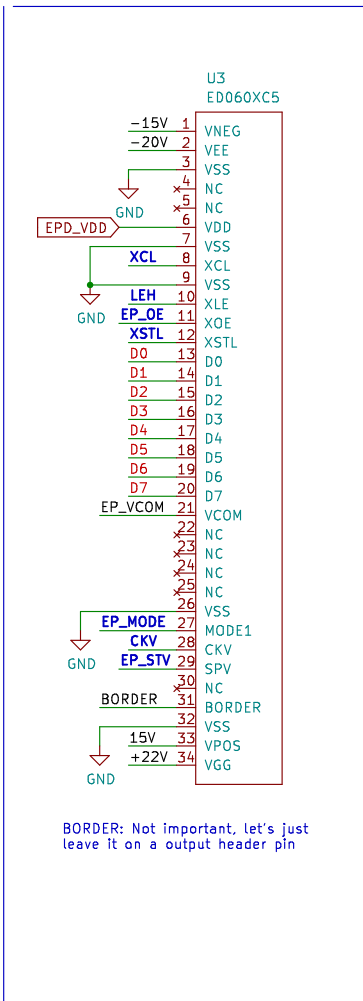
RIGHT IOs for this signals from P-Mic

TPS_WAKEUP OUT Wake up pin. Pull this pin high to wake up from sleep mode.
TPS_nINT INPUT Open drain interrupt pin (active low)
TPS_VCOM_CTRL OUT VCOM enable. Pull this pin high to enable the VCOM amplifier.
TPS_PWRUP OUT Power-up pin. Pull this pin high to power up all output rails.
TPS_PWR_GOOD INPUT Open-drain power good output pin.
HIGH when all power rails OK

EPD Power



EPD 6" Connector 34pin, 8bit



This board aims to be a test-board to connect
an EINK panel (6" & 9.7") and test the signals+voltages

FASANI CORP.

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File: pico-eink.kicad_sch

Title: ALPHA Pico HAT board to test EINK signals

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