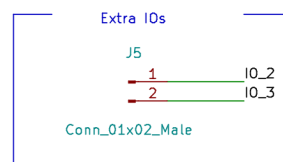
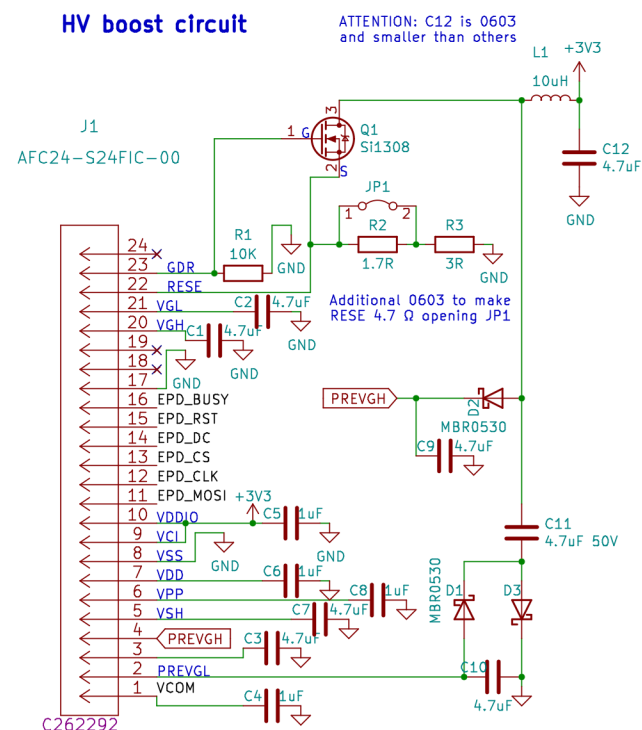
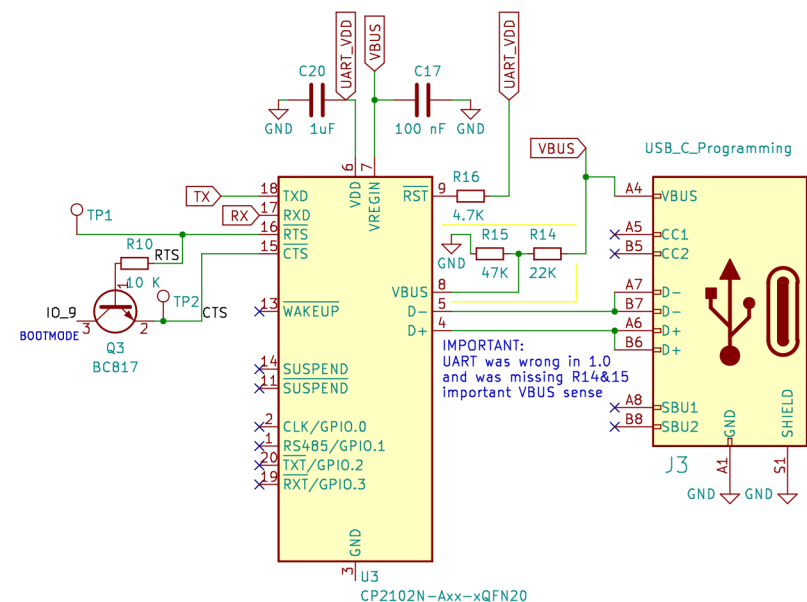
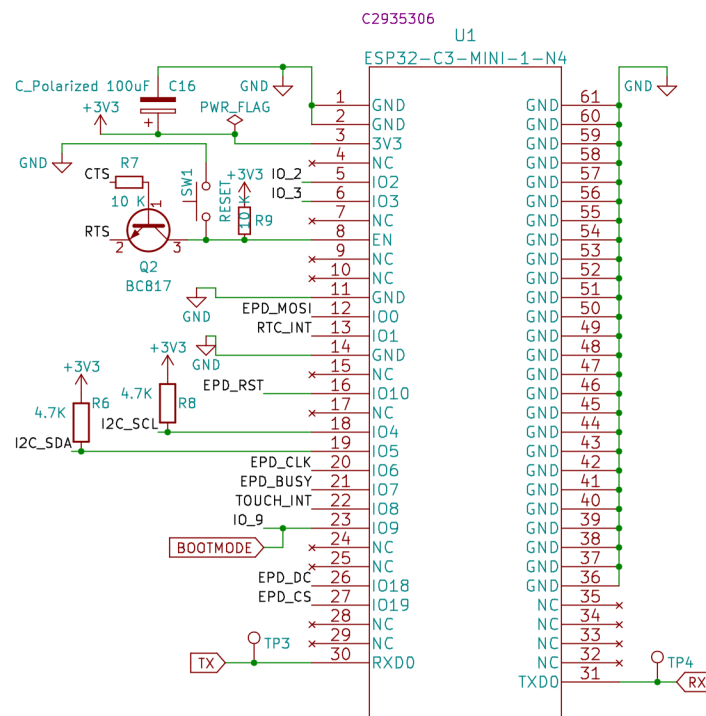
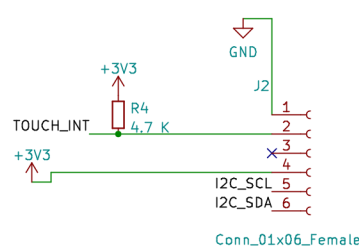


HV boost circuit

ATTENTION: C12 is 0603 and smaller than others

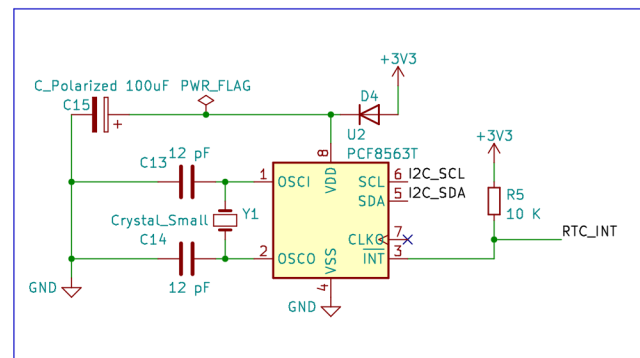


2C connector

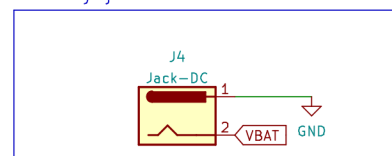


IMPORTANT: This QFN20 version does not have a CTR pin. I've read comments that CTS might work instead:
<https://www.reddit.com/r/AskElectronics/comments/ta5fbt/comment/hzz5ja/>

PCF8563T Real time clock via I2C

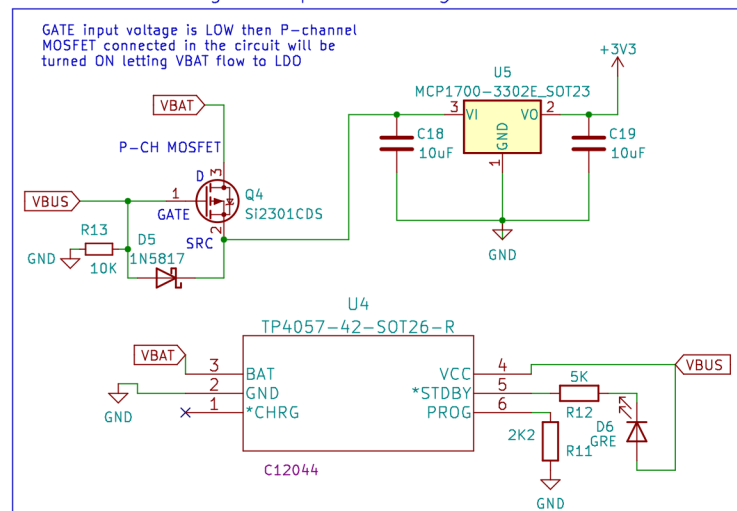


Battery jack



Charge and power management

GATE input voltage is LOW then P-channel MOSFET connected in the circuit will be turned ON letting VBAT flow to LDO



- H1 MountingHole
- H2 Not included in PCB (no space) MountingHole
- H3 MountingHole
- H4 MountingHole

UPDATE LOG

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v1.1 Corrected UART. Added VBUS sense + pullup in RSTB
Updated R1 to be 0201 and moved ext IO pins

v1.0 wrong UART. Touch + epaper work as expected