

```
1  LIBRARY IEEE;
2  USE IEEE.STD_LOGIC_1164.ALL;
3
4  ENTITY BITAND IS
5      PORT (
6          A : IN STD_LOGIC := 'U';
7          B : IN STD_LOGIC := 'U';
8          --
9          S : OUT STD_LOGIC := 'U');
10 END BITAND;
11
12 ARCHITECTURE BEHAVIOR OF BITAND IS
13 BEGIN
14     S <= (A AND B);
15 END BEHAVIOR;
```