

Request For Quotation

Order the parts you need from our real-time inventory database. Simply complete a request for quotation form with your part information and a sales representative will respond to you with price and availability.

Request For Quotation

Your free datasheet starts on the next page.

More datasheets and data books are available from our homepage: http://www.datasheetarchive.com



THREE-TERMINAL POSITIVE FIXED VOLTAGE REGULATORS

SEMICONDUCTOR TECHNICAL DATA

Three-Terminal Positive Voltage Regulators

These voltage regulators are monolithic integrated circuits designed as fixed-voltage regulators for a wide variety of applications including local, on-card regulation. These regulators employ internal current limiting, thermal shutdown, and safe-area compensation. With adequate heatsinking they can deliver output currents in excess of 1.0 A. Although designed primarily as a fixed voltage regulator, these devices can be used with external components to obtain adjustable voltages and currents.

- Output Current in Excess of 1.0 A
- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe-Area Compensation
- Output Voltage Offered in 2% and 4% Tolerance
- Available in Surface Mount D²PAK and Standard 3–Lead Transistor Packages

T SUFFIX PLASTIC PACKAGE CASE 221A Heatsink surface connected to Pin 2.

Pin 1. Input 2. Ground 3. Output

D2T SUFFIXPLASTIC PACKAGE
CASE 936
(D²PAK)



Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.

DEVICE TYPE/NOMINAL OUTPUT VOLTAGE

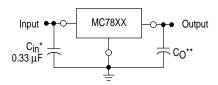
MC7805	5.0 V	MC7812	12 V
MC7806	6.0 V	MC7815	15 V
MC7808	8.0 V	MC7818	18 V
MC7809	9.0 V	MC7824	24 V

ORDERING INFORMATION

Device	Output Voltage Tolerance	Tested Operating Temperature Range	Package
MC78XXACT	2%		Insertion Mount
MC78XXACD2T	270	T. 00 to 140500	Surface Mount
MC78XXCT		T _J = 0° to +125°C	Insertion Mount
MC78XXCD2T	40/		Surface Mount
MC78XXBT	4%	T. 400 to 14250C	Insertion Mount
MC78XXBD2T		$T_J = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	Surface Mount

XX indicates nominal voltage.

STANDARD APPLICATION



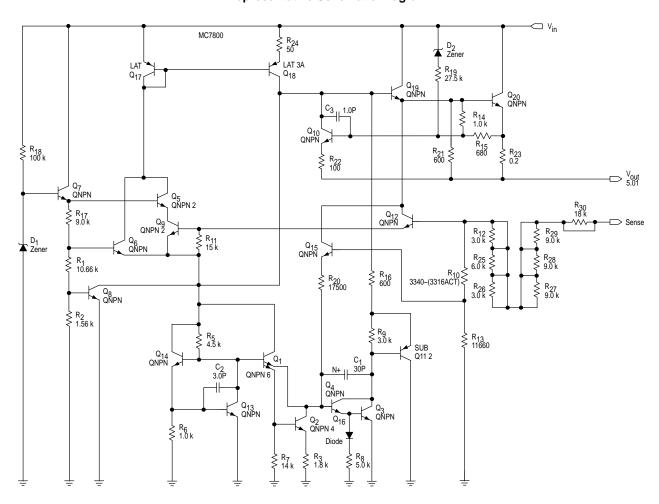
A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

- XX, these two digits of the type number indicate nominal voltage.
- * C_{in} is required if regulator is located an appreciable distance from power supply filter
- ** C_O is not needed for stability; however, it does improve transient response. Values of less than 0.1 μF could cause instability.

$\textbf{MAXIMUM RATINGS} \ (T_A = 25^{\circ}C \ unless \ otherwise \ noted.)$

Rating	Symbol	Value	Unit
Input Voltage (5.0 – 18 V) (24 V)	VI	35 40	Vdc
Power Dissipation Case 221A			
T _A = 25°C	PD	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	65	°C/W
Thermal Resistance, Junction–to–Case Case 936 (D ² PAK)	$R_{\theta JC}$	5.0	°C/W
T _A = 25°C	PD	Internally Limited	W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	See Figure 13	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JA}$	5.0	°C/W
Storage Junction Temperature Range	T _{stg}	-65 to +150	°C
Operating Junction Temperature	TJ	+150	ů

Representative Schematic Diagram



This device contains 22 active transistors.

$\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 10 \ V, \ I_O = 500 \ \text{mA}, \ T_J = T_{low} \ \text{to} \ T_{high} \ [\text{Note 1}], \ \text{unless otherwise noted.})$

		MC7805B						
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	4.8	5.0	5.2	4.8	5.0	5.2	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 7.0 Vdc \leq V _{in} \leq 20 Vdc 8.0 Vdc \leq V _{in} \leq 20 Vdc	Vo	_ 4.75	_ 5.0	_ 5.25	4.75 –	5.0 –	5.25 –	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 2) 7.0 $Vdc \le V_{in} \le 25 \ Vdc$ 8.0 $Vdc \le V_{in} \le 12 \ Vdc$	Reg _{line}	_ _	5.0 1.3	100 50	_ _	5.0 1.3	100 50	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 2) 5.0 mA $\leq I_O \leq$ 1.5 A 250 mA $\leq I_O \leq$ 750 mA	Reg _{load}	_ _	1.3 0.15	100 50	_ _	1.3 0.15	100 50	mV
Quiescent Current (T _J = 25°C)	IB	_	3.2	8.0	_	3.2	8.0	mA
Quiescent Current Change 7.0 Vdc \leq V _{in} \leq 25 Vdc 8.0 Vdc \leq V _{in} \leq 25 Vdc 5.0 mA \leq I _O \leq 1.0 A	ΔΙΒ	- - -	- - -	- 1.3 0.5	- - -	- - -	1.3 - 0.5	mA
Ripple Rejection 8.0 Vdc \leq V _{in} \leq 18 Vdc, f = 120 Hz	RR	_	68	_	_	68	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	_	-	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	_	10	_	_	10	_	μV/V _O
Output Resistance f = 1.0 kHz	rO	_	0.9	_	_	0.9	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	_	0.2	-	_	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCVO	_	-0.3	_	_	-0.3	_	mV/°C

ELECTRICAL CHARACTERISTICS ($V_{in} = 10 \text{ V}$, $I_{O} = 1.0 \text{ A}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

· III · · · · · · · · · · · · · · · · ·	iligii i				
		ı	MC7805A0	:	
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	4.9	5.0	5.1	Vdc
Output Voltage (5.0 mA \leq I $_{O}$ \leq 1.0 A, P $_{D}$ \leq 15 W) 7.5 Vdc \leq V $_{in}$ \leq 20 Vdc	Vo	4.8	5.0	5.2	Vdc
Line Regulation (Note 2) $7.5 \text{ Vdc} \leq V_{in} \leq 25 \text{ Vdc, I}_{O} = 500 \text{ mA} \\ 8.0 \text{ Vdc} \leq V_{in} \leq 12 \text{ Vdc} \\ 8.0 \text{ Vdc} \leq V_{in} \leq 12 \text{ Vdc, T}_{J} = 25^{\circ}\text{C} \\ 7.3 \text{ Vdc} \leq V_{in} \leq 20 \text{ Vdc, T}_{J} = 25^{\circ}\text{C}$	Reg _{line}		5.0 1.3 1.3 4.5	50 50 25 50	mV
Load Regulation (Note 2) 5.0 mA \leq I _O \leq 1.5 A, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A 250 mA \leq I _O \leq 750 mA	Reg _{load}	- - -	1.3 0.8 0.15	100 100 50	mV
Quiescent Current (T _J = 25°C)	IB	_ _	_ 3.2	6.0 6.0	mA
Quiescent Current Change $8.0 \text{ Vdc} \le V_{in} \le 25 \text{ Vdc}, I_O = 500 \text{ mA}$ $7.5 \text{ Vdc} \le V_{in} \le 20 \text{ Vdc}, T_J = 25^{\circ}\text{C}$ $5.0 \text{ mA} \le I_O \le 1.0 \text{ A}$	ΔΙΒ	- - -	- - -	0.8 0.8 0.5	mA

^{2.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

$\textbf{ELECTRICAL CHARACTERISTICS (continued)} \ (V_{in} = 10 \ V, \ I_O = 1.0 \ A, \ T_J = T_{low} \ to \ T_{high} \ [Note \ 1], \ unless \ otherwise \ noted.)$

		ı	MC7805A	-	
Characteristic	Symbol	Min	Тур	Max	Unit
Ripple Rejection 8.0 Vdc \leq V _{in} \leq 18 Vdc, f = 120 Hz, I _O = 500 mA	RR	_	68	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	_	μV/V _O
Output Resistance (f = 1.0 kHz)	ro	-	0.9	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	_	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	-	А
Average Temperature Coefficient of Output Voltage	TCVO	_	-0.3	_	mV/°C

$\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 11 \ V, I_O = 500 \ \text{mA}, \ T_J = T_{low} \ \text{to} \ T_{high} \ [\text{Note 1}], \ \text{unless otherwise noted.})$

			MC7806B			MC7806C	;	
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	5.75	6.0	6.25	5.75	6.0	6.25	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 8.0 Vdc \leq Vi _n \leq 21 Vdc 9.0 Vdc \leq Vi _n \leq 21 Vdc	VO	_ 5.7	- 6.0	- 6.3	5.7 -	6.0 -	6.3	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 2) 8.0 Vdc $\leq V_{in} \leq 25$ Vdc 9.0 Vdc $\leq V_{in} \leq 13$ Vdc	Regline	_ _	5.5 1.4	120 60	_ _	5.5 1.4	120 60	mV
Load Regulation, T_J = 25°C (Note 2) 5.0 mA \leq IO \leq 1.5 A 250 mA \leq IO \leq 750 mA	Reg _{load}	_ _	1.3 0.2	120 60	_ _	1.3 0.2	120 60	mV
Quiescent Current (T _J = 25°C)	IB	-	3.3	8.0	-	3.3	8.0	mA
Quiescent Current Change $8.0 \text{ Vdc} \le V_{\text{in}} \le 25 \text{ Vdc}$ $9.0 \text{ Vdc} \le V_{\text{in}} \le 25 \text{ Vdc}$ $5.0 \text{ mA} \le I_{\text{O}} \le 1.0 \text{ A}$	ΔΙΒ	- - -	_ _ _	- 1.3 0.5	- - -	- - -	1.3 - 0.5	mA
Ripple Rejection 9.0 Vdc ≤ V _{in} ≤ 19 Vdc, f = 120 Hz	RR	-	65	-	_	65	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	_	_	2.0	-	Vdc
Output Noise Voltage (T _A = 25°C) 10 Hz ≤ f ≤ 100 kHz	V _n	_	10	-	_	10	_	μV/VΟ
Output Resistance f = 1.0 kHz	rO	_	0.9	_	_	0.9	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	-	0.2	_	_	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	-	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCVO	-	-0.3	_	_	-0.3	_	mV/°C

^{2.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 11 V, I_{O} = 1.0 A, T_{J} = T_{low} to T_{high} [Note 1], unless otherwise noted.)

		ı			
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	5.88	6.0	6.12	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 8.6 Vdc \leq V _{in} \leq 21 Vdc	Vo	5.76	6.0	6.24	Vdc
Line Regulation (Note 2) 8.6 Vdc \leq V _{in} \leq 25 Vdc, I _O = 500 mA 9.0 Vdc \leq V _{in} \leq 13 Vdc 9.0 Vdc \leq V _{in} \leq 13 Vdc, T _J = 25°C 8.3 Vdc \leq V _{in} \leq 21 Vdc, T _J = 25°C	Reg _{line}	- - - -	5.0 1.4 1.4 4.5	60 60 30 60	mV
Load Regulation (Note 2) 5.0 mA \leq I _O \leq 1.5 A, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A 250 mA \leq I _O \leq 750 mA	Regload	_ _ _	1.3 0.9 0.2	100 100 50	mV
Quiescent Current $T_J = 25^{\circ}C$	lВ	_ _	_ 3.3	6.0 6.0	mA
Quiescent Current Change 9.0 Vdc \leq V $_{in}$ \leq 25 Vdc, I $_{O}$ = 500 mA 8.6 Vdc \leq V $_{in}$ \leq 21 Vdc, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A	ΔΙΒ	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 9.0 Vdc \leq V _{in} \leq 19 Vdc, f = 120 Hz, I _O = 500 mA	RR	_	65	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	Vn	_	10	-	μV/V _O
Output Resistance (f = 1.0 kHz)	rO	-	0.9	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	А
Average Temperature Coefficient of Output Voltage	TCVO	_	-0.3	_	mV/°C

ELECTRICAL CHARACTERISTICS ($V_{in} = 14 \text{ V}, I_{O} = 500 \text{ mA}, T_{J} = T_{low} \text{ to } T_{high} \text{ [Note 1], unless otherwise noted.)}$

			MC7808B			MC7808C		
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	7.7	8.0	8.3	7.7	8.0	8.3	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 10.5 Vdc \leq V _{in} \leq 23 Vdc 11.5 Vdc \leq V _{in} \leq 23 Vdc	Vo	- 7.6	_ 8.0	_ 8.4	7.6 -	8.0 -	8.4 -	Vdc
Line Regulation, $T_J = 25^{\circ}C$, (Note 2) 10.5 $Vdc \le V_{in} \le 25 Vdc$ 11 $Vdc \le V_{in} \le 17 Vdc$	Regline	_ _	6.0 1.7	160 80	_ _	6.0 1.7	160 80	mV
Load Regulation, T_J = 25°C (Note 2) 5.0 mA \leq IO \leq 1.5 A 250 mA \leq IO \leq 750 mA	Reg _{load}	_ _	1.4 .22	160 80		1.4 .22	160 80	mV
Quiescent Current (T _J = 25°C)	IB	_	3.3	8.0	-	3.3	8.0	mA

^{2.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (continued) ($V_{in} = 14 \text{ V}$, $I_{O} = 500 \text{ mA}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

			MC7808B			MC7808C	:	
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Quiescent Current Change 10.5 Vdc \leq V _{in} \leq 25 Vdc 11.5 Vdc \leq V _{in} \leq 25 Vdc 5.0 mA \leq I _O \leq 1.0 A	ΔΙΒ	- - -	- - -	- 1.0 0.5	- - -	- - -	1.0 - 0.5	mA
Ripple Rejection 11.5 Vdc ≤ V _{in} ≤ 18 Vdc, f = 120 Hz	RR	-	62	-	-	62	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	_	-	2.0	-	Vdc
Output Noise Voltage (T _A = 25° C) 10 Hz \leq f \leq 100 kHz	V _n	-	10	-	-	10	_	μV/V _O
Output Resistance f = 1.0 kHz	rO	-	0.9	_	-	0.9	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	-	0.2	_	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	_	-	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCVO	-	-0.4	-	-	-0.4	-	mV/°C

ELECTRICAL CHARACTERISTICS ($V_{in} = 14 \text{ V}$, $I_{O} = 1.0 \text{ A}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

		ı			
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	7.84	8.0	8.16	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 10.6 Vdc \leq V _{in} \leq 23 Vdc	Vo	7.7	8.0	8.3	Vdc
Line Regulation (Note 2) $10.6 \text{ Vdc} \le V_{in} \le 25 \text{ Vdc, I}_O = 500 \text{ mA} \\ 11 \text{ Vdc} \le V_{in} \le 17 \text{ Vdc} \\ 11 \text{ Vdc} \le V_{in} \le 17 \text{ Vdc, T}_J = 25^{\circ}\text{C} \\ 10.4 \text{ Vdc} \le V_{in} \le 23 \text{ Vdc, T}_J = 25^{\circ}\text{C}$	Regline	- - - -	6.0 1.7 1.7 5.0	80 80 40 80	mV
Load Regulation (Note 2) $5.0 \text{ mA} \le I_O \le 1.5 \text{ A}$, $T_J = 25^{\circ}\text{C}$ $5.0 \text{ mA} \le I_O \le 1.0 \text{ A}$ $250 \text{ mA} \le I_O \le 750 \text{ mA}$	Regload	_ _ _	1.4 1.0 .22	100 100 50	mV
Quiescent Current $T_J = 25^{\circ}C$	lВ	_ _	- 3.3	6.0 6.0	mA
Quiescent Current Change 11 Vdc \leq V $_{in}$ \leq 25 Vdc, I $_{O}$ = 500 mA 10.6 Vdc \leq V $_{in}$ \leq 20 Vdc, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A	ΔlB	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 11.5 $Vdc \le V_{in} \le 21.5 Vdc$, f = 120 Hz, $I_O = 500 \text{ mA}$	RR	_	62	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	VI – VO	-	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	_	10	_	μV/V _O
Output Resistance f = 1.0 kHz	rO	_	0.9	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	_	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	_	А
Average Temperature Coefficient of Output Voltage	TCVO	-	-0.4	_	mV/°C

NOTES: 1. T_{low} = 0°C for MC78XXAC, C T_{high} = +125°C for MC78XXAC, C, B = -40°C for MC78XXB

^{2.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS ($V_{in} = 15 \text{ V}$, $I_{O} = 500 \text{ mA}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

		ı			
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	8.65	9.0	9.35	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 11.5 Vdc \leq V _{in} \leq 24 Vdc	Vo	8.55	9.0	9.45	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 2) 11.5 $Vdc \le V_{in} \le 26 Vdc$ 11.5 $Vdc \le V_{in} \le 17 Vdc$	Reg _{line}	_ _	6.2 1.8	50 25	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 2) 5.0 mA $\leq I_O \leq 1.5$ A 250 mA $\leq I_O \leq 750$ mA	Regload	_ _	1.5 0.3	50 25	mV
Quiescent Current (T _J = 25°C)	I _B	-	3.4	8.0	mA
Quiescent Current Change 11.5 $Vdc \le V_{in} \le 26 \ Vdc$ 5.0 $mA \le I_O \le 1.0 \ A$	ΔlB	_ _	_ _	1.0 0.5	mA
Ripple Rejection 11.5 $Vdc \le V_{in} \le 21.5 Vdc$, $f = 120 Hz$	RR	_	61	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	VI – VO	-	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	Vn	-	10	_	μV/VΟ
Output Resistance f = 1.0 kHz	rO	-	1.0	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	А
Average Temperature Coefficient of Output Voltage	TCVO	-	-0.5	-	mV/°C

ELECTRICAL CHARACTERISTICS ($V_{in} = 19 \text{ V}, I_{O} = 500 \text{ mA}, T_{J} = T_{low} \text{ to } T_{high} \text{ [Note 1], unless otherwise noted.)}$

(.	- 1, 10	The same of the state of the st							
			MC7812B	1	MC7812C				
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit	
Output Voltage (T _J = 25°C)	VO	11.5	12	12.5	11.5	12	12.5	Vdc	
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 14.5 Vdc \leq Vi _n \leq 27 Vdc 15.5 Vdc \leq Vi _n \leq 27 Vdc	Vo	_ 11.4	_ 12	_ 12.6	11.4 –	12 -	12.6 -	Vdc	
Line Regulation, T_J = 25°C (Note 2) 14.5 Vdc \leq V _{in} \leq 30 Vdc 16 Vdc \leq V _{in} \leq 22 Vdc	Reg _{line}	- -	7.5 2.2	240 120	_ _	7.5 2.2	240 120	mV	
Load Regulation, $T_J = 25^{\circ}C$ (Note 2) 5.0 mA $\leq I_O \leq 1.5$ A 250 mA $\leq I_O \leq 750$ mA	Reg _{load}	_ _	1.6 1.0	240 120	_ _	1.6 1.0	240 120	mV	
Quiescent Current (T _J = 25°C)	ΙΒ	-	3.4	8.0	_	3.4	8.0	mA	
Quiescent Current Change $14.5 \text{ Vdc} \leq V_{in} \leq 30 \text{ Vdc}$ $15 \text{ Vdc} \leq V_{in} \leq 30 \text{ Vdc}$ $5.0 \text{ mA} \leq I_O \leq 1.0 \text{ A}$	ΔIB	- - -	- - -	- 1.0 0.5	- - -	- - -	1.0 - 0.5	mA	
Ripple Rejection 15 $Vdc \le V_{in} \le 25 Vdc$, f = 120 Hz	RR	_	60	_	_	60	-	dB	
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	_	_	2.0	_	Vdc	

Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

$\textbf{ELECTRICAL CHARACTERISTICS (continued)} \ (V_{in} = 19 \ V, \ I_O = 500 \ \text{mA}, \ T_J = T_{low} \ \text{to } T_{high} \ [\text{Note 1}], \ \text{unless otherwise noted.})$

		MC7812B			MC7812C			
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	_	10	_	_	10	_	μV/V _O
Output Resistance f = 1.0 kHz	rO	-	1.1	_	-	1.1	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	ISC	ı	0.2	ı	ı	0.2	ı	A
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	_	_	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCVO	_	-0.8	_	_	-0.8	_	mV/°C

$\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 19 \ V, \ I_O = 10 \ A, \ T_J = T_{low} \ to \ T_{high} \ [Note \ 1], \ unless \ otherwise \ noted.)$

		ı	MC7812A	C	
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	11.75	12	12.25	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 14.8 Vdc \leq V _{in} \leq 27 Vdc	Vo	11.5	12	12.5	Vdc
Line Regulation (Note 2) $ 14.8 \text{ Vdc} \leq V_{in} \leq 30 \text{ Vdc}, \ I_O = 500 \text{ mA} $ $ 16 \text{ Vdc} \leq V_{in} \leq 22 \text{ Vdc} $ $ 16 \text{ Vdc} \leq V_{in} \leq 22 \text{ Vdc}, \ T_J = 25^{\circ}\text{C} $ $ 14.5 \text{ Vdc} \leq V_{in} \leq 27 \text{ Vdc}, \ T_J = 25^{\circ}\text{C} $	Reg _{line}	- - -	7.5 2.2 2.2 6.0	120 120 60 120	mV
Load Regulation (Note 2) 5.0 mA \leq IO \leq 1.5 A, TJ = 25°C 5.0 mA \leq IO \leq 1.0 A 250 mA \leq IO \leq 750 mA	Regload	_ _ _	1.6 1.2 1.0	100 100 50	mV
Quiescent Current $T_J = 25^{\circ}C$	lВ	_ _	- 3.4	6.0 6.0	mA
Quiescent Current Change 15 Vdc \leq V _{in} \leq 30 Vdc, I _O = 500 mA 14.8 Vdc \leq V _{in} \leq 27 Vdc, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A	ΔlB	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 15 $Vdc \le V_{in} \le 25 Vdc$, f = 120 Hz, $I_O = 500 \text{ mA}$	RR	_	60	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	$V_I - V_O$	-	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	Vn	_	10	_	μV/VΟ
Output Resistance (f = 1.0 kHz)	ro	_	1.1	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCVO	-	-0.8	_	mV/°C

Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

$\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 23 \ V, \ I_O = 500 \ \text{mA}, \ T_J = T_{low} \ \text{to} \ T_{high} \ [\text{Note 1}], \ \text{unless otherwise noted.})$

	MC7815B				MC78150	;		
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage ($T_J = 25^{\circ}C$)	Vo	14.4	15	15.6	14.4	15	15.6	Vdc
Output Voltage $(5.0 \text{ mA} \le I_O \le 1.0 \text{ A}, P_D \le 15 \text{ W})$ $17.5 \text{ Vdc} \le V_{in} \le 30 \text{ Vdc}$ $18.5 \text{ Vdc} \le V_{in} \le 30 \text{ Vdc}$	Vo	_ 14.25	_ 15	_ 15.75	14.25 –	15 -	15.75 –	Vdc
Line Regulation, T_J = 25°C (Note 2) 17.5 $Vdc \le V_{in} \le 30 \ Vdc$ 20 $Vdc \le V_{in} \le 26 \ Vdc$	Reg _{line}	_ _	8.5 3.0	300 150	_ _	8.5 3.0	300 150	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 2) 5.0 mA \leq I _O \leq 1.5 A 250 mA \leq I _O \leq 750 mA	Regload	_ _	1.8 1.2	300 150	_ _	1.8 1.2	300 150	mV
Quiescent Current (T _J = 25°C)	ΙΒ	_	3.5	8.0	_	3.5	8.0	mA
Quiescent Current Change 17.5 $Vdc \le V_{in} \le 30 \ Vdc$ 18.5 $Vdc \le V_{in} \le 30 \ Vdc$ 5.0 $mA \le I_O \le 1.0 \ A$	ΔlB	- - -	- - -	- 1.0 0.5	- - -	- - -	1.0 - 0.5	mA
Ripple Rejection 18.5 $Vdc \le V_{in} \le 28.5 Vdc$, $f = 120 Hz$	RR	_	58	_	_	58	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	_	_	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	Vn	_	10	_	_	10	_	μV/V _O
Output Resistance f = 1.0 kHz	ro	_	1.2	_	_	1.2	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	_	0.2	_	_	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	-	2.2	-	А
Average Temperature Coefficient of Output Voltage	TCVO	_	-1.0	_	_	-1.0	_	mV/°C

ELECTRICAL CHARACTERISTICS ($V_{in} = 23 \text{ V}$, $I_{O} = 1.0 \text{ A}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

		ı	MC7815AC		
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	14.7	15	15.3	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 17.9 Vdc \leq V _{in} \leq 30 Vdc	VO	14.4	15	15.6	Vdc
Line Regulation (Note 2) $17.9 \text{ Vdc} \le V_{in} \le 30 \text{ Vdc}, I_O = 500 \text{ mA}$ $20 \text{ Vdc} \le V_{in} \le 26 \text{ Vdc}$ $20 \text{ Vdc} \le V_{in} \le 26 \text{ Vdc}, T_J = 25^{\circ}\text{C}$ $17.5 \text{ Vdc} \le V_{in} \le 30 \text{ Vdc}, T_J = 25^{\circ}\text{C}$	Regline	- - -	8.5 3.0 3.0 7.0	150 150 75 150	mV
Load Regulation (Note 2) 5.0 mA \leq I _O \leq 1.5 A, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A 250 mA \leq I _O \leq 750 mA	Reg _{load}	- - -	1.8 1.5 1.2	100 100 50	mV
Quiescent Current $T_J = 25^{\circ}C$	lΒ	_ _	- 3.5	6.0 6.0	mA
Quiescent Current Change 17.5 $Vdc \le V_{in} \le 30 \ Vdc, \ I_O = 500 \ mA$ 17.5 $Vdc \le V_{in} \le 30 \ Vdc, \ T_J = 25^{\circ}C$ 5.0 $mA \le I_O \le 1.0 \ A$	ΔlB	- - -	- - -	0.8 0.8 0.5	mA

Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (continued) ($V_{in} = 23 \text{ V}, I_{O} = 1.0 \text{ A}, T_{J} = T_{low} \text{ to } T_{high} \text{ [Note 1], unless otherwise noted.)}$

		ı	MC7815AC		
Characteristic	Symbol	Min	Тур	Max	Unit
Ripple Rejection 18.5 Vdc \leq V $_{in}$ \leq 28.5 Vdc, f = 120 Hz, I $_{O}$ = 500 mA	RR	_	58	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	_	μV/V _O
Output Resistance f = 1.0 kHz	ro	-	1.2	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCVO	_	-1.0	_	mV/°C

$\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 27 \ \text{V}, \ I_O = 500 \ \text{mA}, \ T_J = T_{low} \ \text{to} \ T_{high} \ [\text{Note 1}], \ \text{unless otherwise noted.})$

			MC7818B			MC7818C		
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	17.3	18	18.7	17.3	18	18.7	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 21 Vdc \leq Vi _n \leq 33 Vdc 22 Vdc \leq Vi _n \leq 33 Vdc	VO	_ 17.1	_ 18	_ 18.9	17.1 –	18 -	18.9 –	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 2) 21 Vdc \leq V _{in} \leq 33 Vdc 24 Vdc \leq V _{in} \leq 30 Vdc	Reg _{line}	_ _	9.5 3.2	360 180	_ _	9.5 3.2	360 180	mV
Load Regulation, T_J = 25°C (Note 2) 5.0 mA \leq IO \leq 1.5 A 250 mA \leq IO \leq 750 mA	Reg _{load}	_ _	2.0 1.5	360 180	_ _	2.0 1.5	360 180	mV
Quiescent Current (T _J = 25°C)	ΙB	_	3.5	8.0	_	3.5	8.0	mA
Quiescent Current Change 21 Vdc \leq V _{in} \leq 33 Vdc 22 Vdc \leq V _{in} \leq 33 Vdc 5.0 mA \leq I _O \leq 1.0 A	ΔΙΒ	- - -	- - -	- 1.0 0.5		- - -	1.0 - 0.5	mA
Ripple Rejection 22 Vdc ≤ V _{in} ≤ 33 Vdc, f = 120 Hz	RR	-	57	-	_	57	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _{iI} – V _O	-	2.0	_	_	2.0	-	Vdc
Output Noise Voltage (T _A = 25°C) 10 Hz ≤ f ≤ 100 kHz	V _n	_	10	-	_	10	_	μV/VΟ
Output Resistance f = 1.0 kHz	rO	_	1.3	_	_	1.3	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc		0.2	-	-	0.2		А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	_	2.2	-	А
Average Temperature Coefficient of Output Voltage	TCVO	-	-1.5	_	-	-1.5	_	mV/°C

^{2.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS ($V_{in} = 27 \text{ V}$, $I_{O} = 10 \text{ A}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

		ı			
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	17.64	18	18.36	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 21 Vdc \leq V _{in} \leq 33 Vdc	Vo	17.3	18	18.7	Vdc
Line Regulation (Note 2) 21 Vdc \leq V _{in} \leq 33 Vdc, I _O = 500 mA 24 Vdc \leq V _{in} \leq 30 Vdc 24 Vdc \leq V _{in} \leq 30 Vdc, T _J = 25°C 20.6 Vdc \leq V _{in} \leq 33 Vdc, T _J = 25°C	Regline	- - - -	9.5 3.2 3.2 8.0	180 180 90 180	mV
Load Regulation (Note 2) 5.0 mA \leq I _O \leq 1.5 A, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A 250 mA \leq I _O \leq 750 mA	Regload	_ _ _	2.0 1.8 1.5	100 100 50	mV
Quiescent Current T _J = 25°C	lΒ	- -	- 3.5	6.0 6.0	mA
Quiescent Current Change 21 Vdc \leq V $_{in}$ \leq 33 Vdc, I $_{O}$ = 500 mA 21 Vdc \leq V $_{in}$ \leq 33 Vdc, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A	ΔlB	_ _ _	- - -	0.8 0.8 0.5	mA
Ripple Rejection 22 Vdc \leq V _{in} \leq 32 Vdc, f = 120 Hz, I _O = 500 mA	RR	-	57	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	_	μV/VΟ
Output Resistance f = 1.0 kHz	rO	_	1.3	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCVO	_	-1.5	_	mV/°C

ELECTRICAL CHARACTERISTICS ($V_{in} = 33 \text{ V}$, $I_{O} = 500 \text{ mA}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

		MC7824B			MC7824C			
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	VO	23	24	25	23	24	25	Vdc
Output Voltage $ (5.0 \text{ mA} \leq I_O \leq 1.0 \text{ A}, P_D \leq 15 \text{ W}) $ $ 27 \text{ Vdc} \leq V_{in} \leq 38 \text{ Vdc} $ $ 28 \text{ Vdc} \leq V_{in} \leq 38 \text{ Vdc} $	Vo	_ 22.8	_ 24	_ 25.2	22.8 -	24 -	25.2 –	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 2) 27 Vdc $\leq V_{in} \leq 38$ Vdc 30 Vdc $\leq V_{in} \leq 36$ Vdc	Regline	- -	11.5 3.8	480 240	_ _	11.5 3.8	480 240	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 2) 5.0 mA $\leq I_O \leq$ 1.5 A 250 mA $\leq I_O \leq$ 750 mA	Reg _{load}	_ _	2.1 1.8	480 240	_ _	2.1 1.8	480 240	mV
Quiescent Current (T _J = 25°C)	ΙΒ	-	3.6	8.0	-	3.6	8.0	mA

^{2.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

$\textbf{ELECTRICAL CHARACTERISTICS (continued)} \ (V_{in} = 33 \ \text{V}, \ I_{O} = 500 \ \text{mA}, \ T_{J} = T_{low} \ \text{to } T_{high} \ [\text{Note 1}], \ \text{unless otherwise noted.})$

		MC7824B			MC7824C			
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Quiescent Current Change 27 Vdc \leq V _{in} \leq 38 Vdc 28 Vdc \leq V _{in} \leq 38 Vdc 5.0 mA \leq I _O \leq 1.0 A	ΔΙΒ	- - -	- - -	- 1.0 0.5	- - -	- - -	1.0 - 0.5	mA
Ripple Rejection 28 Vdc ≤ V _{in} ≤ 38 Vdc, f = 120 Hz	RR	-	54	_	-	54	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	_	-	2.0	-	Vdc
Output Noise Voltage (T _A = 25° C) 10 Hz \leq f \leq 100 kHz	V _n	-	10	-	-	10	_	μ٧/٧Ο
Output Resistance f = 1.0 kHz	rO	-	1.4	_	-	1.4	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	Isc	_	0.2	-	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	_	-	2.2	_	А
Average Temperature Coefficient of Output Voltage	TCVO	_	-2.0	_	-	-2.0	-	mV/°C

ELECTRICAL CHARACTERISTICS ($V_{in} = 33 \text{ V}$, $I_{O} = 1.0 \text{ A}$, $T_{J} = T_{low}$ to T_{high} [Note 1], unless otherwise noted.)

		ı	MC7824AC			
Characteristic	Symbol	Min	Тур	Max	Unit	
Output Voltage (T _J = 25°C)	VO	23.5	24	24.5	Vdc	
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 27.3 Vdc \leq V _{in} \leq 38 Vdc	Vo	23	24	25	Vdc	
Line Regulation (Note 2) $27 \text{ Vdc} \le V_{in} \le 38 \text{ Vdc}, I_O = 500 \text{ mA}$ $30 \text{ Vdc} \le V_{in} \le 36 \text{ Vdc}$ $30 \text{ Vdc} \le V_{in} \le 36 \text{ Vdc}, T_J = 25^{\circ}\text{C}$ $26.7 \text{ Vdc} \le V_{in} \le 38 \text{ Vdc}, T_J = 25^{\circ}\text{C}$	Reg _{line}	- - - -	11.5 3.8 3.8 10	240 240 120 240	mV	
Load Regulation (Note 2) 5.0 mA \leq IO \leq 1.5 A, TJ = 25°C 5.0 mA \leq IO \leq 1.0 A 250 mA \leq IO \leq 750 mA	Reg _{load}	- - -	2.1 2.0 1.8	100 100 50	mV	
Quiescent Current $T_J = 25^{\circ}C$	IB	_ _	- 3.6	6.0 6.0	mA	
Quiescent Current Change 27.3 Vdc \leq V _{in} \leq 38 Vdc, I _O = 500 mA 27.3 Vdc \leq V _{in} \leq 38 Vdc, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A	ΔΙΒ	_ _ _	_ _ _	0.8 0.8 0.5	mA	
Ripple Rejection 28 Vdc \leq V _{in} \leq 38 Vdc, f = 120 Hz, I _O = 500 mA	RR	_	54	-	dB	
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	Vdc	
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	Vn	_	10	-	μV/VΟ	
Output Resistance (f = 1.0 kHz)	ro	-	1.4	_	mΩ	
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	А	
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	А	
Average Temperature Coefficient of Output Voltage	TCVO	-	-2.0	_	mV/°C	

^{2.} Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

Figure 1. Peak Output Current as a Function of Input/Output Differential Voltage (MC78XXC, AC, B)

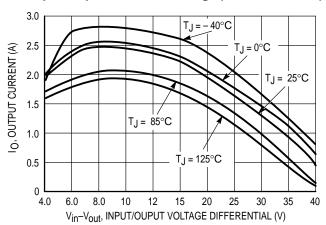


Figure 2. Ripple Rejection as a Function of Output Voltages (MC78XXC, AC)

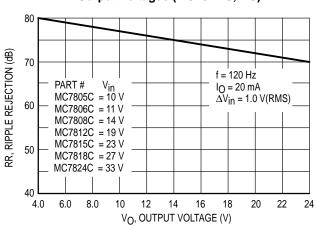


Figure 3. Ripple Rejection as a Function of Frequency (MC78XXC, AC)

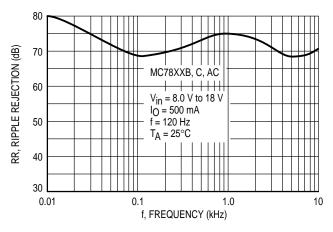


Figure 4. Output Voltage as a Function of Junction Temperature (MC7805C, AC, B)

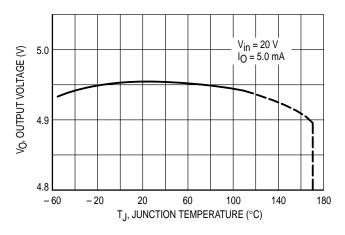


Figure 5. Output Impedance as a Function of Output Voltage (MC78XXC, AC)

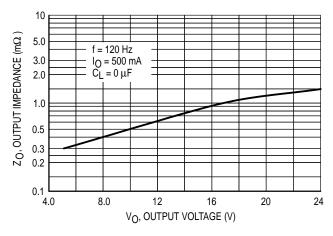
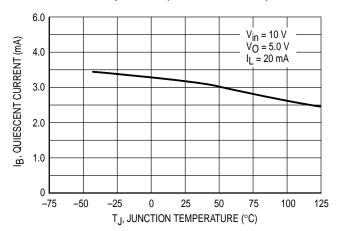


Figure 6. Quiescent Current as a Function of Temperature (MC78XXC, AC, B)



APPLICATIONS INFORMATION

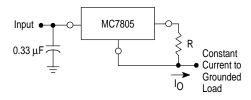
Design Considerations

The MC7800 Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition, Internal Short Circuit Protection that limits the maximum current the circuit will pass, and Output Transistor Safe–Area Compensation that reduces the output short circuit current as the voltage across the pass transistor is increased.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long

wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high–frequency characteristics to insure stable operation under all load conditions. A 0.33 μF or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Normally good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead.

Figure 7. Current Regulator



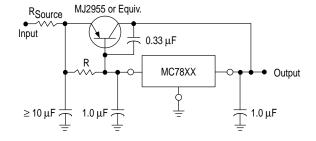
The MC7800 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC7805C is chosen in this application. Resistor R determines the current as follows:

$$I_0 = \frac{5.0 \text{ V}}{R} + I_B$$

 $I_B \cong 3.2$ mA over line and load changes.

For example, a 1.0 A current source would require R to be a 5.0 $\,\Omega_{\rm L}$ 10 W resistor and the output voltage compliance would be the input voltage less 7.0 V.

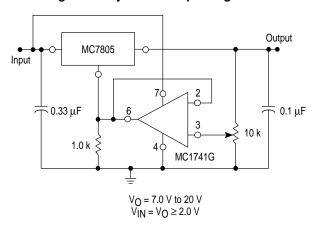
Figure 9. Current Boost Regulator



XX = 2 digits of type number indicating voltage.

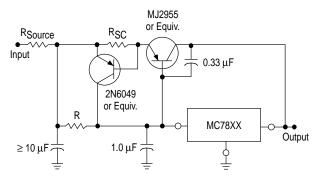
The MC7800 series can be current boosted with a PNP transistor. The MJ2955 provides current to 5.0 A. Resistor R in conjunction with the V_{BE} of the PNP determines when the pass transistor begins conducting; this circuit is not short circuit proof. Input/output differential voltage minimum is increased by V_{BE} of the pass transistor.

Figure 8. Adjustable Output Regulator



The addition of an operational amplifier allows adjustment to higher or intermediate values while retaining regulation characteristics. The minimum voltage obtainable with this arrangement is 2.0 V greater than the regulator voltage.

Figure 10. Short Circuit Protection



XX = 2 digits of type number indicating voltage.

The circuit of Figure 9 can be modified to provide supply protection against short circuits by adding a short circuit sense resistor, R_{SC} , and an additional PNP transistor. The current sensing PNP must be able to handle the short circuit current of the three–terminal regulator. Therefore, a four–ampere plastic power transistor is specified.

Figure 11. Worst Case Power Dissipation versus
Ambient Temperature (Case 221A)

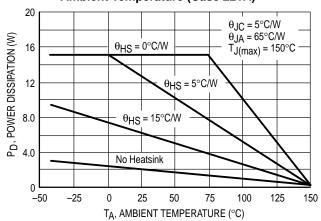


Figure 12. Input Output Differential as a Function of Junction Temperature (MC78XXC, AC, B)

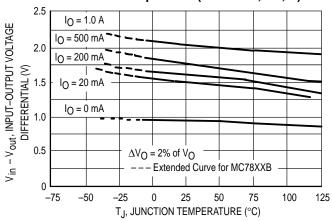
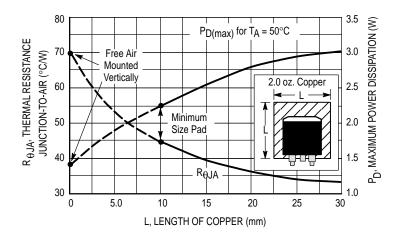


Figure 13. D²PAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length



DEFINITIONS

Line Regulation – The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation – The change in output voltage for a change in load current at constant chip temperature.

Maximum Power Dissipation – The maximum total device dissipation for which the regulator will operate within specifications.

Quiescent Current – That part of the input current that is not delivered to the load.

Output Noise Voltage – The rms AC voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

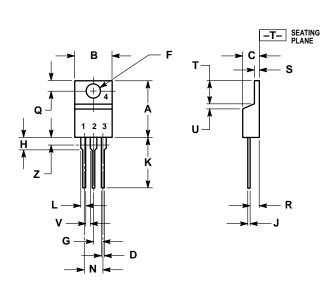
Long Term Stability – Output voltage stability under accelerated life test conditions with the maximum rated voltage listed in the devices' electrical characteristics and maximum power dissipation.

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and are registered trademarks of Motorola, Inc. Motorola, inc. is an Equal Opportunity/Affirmative Action Employer.

OUTLINE DIMENSIONS

T SUFFIX PLASTIC PACKAGE

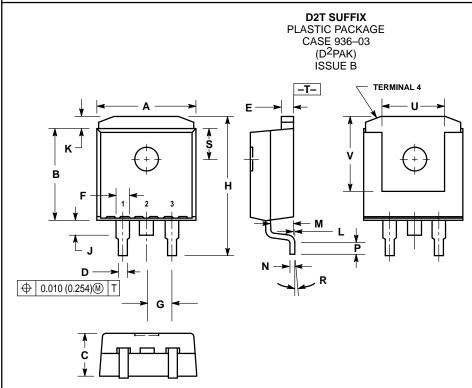
CASE 221A-06 ISSUE Y



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
 DIM Z DEFINES A ZONE WHERE ALL BODY AND
 LEAD IRREGULARITIES ARE ALLOWED.

	INC	INCHES MILL				
DIM	MIN	MAX	MIN	MAX		
A	0.570	0.620	14.48	15.75		
В	0.380	0.405	9.66	10.78		
Ċ	0.160	0.190	4.07	4.82		
D	0.025	0.035	0.64	0.88		
F	0.142	0.147	3.61	3.73		
G	0.095	0.105	2.42	2.66		
H	0.110	0.155	2.80	3.93		
J	0.018	0.025	0.46	0.64		
K	0.500	0.562	12.70	14.27		
L	0.045	0.060	1.15	1.52		
N	0.190	0.210	4.83	5.33		
Q	0.100	0.120	2.54	3.04		
R	0.080	0.110	2.04	2.79		
S	0.045	0.055	1.15	1.39		
T	0.235	0.255	5.97	6.47		
U	0.000	0.050	0.00	1.27		
٧	0.045	_	1.15			
Z	_	0.080	-	2.04		



NOTES:

- 1 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2 CONTROLLING DIMENSION: INCH.
- 3 TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A AND K.
- A AND A.

 A DIMENSIONS U AND V ESTABLISH A MINIMUM
 MOUNTING SURFACE FOR TERMINAL 4.

 5 DIMENSIONS A AND B DO NOT INCLUDE MOLD
- FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
Е	0.045	0.055	1.143	1.397
F	0.051 REF		1.295 REF	
O	0.100 BSC		2.540 BSC	
Н	0.539	0.579	13.691	14.707
J	0.125 MAX		3.175 MAX	
K	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254
M	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
Р	0.058	0.078	1.473	1.981
R	5° REF		5°REF	
S	0.116 REF		2.946 REF	
C	0.200 MIN		5.080 MIN	
٧	0.250 MIN		6.350 MIN	

Literature Distribution Centers:

USA/EUROPE: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. JAPAN: Nippon Motorola Ltd.; 4–32–1, Nishi-Gotanda, Shinagawa-ku, Tokyo 141, Japan.

ASIA PACIFIC: Motorola Semiconductors H.K. Ltd.; Silicon Harbour Center, No. 2 Dai King Street, Tai Po Industrial Estate, Tai Po, N.T., Hong Kong.



