

REVISIONS

LTR	ZONE	DESCRIPTION	DATE	APPROVED
B		REDRAWN & REVISED PER ECO 910309 REV 5 PCB	8-27-91	R. BUCH
C		REVISED PER ECO 910464	12-5-91	R. BUCH

commodore

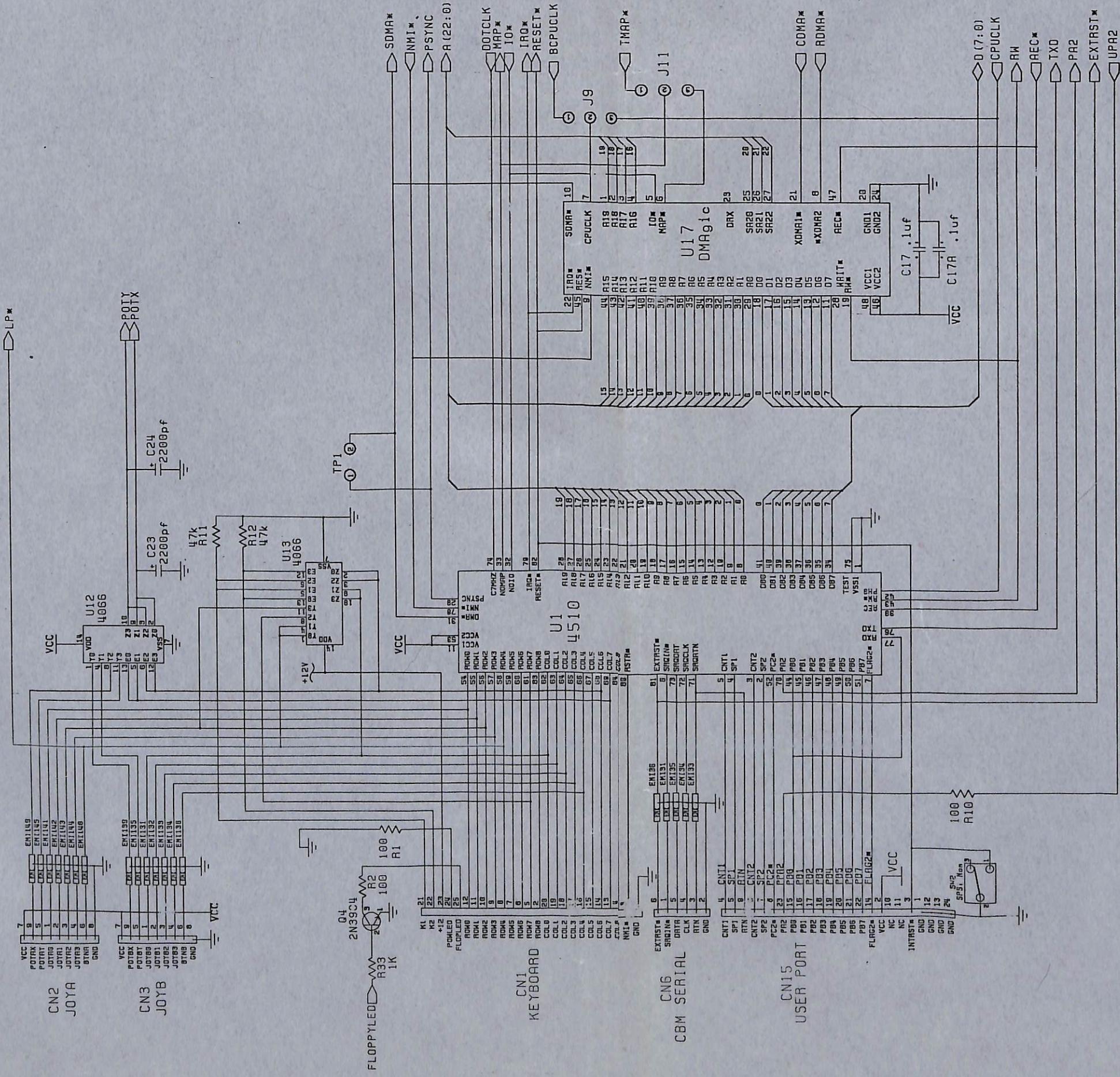
SCHEMATIC

C65 PCB ASSY

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UNLESS OTHERWISE SPECIFIED			DRAWN BY: G. BERLIN	
TOLERANCES ON: DECIMALS			CHKD:	
.X	.XX	.XXX	<'S	ENGR:
±	±	±	±	APPR:
MATERIAL:			USED ON	NEXT ASSY
FINISH:			C65	313365
SIZE	REV			
B	C			
SCALE	SHEET / OF			



CREATE BY	commodore		
G. BERNIN	C65 R5		
DATE DRAWN			
2/27/81			
ENGR'D	CPU AND DMA		
PAUL LASASA			
DATE APPROVED	3133366	C	
11/15/81			
PATENTNAME	/projdir/c65/r5.apu	Sheet	2 of 7

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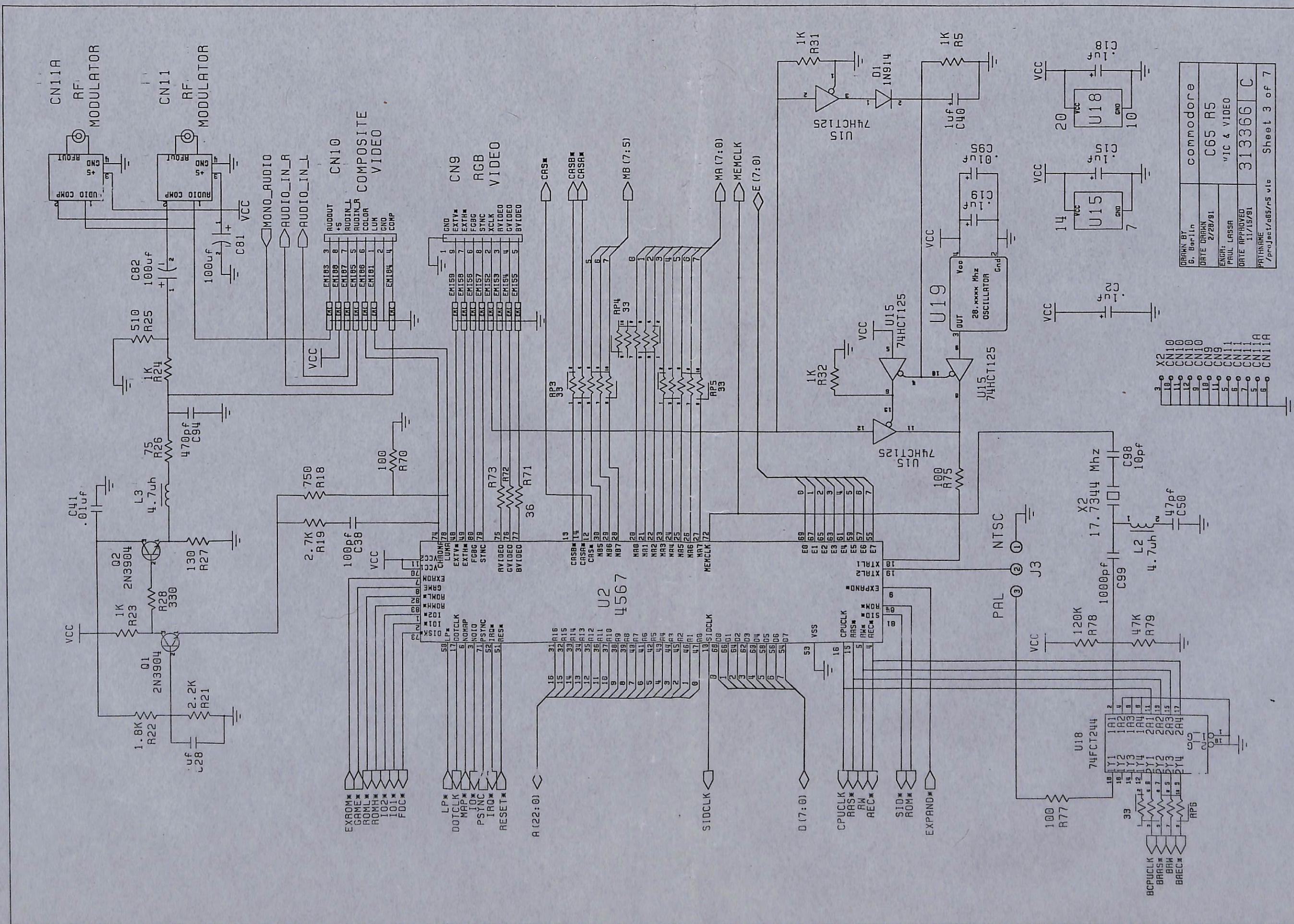
C.R.D. Generated
No manual changes may be made to this document

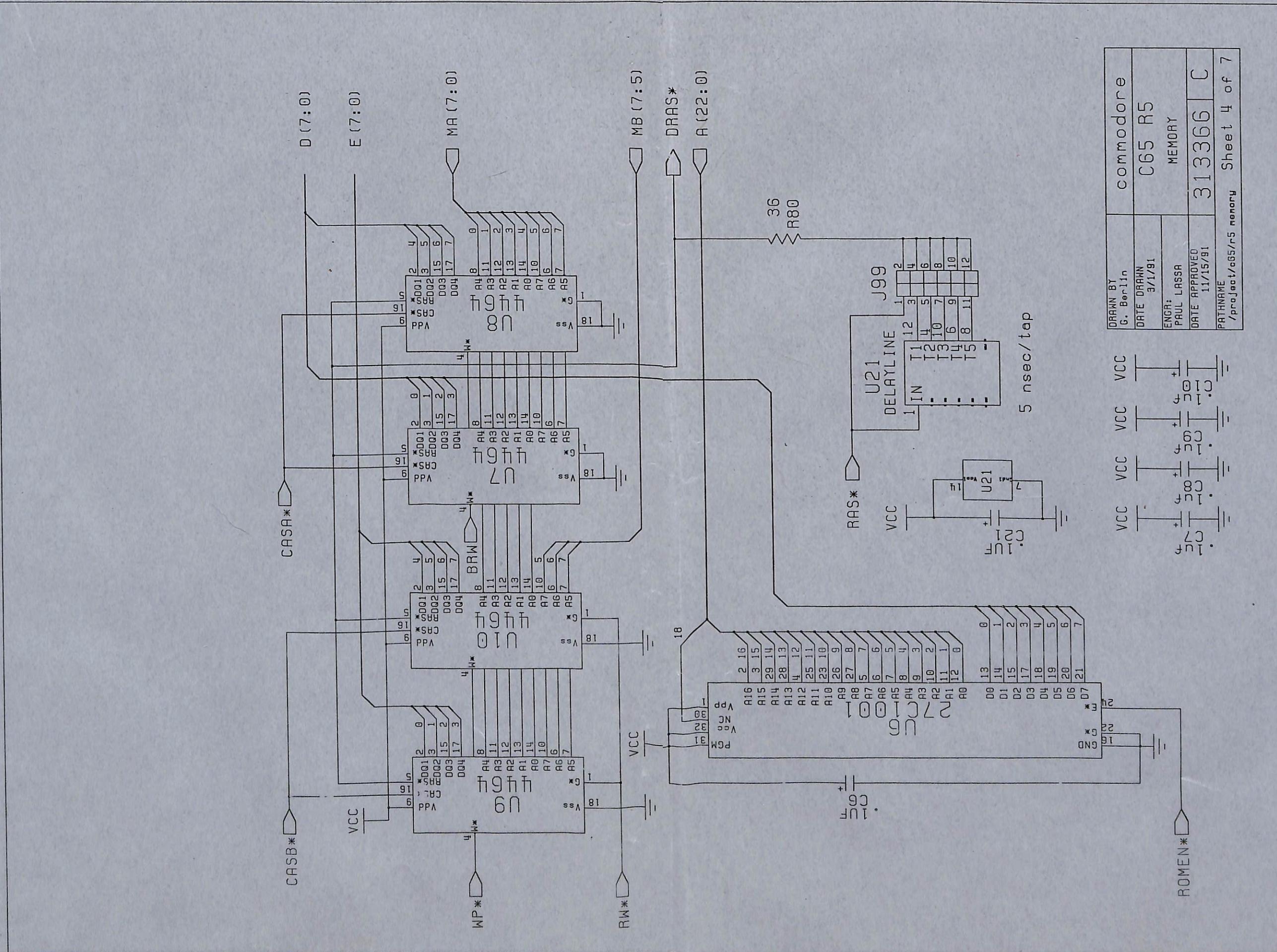
C15
Luf

C13
. Lup
+12V

200

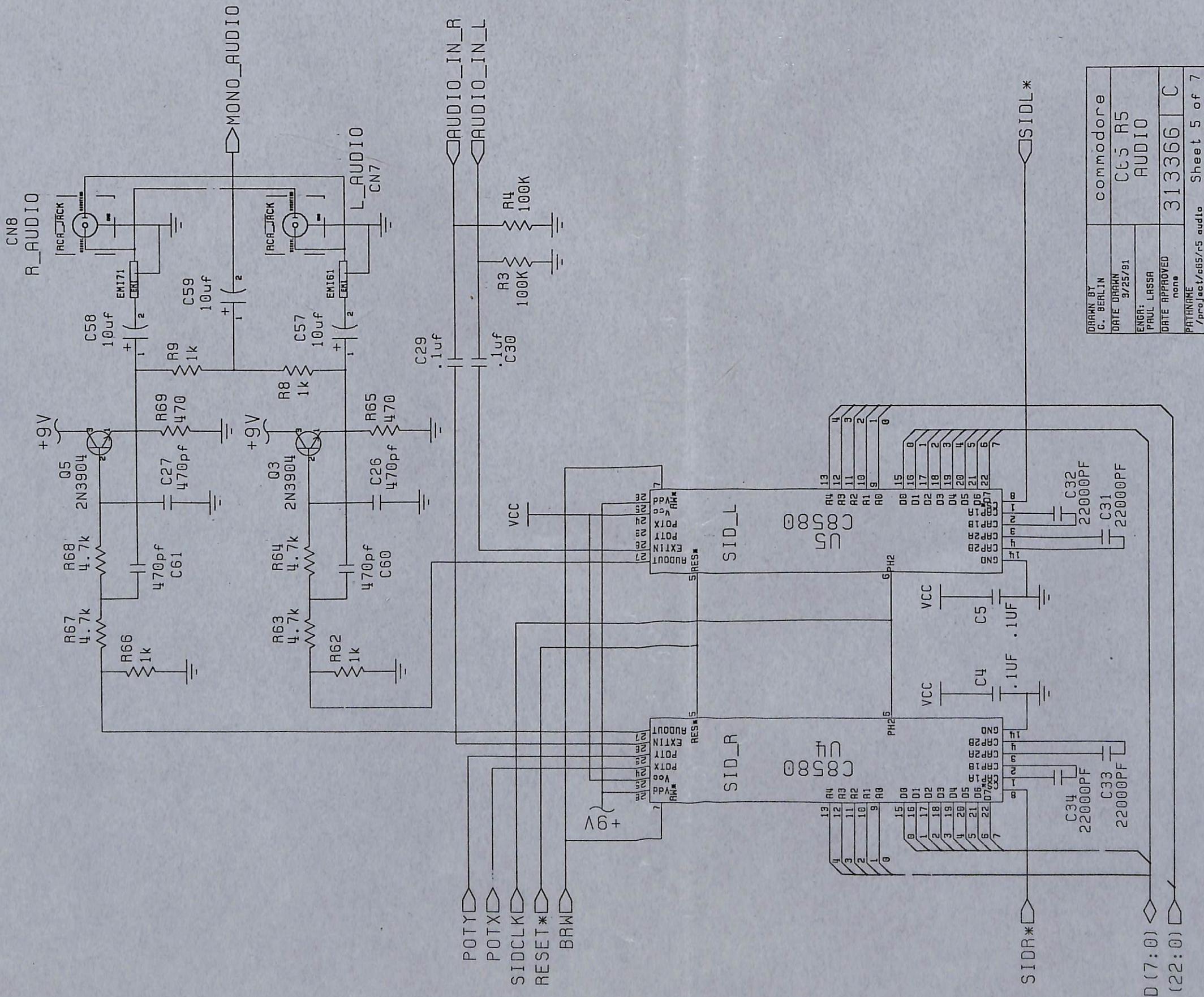
11
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4
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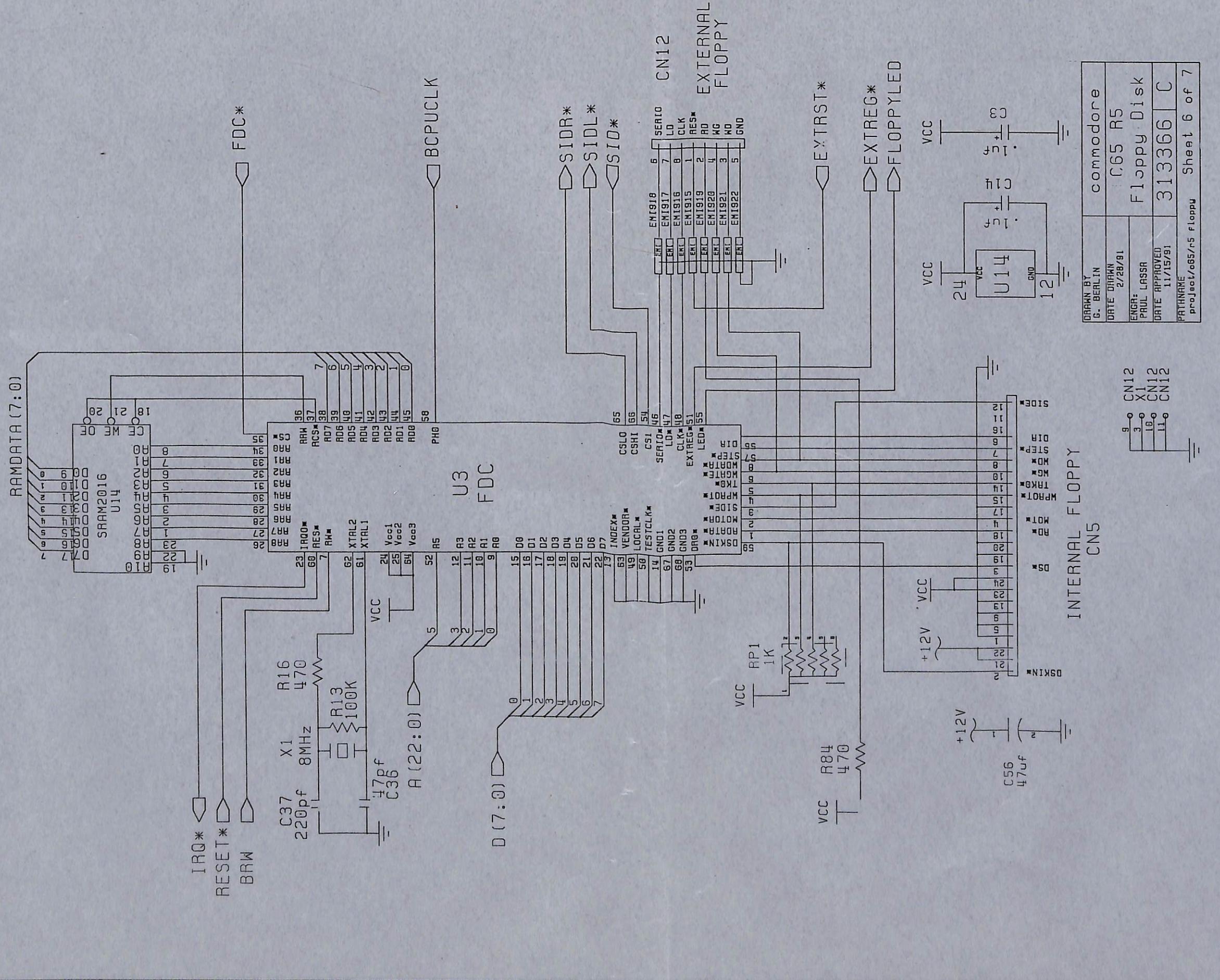




DRAWN BY	G. Berlin	commodore
DATE DRAWN	3/1/91	
ENGR:	PAUL LASA	
DATE APPROVED	11/15/91	
PATHNAME	313366 C	
/project/c65/rs memory Sheet 4 of 7		

VCC	VCC	VCC	VCC
+-----	+-----	+-----	+-----
+-----	+-----	+-----	+-----
+-----	+-----	+-----	+-----





DRAWN BY G. BERLIN	commodore
DATE DRAWN 2/28/91	C65 R5
ENGR: PAUL LASA	Floppy Disk
DATE APPROVED 11/15/91	313366 C
PATHNAME project/o65/r5 floppy	
Sheet 6 of 7	

9	CN12
3	X1
10	CN12
11	CN12

INTERNAL FLOPPY CNS

