Bitfire Development Kit

- ARM7: Philips LPC2129 ARM7TDMI with 256kB Flash and 16kB RAM
- FPGA: Altera Cyclone EP1C6T144 FPGA with configuration memory
- Giant high intensity bicolor LED-matrix (40x16, with 1280 LEDs total)
- Dual high speed CAN channels with programmable termination
- Onboard tools - Macraigor compliant wiggler and ByteBlaster compliant J-tag interface
- Connectors for external J-Tag tools
- Flexible expansion connectors for user as well as dedicated module expansion
- Flexible UART I/F, programmable RS232 and RS485 modes
- Complete software development and debug tools based on GCC
- Complete FPGA development tools
- Fully documented BSP, Graphics Library and examples supplied in C and assembler source
- Fully documented FPGA Reference design supplied in VHDL source
- 2 ported operating systems, FreeRTOS and uC/OS-II

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