Signal Exceptions Part 1





















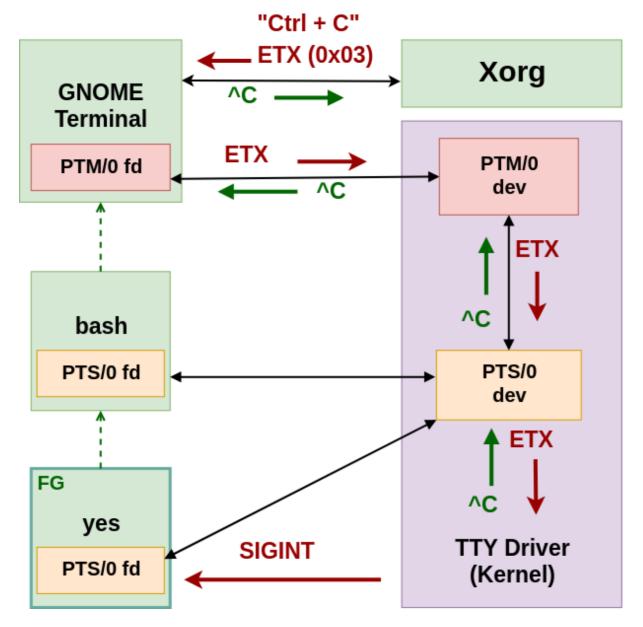






What happened here?

```
martin@vmhost:~
File Edit View Search Terminal Help
[martin@vmhost ~]$ yes
[martin@vmhost ~]$
```



More information:

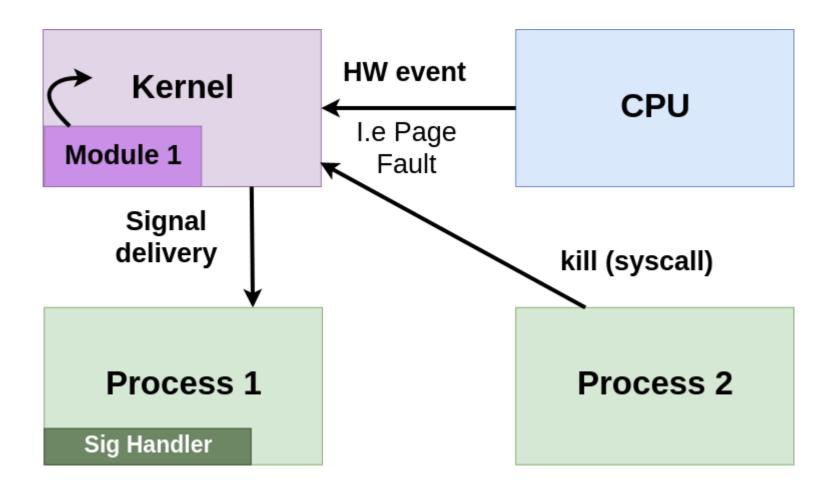
https://martin.uy/blog/h ow-terminal-emulatorswork-on-linux/

```
Signal target:
"yes"
Signal:
0x2 (SIGINT)
Backtrace:
#0 send signal at kernel/signal.c
   n tty receive char special (c=3) at
drivers/tty/n tty.c
#15 receive buf at drivers/tty/tty buffer.c
#19 kthread at kernel/kthread.c
```

What happened here?

```
int main(void) {
  long addr = 0x0L;
  *(int*)addr = 0;
}
```

```
Signal target:
"main"
Signal:
0xb (SIGSEGV)
Backtrace:
    send_signal at kernel/signal.c
#0
#6
    bad area (address=0) at arch/x86/mm/fault.c
#10 async page fault at
arch/x86/entry/entry 64.S
```



- Portable Operating System Interface (POSIX)
 - APIs, shells, utilities, etc.
 - Linux distributions implement most of the standard



- Signals: asynchronous IPC mechanism to notify processes or threads
- Originated in Bell Labs Unix (1970s)
- SIGSEGV, SIGKILL, SIGILL, SIGFPE, etc.
- Default or custom handlers



Signal handler setup (API)

```
void sigsegv handler (int signal,
                      siginfo t* info,
                      void* context) {
void main() {
  struct sigaction act = \{0x0\};
  act.sa sigaction = sigsegv handler;
  act.sa flags = SA SIGINFO;
  sigaction(SIGSEGV, &act, NULL);
```

- Handler is global to the processes group
- Multiple libraries, only one handler?
 - Signal demultiplexing
- Handler is executed in the context of a trap
 - Only async-signal-safe functions can be invoked
 - Reentrancy: what happens if a signal interrupts the execution of a non-reentrant function and the function is invoked again from the handler?
 - Handler has to be reentrant in respect to global variables

Idea



```
void main(void) {
    try {
      long addr = 0 \times 0 L;
      *(int*)addr = 0;
  } catch {
      printf("SIGSEGV\n");
                 gcc -fsignal-exceptions -o main main.c
```

Scope



•GCC



GNU Linker



Glibc



Kernel



Exceptions in C++



What is an exception?

```
void f(void) {
    throw std::exception();
int main(void) {
    try {
      f();
    } catch (std::exception& e) {
      std::cout << "Catch" <<
           std::endl;
```

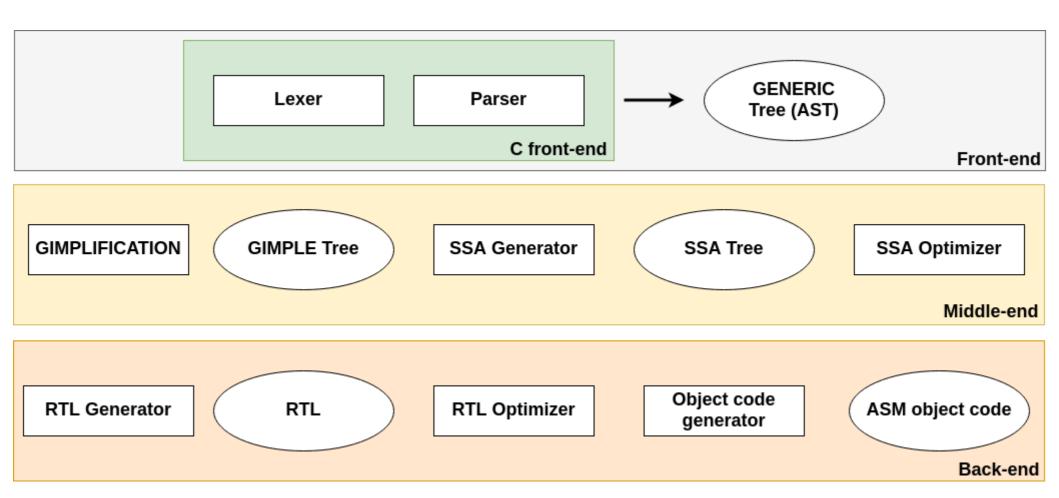
Compiler and Runtime

```
00000000004011d2 < Z1fv>:
                      callq 401070 < __cxa_allocate_exception@plt>
4011e0: e8 8b fe ff ff
4011e5: 48 89 c3
                      mov %rax,%rbx
                      mov %rbx,%rdi
4011e8: 48 89 df
4011eb: e8 d6 00 00 00 callq 4012c6 < ZNSt9exceptionC1Ev>
4011f0: ba f0 10 40 00 mov $0x4010f0,%edx
4011f5: be c0 3d 40 00
                      mov $0x403dc0,%esi
                      mov %rbx,%rdi
4011fa: 48 89 df
                      callq 401080 < __cxa_throw@plt>
4011fd: e8 7e fe ff ff
libstdc++.so.6 => /lib64/libstdc++.so.6 (0x00007fca1cd66000)
8f4b0 FUNC GLOBAL DEFAULT __cxa_throw@@CXXABI_1.3
```

- GCC
- libgcc (libgcc_s.so.1)
 - Runtime with stack unwinder (setjmp/longjmp in C)

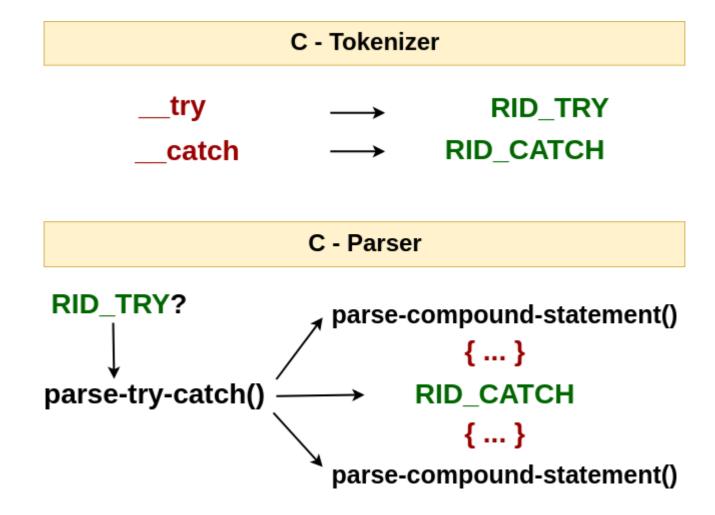
GCC overview





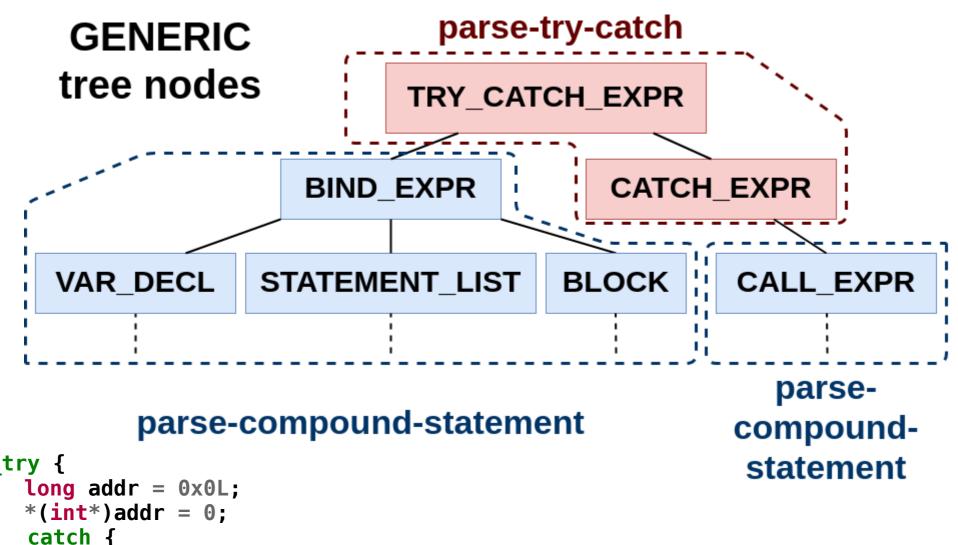
Lexical analysis and parsing





Abstract Syntax Tree (AST)





printf("SIGSEGV\n");

Optimizations



- Remove dead code and annotations: may an instruction throw?
- For SIGSEGV read or write memory accesses are relevant
 - All of them?
 - References to variables
 - References to memory (I.e.: *(expr))
 - Arrays with safe indexes (known at compile time)
 - Inline assembly

Output #1: .text (code)



0000000000401132 <main>:

401132: 55 push %rbp

 401133:
 48 89 e5
 mov %rsp,%rbp

 401136:
 48 83 ec 10
 sub \$0x10,%rsp

40113a: 48 c7 45 f8 00 00 00 00 movq \$0x0,-0x8(%rbp)

401142: 48 8b 45 f8 mov -0x8(%rbp),%rax

401146: c7 00 00 00 00 movl \$0x0,(%rax)

40114c: b8 00 00 00 00 mov \$0x0,%eax

401151: eb 0c jmp 40115f <main+0x2d>

401153: bf 04 20 40 00 mov \$0x402004,%edi

401158: e8 e3 fe ff ff callq 401040 <puts@plt>

40115d: eb ed jmp 40114c <main+0x1a>_

40115f: c9 leaveq

401160: c3 retq

catch

Output #2: .gcc_except_table



```
0xFF ------ landing pads format: DW EH PE omit
  0x03 ----- Types Table format: udata4
  0x11 (17) ---- offset to the end of the Types Table
  0x01 ----- region offsets format: uleb128
  0x08 ----- Call-sites (regions) Table length
  0x14(20) ---- region 0: start = offset 20
  0x06 ----- region 0: length = 6 bytes
  0x21 (33) ---- region 0: landing pad = offset 33
  0x01 ----- region 0: action = 1
  0x26 (38) ---- region 1: start = offset 38
  0x07 ----- region 1: length = 7 bytes
  0x00 ----- region 1: landing pad = 0
  0x00 - - - - region 1: action = 0
\cdot - \cdot 0x01 ----- Action Record Table Entry #1 - filter - - \cdot
  0x00 ----- Action Record Table Entry #1 - next
  0x00 ----- Padding bytes for alignment
  0x00000000 - Types Table
```

Output #2: .gcc_except_table



00000000000401132 <main>:

401132:	55	push %rbp
401133:	48 89 e5	mov %rsp,%rbp
401136:	48 83 ec 10	sub \$0x10,%rsp
40113a:	48 c7 45 f8 00 00 00 00	movq \$0x0,-0x8(%rbp)
401142:	48 8b 45 f8	mov -0x8(%rbp),%rax

no-throw

401146: c7 00 00 00 00 00

\$0x0,(%rax) movl

Region 0

40114c: b8 00 00 00 00

401151: eb 0c

\$0x0,%eax mov

40115f <main+0x2d>

no-throw

401153: bf 04 20 40 00

mov

\$0x402004,%edi

Region 0: Landing Pad

Region 1

401158: e8 e3 fe ff ff

40115d: eb ed

40115f:

c9

401160: c3

callq 401040 <puts@plt>

40114c <main+0x1a>

leaveg

retq

jmp

Output #3: .eh_frame & .eh_frame_hdr



- Information for call stack unwinding
- Call Frame Information (CFI) #1
 - Common Information Entry (CIE)
 - Frame Description Entry (FDE) #1
 - Call Frame Instructions

- ...

• ...

And then?



- When a binary with exceptions information is executed:
 - 1) **Is it necessary** to register a signals handler?
 - 2) Who and when does register it?
 - 3) What happens if the signals handler gets called anytime?

Handler registration

- GCC generated binaries with exceptions handling are indistinguishable
 - I.e.: setjmp/longjmp exceptions or signalexceptions?
- Idea: artificially generate from GCC a call to the runtime in signal-exceptions binaries
 - Only one call (load time)
 - If the runtime did not previously register a handler, do it

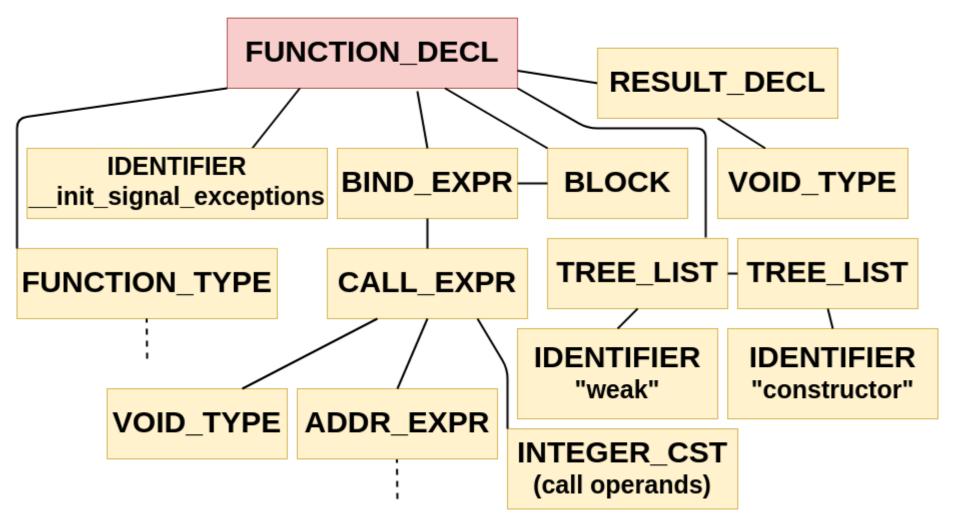
Handler registration

```
_attribute___((constructor,weak))
void __init_signal_exceptions (void) {
      built in register signal_exceptions();
401161: 55
                     push %rbp
401162: 48 89 e5 mov %rsp,%rbp
401165: e8 c6 fe ff ff callq 401030 < register_signal_exceptions@plt>
                           %rbp
40116a: 5d
                    pop
40116b: c3
                     retq
Hex dump of section '.init array':
 0x00403dd8 30114000 00000000 61114000 00000000
                                                        Compiled binary
                                                         libgcc (runtime)
readelf -s libgcc s.so.1
011000 164 FUNC GLOBAL DEFAULT _register_signal_exceptio
```

AST nodes injection



cgraph_node::add_new_function (...)



Stack unwinding (libgcc)



```
void register signal exceptions (void) {
 if (sigexcept_status != SIGEXCEPT_UNREGISTERED)
  return;
 if (sigaction(SIGSEGV, &sigsegv_sa, NULL) != 0)
  goto error;
 sigexcept status = SIGEXCEPT REGISTERED;
 return;
```

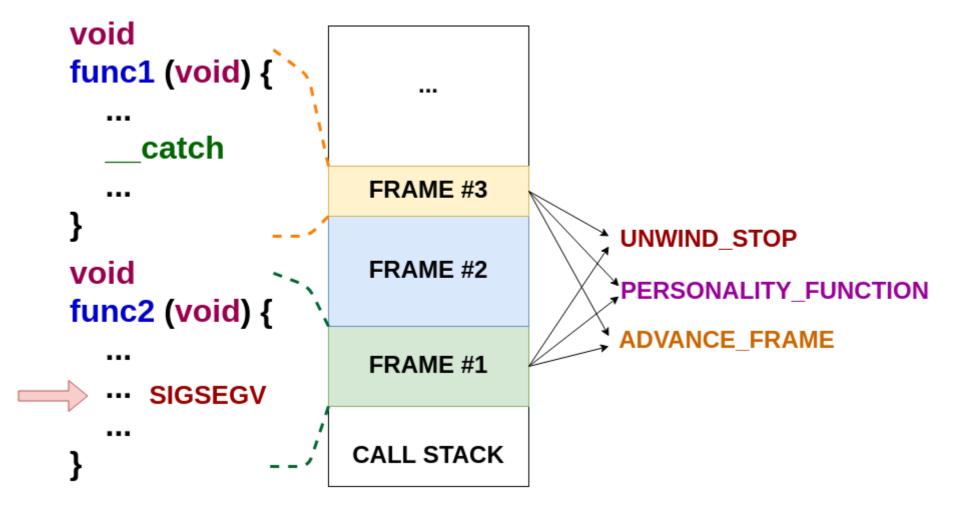
Stack unwinding (libgcc)

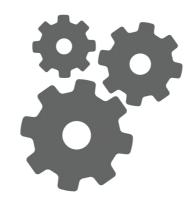


```
static void x86_64_sigexcept_sigsegv_handler (
     int signal, siginfo_t* info, void* context) {
   context.gregs[RSP] -= sizeof(void*);
   *(context.gregs[RSP]) = context.gregs[RIP] + 1;
   context.gregs[RIP] = sigexcept unwind trampoline;
                                  callq 401080 < __cxa_throw@plt>
static void sigexcept_unwind_trampoline (void) {
   struct Unwind Exception *exc = ...;
   exc->exception_cleanup = sigexcept_exception_cleanup;
   _Unwind_ForcedUnwind(exc, sigexcept_unwind_stop, NULL);
```

Stack unwinding (libgcc)







Demo

Future work



```
void main(void) {
   try {
     long addr = 0 \times 0 L;
      *(int*)addr = 0;
 } __catch (SIGSEGV : int signal,
siginfo t* info, void* context) {
      printf("SIGSEGV\n");
                gcc -fsignal-exceptions -o main main.c
```



Thanks!

https://martin.uy/blog/gcc-signal-exceptions-part-1

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