Laboratory Worksheet #04 Hardware: Digital Input and Output Exercise

When complete, insert Worksheet 04 in your laboratory notebook. Worksheets are required when the notebooks are graded. Perform any necessary calculations on the left page of the notebook where the worksheet is placed. Keep individual copies of the worksheet for your own records. This worksheet is a pre-lab exercise to be done before starting Lab 1-1 and should not be confused with the lab.

One of the important aspects of the software is initializing Special Function Registers (SFRs). In Laboratory 1, you will create initialization functions for Port I/O, which involves setting the correct SFR bits to 0 or 1, as needed. The logic assignment operations developed in Worksheet 2 are used to set the appropriate bits without changing the other bits.

Additionally, using the sbit command to assign a variable name to a single bit in the SFR can make programming and code execution much simpler. You will read from or write to individual bits when performing Input/Output operations on the Port pins. These read and write operations will be performed using the sbit labels assigned to the specific Port pins.

As an example problem, Port 2 will be configured for both input and output. Note, this is an example problem and is not to confused with Laboratory 1. The followed Port bits will be assigned as inputs or outputs:

Pn.m (Port n, bit m)	Description	Bit Label
P2.1	Input bit for doorbell	DB
P2.3	Input bit for an alarm clock	AC
P2.5	Output bit for a porch light	Porch
P2.6	Input bit for a garage door	GD

All other bits are considered previously assigned and should not be changed.

Refer to the manual section *Input/Output Ports on the C8051* or the course slides to determine the syntax. The memory locations for Port 2 bit 0 is 0xA0, Port 2 bit 1 is 0xA1, through Port 2 bit 7 at 0xA7. Complete the following four lines of code to assign the labels to the appropriate bit using the sbit command.

_sbit _at 0x	// remember to include the double " $$ "

In the following SFR data tables, indicate whether the bit should be set high (1), low (0), or undetermined/unchanged (X).

bit	7	6	5	4	3	2	1	0
etermine t	he bit mask	for setting	g the approp	oriate bits hi	gh (logic 1)	P2MDO	UT = .	
etermine t	he bit mask	for setting	g the approp	oriate bits lo	w (logic 0)	P2MDO	UT & = .	
l (input bit	ts are set to	1 which is	a high impe	edance state.	, all other bits	s are uncha	nged X)	
bit	7	6	5	4	3	2	1	0
summary,	using the al	oove defini	tions, compl	ete the Port.	Init() function	on for this e	$\mathbf{xample}.$	
ort_Init()								
			_; /,	/configure Po	ort 2 bits as i	nputs		
			_; /,	/configure Po	ort 2 bits as o	outputs		
			_; /.	/set Port 2 is	nput bits to a	high impe	dance state	

When complete, include Worksheet 4 with your Laboratory 1-1 Pre-lab submission.

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