NOTES	SoC Pin #	MicroZed Net	JX1 PIN #	JX1 PIN#	MicroZed Net	SoC Pin #	NOTES
TP69	Bank 0, F9	JTAG_TCK	1	2	JTAG_TMS	Bank 0, J6	TP67
TP70	Bank 0, F6	JTAG_TDO	3	4	JTAG_TDI	Bank 0, G6	TP64
TP71	N/A	PWR_ENABLE	5	6	CARRIER_SRST#	N/A	TP65
TP72	N/A	FPGA_VBATT	7	8	FPGA_DONE	Bank 0, R11	TP66
	Bank 34, R19	JX1_SE_0	9	10	JX1_SE_1	Bank 34, T19	
	Bank 34, T11		11	12		Bank 34, T12	
	Bank 34, T10		13	14		Bank 34, U12	TP68
	N/A	GND	15	16	GND	N/A	
	Bank 34, U13	CAEN DATA RDY	17	18	MZ Нарру	Bank 34, V12	
	Bank 34, V13	GENERIC PULSE	19	20	TUBII RT OUT	Bank 34, W13	
	N/A	GND	21	22	GND	N/A	
	Bank 34, T14	GENERIC DELAY	23	24	SCALER 4	Bank 34, P14	
	Bank 34, T15	CLOCKS DATA RDY	25	26	SCALER 5	Bank 34, R14	
	N/A	GND	27	28	GND	N/A	
	Bank 34, Y16		29	30	SCALER 6	Bank 34, W14	
	Bank 34, Y17		31	32	CNT REG CHK	Bank 34, Y14	
	N/A	GND	33	34	GND	N/A	
	Bank 34, T16		35	36	READ CNTRL REG	Bank 34, V15	
	Bank 34, U17		37	38	CNTRL RDY	Bank 34, W15	
	N/A	GND	39	40	GND	N/A	
	Bank 34, U14	BCKP CLK IN USE	41	42		Bank 34, U18	
	Bank 34, U15	CLK100 IN	43	44		Bank 34, U19	
	N/A	GND	45	46	GND	N/A	
	Bank 34, N18	RESET CLK	47	48		Bank 34, N20	
	Bank 34, P19		49	50		Bank 34, P20	
	N/A	GND	51	52	GND	N/A	
	Bank 34, T20		53	54		Bank 34, V20	
	Bank 34, U20		55	56		Bank 34, W20	
+5V	N/A	VIN_HDR	57	58	VIN_HDR	N/A	+5V
+5V	N/A	VIN_HDR	59	60	VIN_HDR	N/A	+5V
	Bank 34, Y18	UNUSED MZ	61	62	EXT TRIG 7	Bank 34, V16	
	Bank 34, Y19	UNUSED MZ	63	64	EXT TRIG 6	Bank 34, W16	
	N/A	GND	65	66	GND	N/A	
	Bank 34, R16	UNUSED MZ	67	68	EXT TRIG 5	Bank 34, T17	
	Bank 34,R17	UNUSED MZ	69	70	EXT TRIG 4	Bank 34, R18	
	N/A	GND	71	72	GND	N/A	
	Bank 34, V17	UNUSED MZ	73	74	EXT TRIG 3	Bank 34, W18	
	Bank 34, V18	UNUSED MZ	75	76	EXT TRIG 2	Bank 34, W19	
	N/A	GND	77	78	VCCO_34	N/A	VCCIO (3.3V)
VCCIO (3.3V)	N/A	VCCO_34	79	80	VCCO_34	N/A	VCCIO (3.3V)
	Bank 34, N17	UNUSED MZ	81	82	EXT TRIG 1	Bank 34, P15	
	Bank 34, P18	UNUSED MZ	83	84	EXT TRIG 0	Bank 34, P16	
	N/A	GND	85	86	GND	N/A	
	Bank 13, U7		87	88		Bank 13, T9	
	Bank 13, V7		89	90		Bank 13, U10	
	Bank 13, V8		91	92		Bank 13, T5	
	Bank 13, W8		93			Bank 13, U5	
	N/A	GND	95	96	GND	N/A	
	Bank 0, L10		97	98		Bank 0, M9	
	Bank 0, K9		99	100		Bank 0, M10	

Notes	SoC Pin #	MicroZed Net	JX2 PIN #	JX2 PIN#	MicroZed Net	SoC Pin #	Notes
TP60	Bank 500, E8	I2C_SCL	1	2	I2C_SDA	Bank 500, E9	TP54
N/A	Bank 500, C6	GPIO_0	3	4	GPIO_1	Bank 500, D9	N/A
N/A	Bank 500, E6	GPIO_2	5	6	GPIO_5	Bank 500, B5	N/A
N/A	Bank 500, C5	GPIO_3	7	8	GPIO_5	Bank 500, C8	N/A
TP61	Bank 0, R10	INIT#	9	10	VCCIO_EN	N/A	Decides when to power
TP62	Bank 500, C7	PWR_GOOD	11	12	VIN_HDR	N/A	+5V
TP63	Bank 35, G14	JX2_SE_0	13	14	JX2_SE_1	Bank 35, J15	TP59
	N/A	GND	15	16	GND	N/A	
	Bank 35, C20	SMELLIE_PRE_DELAY	17	18	Sync24	Bank 35, B19	
	Bank 35, B20	TELLIE_PRE_DELAY	19	20	Sync	Bank 35, A20	
	N/A	GND	21	22	GND	N/A	
	Bank 35, E17	Generic Delay IN	23	24	GT	Bank 35, D19	
	Bank 35, D18		25	26	MTCA_MIMIC_Trig2	Bank 35, D20	
	N/A	GND	27	28	GND	N/A	
	Bank 35, E18		29	30	MTCA_MIMIC_Trig1	Bank 35, F16	
	Bank 35, E19		31	32	MTCA_MIMIC_DATA_RDY	Bank 35, F17	
	N/A	GND	33	34	GND	N/A	
	Bank 35, L19		35	36	DATA	Bank 35, M19	For Shift Registers
	Bank 35, L20		37	38	CLK	Bank 35, M20	For Shift Registers
	N/A	GND	39	40	GND	N/A	
	Bank 35, M17		41		SPKR	Bank 35, K19	Speaker Signal
	Bank 35, M18		43	44	Tellie_Pulse	Bank 35, J19	
	N/A	GND	45	46	GND	N/A	
Latch Enable MUX bit	Bank 35, L16	LE0	47	48	Smellie_Pulse	Bank 35, K17	
Latch Enable MUX bit	Bank 35, L17	LE1	49	50	Tellie_delay	Bank 35, K18	
	N/A	GND	51	52	GND	N/A	
Latch Enable MUX bit	Bank 35, H16	LE2	53	54	Smellie Delay	Bank 35, J18	
	Bank 35, H17	MUX_ENABLE	55	56		Bank 35, H18	
	N/A	VIN_HDR	57	58	VIN_HDR	N/A	+5V
	N/A	VIN_HDR	59	60	VIN_HDR	N/A	+5V
	Bank 35, G17	EXT TRIG 10	61	62	Unused MZ	Bank 35, F19	
	Bank 35, G18	EXT TRIG 9	63	64	Unused MZ	Bank 35, F20	
	N/A	GND	65	66	GND	N/A	
	Bank 35, G19	EXT TRIG 8	67	68	Unused MZ	Bank 35, J20	
	Bank 35, G20	EXT TRIG 11	69	70	Unused MZ	Bank 35, H20	
	N/A	GND	71	72	GND	N/A	
	Bank 35, K14	EXT TRIG 14	73	74	Unused MZ	Bank 35, H15	
	Bank 35, J14	EXT TRIG 13	75	76	Unused MZ	Bank 35, G15	
	N/A	GND	77	78	VCCO_35	N/A	VCCIO (3.3V)
VCCIO (3.3V)	N/A	VCCO_35	79		VCCO_35	N/A	VCCIO (3.3V)
(3.5.7)	Bank 35, N15	EXT TRIG 12	81		Unused MZ	Bank 35,L14	(0.01)
	Bank 35, N16	EXT TRIG 15	83		Unused MZ	Bank 35,L15	
	N/A	GND	85		GND	N/A	
	Bank 35, M14	GIVE	87	<u> </u>		Bank 35, K16	
	Bank 35, M15		89			Bank 35, K16	
	N/A	GND	91		GND	N/A	
	Bank 13, Y12	BANK13_LVDS_4_P	93		BANK13_LVDS_5_P	Bank 13, V11	
	Bank 13, Y13	BANK13_LVDS_4_N	95		BANK13_LVDS_5_N	Bank 13, V10	
	Bank 13, V6	BANK13_LVDS_6_P	97		VCCO_13	N/A	
	Bank 13, W6	BANK13_LVDS_6_N	99	100	BANK13_SE_0	Bank 13, V5	