

ISE Project Navigator (P.20131013) - C:\Xilinx\codes\finalProject\finalProject.xise - [incrementer (RTL1)]

File Edit View Project Source Process Tools Window Layout Help

Design

View: Implementation Simulation

Hierarchy

- finalProject
 - xc6slx9-3tqg144
 - MIPS_top_module (MIPS_top_modu
 - pc - PC (PC.v)
 - add_1 - incrementer (increme
 - Add - add (add.v)
 - mux1 - mux_2to1 (mux_2to1.v)
 - ins_mem - instruction_memory
 - sign_extend - sign_extend (sign_
 - alu_control - ALU_control (ALU_
 - mux2 - mux_2to1 (mux_2to1.v)

No Processes Running

Processes: add_1 - incrementer

- Design Summary/Reports
- Design Utilities
- User Constraints
- Synthesize - XST
 - View RTL Schematic
 - View Technology Schematic
 - Check Syntax
 - Generate Post-Synthesis Simul...
- Implement Design
- Generate Programming File
- Configure Target Device

Libraries

Source Libraries

- work

View by Category

Design Objects of Top Level Block

Instances	Pins	Signals	Name	Value
incrementer				

Properties: (No Selection)

Console Errors Warnings Find in Files Results View by Category

[236,396]

incrementer

in(31:0)

in_plus(31:0)

incrementer

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View by Category

Design Objects of Top Level Block

Instances

- incrementer
- Madd_in_plus1

Pins

- incrementer

Signals

- incrementer

Properties of Instance: Madd_in_plus1

Name	Value
Type	Madd_in_plus1
Instance Name	Madd_in_plus1

Console Errors Warnings Find in Files Results View by Category

[1200,736]

incrementer:1

Madd_in_plus1

in(31:0) DataA(31:0) Result(31:0) in_plus(31:0)

DataB(31:0)

Madd_in_plus1

incrementer

incrementer.v incrementer_TB.v incrementer (RTL1)

8:22 PM 7/18/2020