

ModelSim SE-64 10.6d

File Edit View Compile Simulate Add Wave Tools Layout Bookmarks Window Help

100 ps

Layout Simulate ColumnLayout Default

Search:

sim - Default

Instance

- MIPS_top_module_TB
 - uut
 - #ASSIGN#16
 - #ASSIGN#48
 - #ASSIGN#49
 - #ASSIGN#50
 - #vsm_capacity#

Wave - Default

Signal	Value
/MIPS_top_module_TB/clock	1h0
/MIPS_top_module_TB/data1	32hxxxxxxxx
/MIPS_top_module_TB/data2	32hxxxxxxxx
/MIPS_top_module_TB/alu_zero	1h0
/MIPS_top_module_TB/alu_result	32hxxxxxxxx
/MIPS_top_module_TB/mux4_out	32hxxxxxxxx
/gbl/GSR	1h0

Now: 1000 ns
Cursor 1: 999.136 ns

Transcript

```
# Loading work.gbl(fast)
# ** Warning: (vsim-3015) MIPS_top_module.v(20): [PCDPC] - Port size (5) does not match connection size (32) for port 'address'. The port definition is at: instruction_memory.v(3).
# Time: 0 ps Iteration: 0 Instance: /MIPS_top_module_TB/uut/ins_mem File: instruction_memory.v
# ** Warning: (vsim-3015) MIPS_top_module.v(26): [PCDPC] - Port size (32) does not match connection size (5) for port 'in1'. The port definition is at: mux_2tol.v(3).
# Time: 0 ps Iteration: 0 Instance: /MIPS_top_module_TB/uut/mux2 File: mux_2tol.v
# ** Warning: (vsim-3015) MIPS_top_module.v(26): [PCDPC] - Port size (32) does not match connection size (5) for port 'in2'. The port definition is at: mux_2tol.v(3).
# Time: 0 ps Iteration: 0 Instance: /MIPS_top_module_TB/uut/mux2 File: mux_2tol.v
# ** Warning: (vsim-3015) MIPS_top_module.v(28): [PCDPC] - Port size (5) does not match connection size (32) for port 'writeRegister'. The port definition is at: register_bank.v(3).
# Time: 0 ps Iteration: 0 Instance: /MIPS_top_module_TB/uut/registers File: register_bank.v
# ** Warning: (vsim-3015) MIPS_top_module.v(36): [PCDPC] - Port size (7) does not match connection size (32) for port 'address'. The port definition is at: data_memory.v(3).
# Time: 0 ps Iteration: 0 Instance: /MIPS_top_module_TB/uut/data_mem File: data_memory.v
# ** Warning: (vsim-WLF-5000) WLF file currently in use: vsim.wlf
# File in use by: Marzieh Hostname: MARZIEH-DESKTOP ProcessID: 7336
# Attempting to use alternate WLF file ".\wlf8jxa7t".
# ** Warning: (vsim-WLF-5001) Could not open WLF file: vsim.wlf
# Using alternate file: .\wlf8jxa7t
# .main_pane.wave.interior.cs.body.pw.wf
# .main_pane.structure.interior.cs.body.struct
# .main_pane.objects.interior.cs.body.tree
VSIM 2>
```

Now: 1us Delta: 0 /MIPS_top_module_TB/data1 0 ps to 482906 ps Keep 0