

ISE Project Navigator (P.20131013) - C:\Xilinx\codes\finalProject\finalProject.xise - [instruction_memory (RTL1)]

File Edit View Project Source Process Tools Window Layout Help

Design

View: Implementation Simulation

Hierarchy

- finalProject
 - xc6slx9-3tqg144
 - MIPS_top_module (MIPS_top_modu
 - pc - PC (PC.v)
 - add_1 - incrementer (incremente
 - Add - add (add.v)
 - mux1 - mux_2to1 (mux_2to1.v)
 - ins_mem - instruction_memo
 - sign_ext - sign_extend (sign_exte
 - alu_control - ALU_control (ALU_
 - mux2 - mux_2to1 (mux_2to1.v)
 - registers - register_bank (register

No Processes Running

Processes: ins_mem - instruction_memory

- Design Summary/Reports
- Design Utilities
- User Constraints
- Synthesize - XST
 - View RTL Schematic
 - View Technology Schematic
 - Check Syntax
 - Generate Post-Synthesis Simulati...
- Implement Design
 - Generate Programming File
 - Configure Target Device
 - Analyze Design Using ChipScope

Libraries

Source Libraries

- work

View by Category

Design Objects of Top Level Block

Instances	Pins	Signals	Name	Value
instruction_memory				

Properties: (No Selection)

Console Errors Warnings Find in Files Results View by Category

[192,328]

instruction_memory

address(4:0)

clk

instruction(31:0)

instruction_memory

MIPS_top_module.v MIPS_top_module_TB.v instruction_memory.v instruction_memory (RTL1)

ENG

12:51 PM

7/24/2020

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Processes: ins_mem - instruction_memory

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View by Category

Design Objects of Top Level Block

Instances

- instruction_memory
- XST_GND

Pins

- instruction_memory

Signals

- instruction_memory

Properties of Instance: XST_GND

Name	Value
Type	gnd
Instance Name	XST_GND

Console Errors Warnings Find in Files Results View by Category

[256,280]

instruction_memory:1

address(4:0)

clk

gnd_G

XST_GND

instruction(3

MIPS_top_module.v MIPS_top_module_TB.v instruction_memory.v instruction_memory (RTL1)

ENG 12:51 PM 7/24/2020