ModelSim SE-64 10.6d

File Edit View Compile Simulate Add Wave Tools Layout Bookmarks Window Help

Wave - Default

| Msgs | |
|---|---|
| /add_TB/in1 | 32'd5 |
| /add_TB/in2 | 32'd100 |
| /add_TB/out | 32'd105 |
| /glbl/GSR | 1'h0 |

Now 1000 ns

Cursor 1 999.27 ns

Transcript

```
# Errors: 0, Warnings: 0
# Model Technology ModelSim SE-64 vlog 10.6d Compiler 2018.02 Feb 24 2018
# Start time: 12:42:04 on Jul 23,2020
# vlog -reportprogress 300 C:/Xilinx/14.7/ISE_DS/ISE/verilog/src/glbl.v
# -- Compiling module glbl
#
# Top level modules:
#       glbl
# End time: 12:42:04 on Jul 23,2020, Elapsed time: 0:00:00
# Errors: 0, Warnings: 0
# vsim -voptargs=""+acc"" -t 1ps -L xilinxcorelib_ver -L unisims_ver -L unimacro_ver -L secureip -lib work work.add_TB glbl
# Start time: 12:42:05 on Jul 23,2020
# ** Note: (vsim-3812) Design is being optimized...
# Loading work.add_TB(fast)
# Loading work.add(fast)
# Loading work.glbl(fast)
# .main_pane.wave.interior.cs.body.pw.wf
# .main_pane.structure.interior.cs.body.struct
# .main_pane.objects.interior.cs.body.tree

VSIM 2>
```

Now: 1 us Delta: 0 | sim:/add_TB | 0 ps to 525 ns | Keep 0

ENG

12:42 PM
7/23/2020