

ISE Project Navigator (P.20131013) - C:\Xilinx\codes\finalProject\finalProject.xise - [mux_2to1 (RTL2)]

File Edit View Project Source Process Tools Window Layout Help

Design

View: Implementation Simulation

Hierarchy

- finalProject
 - xc6slx9-3tqg144
 - MIPS_top_module (MIPS_top_modu
 - pc - PC (PC.v)
 - add_1 - incrementer (incremente
 - Add - add (add.v)
 - mux1 - mux_2to1 (mux_2to1
 - ins_mem - instruction_memory
 - sign_extend - sign_extend (sign
 - alu_control - ALU_control (ALU
 - mux2 - mux_2to1 (mux_2to1

No Processes Running

Processes: mux1 - mux_2to1

- Design Summary/Reports
- Design Utilities
- User Constraints
- Synthesize - XST
 - View RTL Schematic
 - View Technology Schematic
 - Check Syntax
 - Generate Post-Synthesis Simul...
- Implement Design
 - Generate Programming File
 - Configure Target Device

Libraries

Source Libraries

- work

View by Category

Design Objects of Top Level Block

Instances

- mux_2to1

Pins

Signals

Name

Value

Properties: (No Selection)

mux_2to1

in1(31:0)

in2(31:0)

select

out(31:0)

mux_2to1

mux_2to1.v mux_2to1_TB.v mux_2to1 (RTL2)

Console Errors Warnings Find in Files Results View by Category

[0,0]

ENG

9:27 PM

7/18/2020

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No Processes Running

Processes: mux1 - mux_2to1

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Libraries

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mux_2to1.v mux_2to1_TB.v mux_2to1 (RTL2)

Design Objects of Top Level Block

Instances

- mux_2to1
- Mmux_out1

Pins

- mux_2to1

Signals

- mux_2to1

Properties of Instance: Mmux_out1

Name	Value
Type	Mmux_out1
Instance Name	Mmux_out1

Console Errors Warnings Find in Files Results View by Category

[1040,136]

mux_2to1:1

Mmux_out1

in2(31:0) Data0(31:0) Result(31:0) out(31:0)

in1(31:0) Data1(31:0)

select Sel(0)

Mmux_out1

mux_2to1

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