

ModelSim SE-64 10.6d

File Edit View Compile Simulate Add Wave Tools Layout Bookmarks Window Help

100 ps

Layout Simulate ColumnLayout Default

Search:

sim - Default

Instance

- mux\_2to1\_TB
  - uit
  - gbl
    - #ASSIGN#16
    - #ASSIGN#48
    - #ASSIGN#49
    - #ASSIGN#50
    - #vsim\_capacity#

Wave - Default

Signal	Value
/mux_2to1_TB/in1	32'h00000002
/mux_2to1_TB/in2	32'h0000000a5
/mux_2to1_TB/select	1'h1
/mux_2to1_TB/out	32'h00000002
/gbl/GSR	1'h0

Now: 1000 ns  
Cursor 1: 999.145 ns

Transcript

```
# Errors: 0, Warnings: 0
# Model Technology ModelSim SE-64 vlog 10.6d Compiler 2018.02 Feb 24 2018
# Start time: 21:01:58 on Jul 18, 2020
# vlog -reportprogress 300 C:/Xilinx/14.7/ISE_DS/ISE/verilog/src/glbl.v
# -- Compiling module glbl
#
# Top level modules:
# glbl
# End time: 21:01:58 on Jul 18, 2020, Elapsed time: 0:00:00
# Errors: 0, Warnings: 0
# vsim -voptargs="" -t lps -L xilinxcorelib_ver -L unisims_ver -L unimacro_ver -L secureip -lib work work.mux_2to1_TB glbl
# Start time: 21:01:58 on Jul 18, 2020
# ** Note: (vsim-3812) Design is being optimized...
# Loading work.mux_2to1_TB(fast)
# Loading work.mux_2to1_TB(fast)
# Loading work.glbl(fast)
# .main_pane.wave.interior.cs.body.pw.wf
# .main_pane.structure.interior.cs.body.struct
# .main_pane.objects.interior.cs.body.tree
VSIM 2>
```

Now: 1us Delta: 0 sim:/mux\_2to1\_TB 0 ps to 262500 ps Keep 0