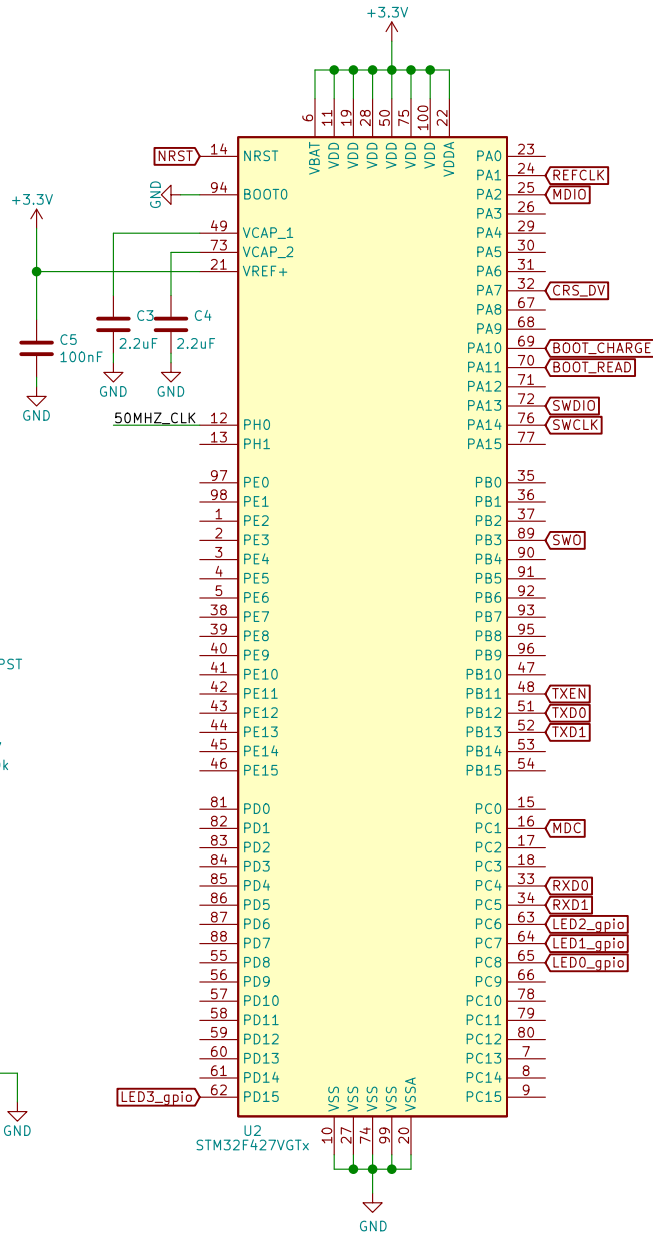
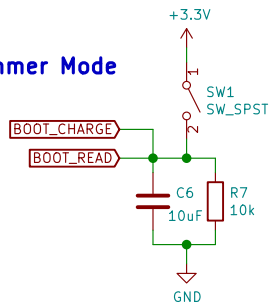


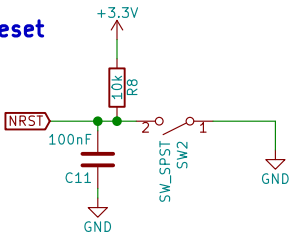
Micro



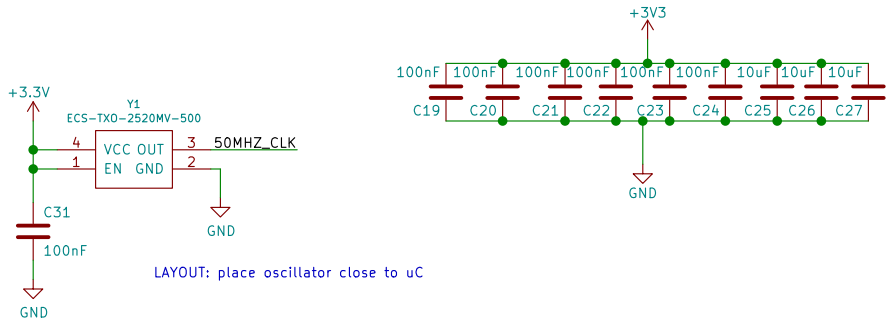
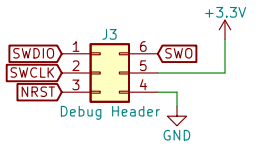
Programmer Mode



MCU Reset

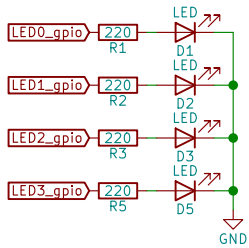


Programming Header



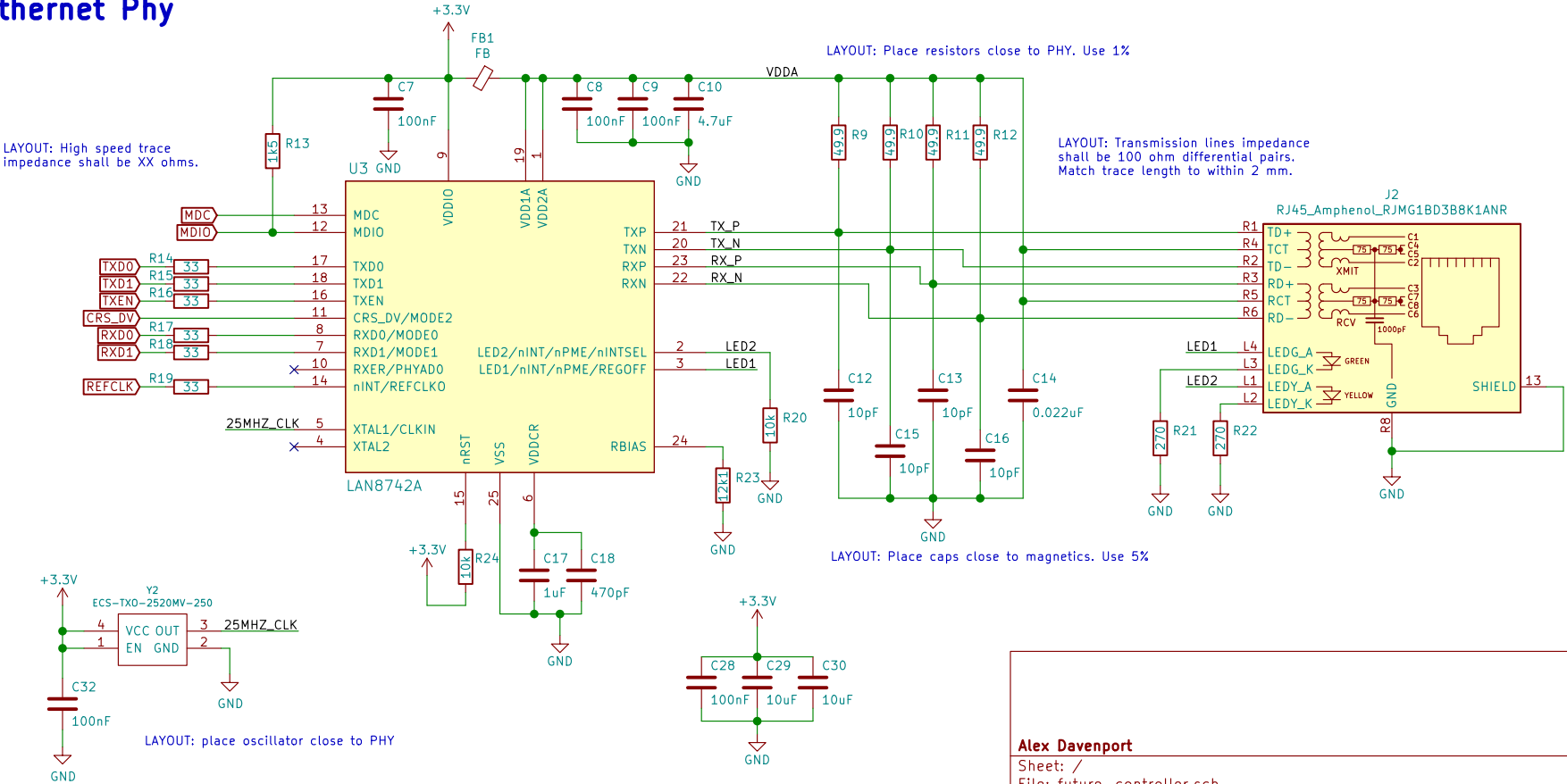
LAYOUT: place oscillator close to uC

LEDs



TODO:
Impedance matching of transmission lines
rs-422?
strapping configuration
footprint scrub

Ethernet Phy



LAYOUT: High speed trace impedance shall be XX ohms.

LAYOUT: Place resistors close to PHY. Use 1%

LAYOUT: Transmission lines impedance shall be 100 ohm differential pairs. Match trace length to within 2 mm.

LAYOUT: Place caps close to magnetics. Use 5%

Alex Davenport

Sheet: /
File: future-controller.sch

Title: Ethernet Test Board

Size: B Date: 2021-07-18

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Rev: A

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