

J17			
1	GP182_PWM2	PWM_2	GPIO capable of PWMoutput
2	- NC -		No connect.
3	- NC -		No connect.
4	DCIN		7to 15V. Input
5	GP135	UART_2_TXGPIO,	UART2 transmit (output)
6	RCVR_MODE		Firmware recovery mode
7	GP27	I2C_6_SCL	GPIO, I2C6 clock (open collector when configured for I2C)
8	GP20	I2C_1_SDA	GPIO, I2C1 data (open collector when configured for I2C)
9	GP28	I2C_6_SDA	GPIO, I2C6 data (open collector when configured for I2C)
10	GP111	SPI_2_FS1	GPIO, SPI2 chip select 1 (output)
11	GP109	SPI_2_CLK	GPIO, SPI2 clock output
12	GP115	SPI_2_TXD	GPIO, SPI2 transmit data (output)
13	OSC_CLK_OUT_0		19.2 MHz High speed clock output
14	GP128	UART_1_CTS	GPIO, UART1 clear to send (input)

J19			
1	- NC -		No connect.
2	V_1P80		System 1.8V I/O output power.
3	GND		Ground.
4	GP44		GPIO
5	GP46		GPIO
6	GP48		GPIO
7	RESETOUT#		System resetout low.
8	GP131	UART1_TX	GPIO, UART1 transmit (output)
9	GP14		GPIO
10	GP40	I2S_2_CLK	GPIO, I2S2 clock (output)
11	GP43	I2S_2_TXD	GPIO, I2S2 transmit data (output)
12	GP77	SD_0_CD#	GPIO, SD card detect input (active low)
13	GP82	SD_0_DAT2	GPIO, SD data 2
14	GP83	SD_0_DAT3	GPIO, SD data

J18			
1	GP13_PWM1	PWM_1	GPIO, capable of PWMoutput
2	GP165		GPIO
3	PWRBTN#		Power/sleep button input (active low)
4	MSIC_SLP_CLK3		32kHz sleep clock output
5	V_VBAT_BKUP		Real-time clock (RTC) backup battery input
6	GP19	I2C_1_SCL	GPIO, I2C1 clock (open collector when configured for I2C)
7	GP12_PWM0	PWM_0	GPIO, capable of PWMoutput
8	GP183_PWM3	PWM_3	GPIO, capable of PWMoutput
9	- NC -		
10	GP110	SPI_2_FS0	GPIO, SPI2 chip select 0 (output)
11	GP114	SPI_2_RXD	GPIO, SPI2 receive data (input)
12	GP129	UART_1_RTS	GPIO, UART1 ready to send (output)
13	GP130	UART_1_RX	GPIO, UART1 receive data (input)
14	FW_RCVR		Firmware recovery, active high on boot.

J20			
1	V_SYS		System input power.
2	V_3P30		System 3.3V output.
3	GP134	UART_2_RX	GPIO, UART2 receive (input)
4	GP45		GPIO
5	GP47		GPIO
6	GP49		GPIO
7	GP15		GPIO.
8	GP84	SD_0_CLK_FB	GPIO, SD clock feedback
9	GP42	I2S_2_RXD	GPIO, I2S2 receive data (input)
10	GP41	I2S_2_FS	GPIO, I2S2 frame sync (output)
11	GP78	SD_0_CLK	GPIO, SD clock output
12	GP79	SD_0_CMD	GPIO, SD command
13	GP80	SD_0_DAT0	GPIO, SD data 0
14	GP81	SD_0_DAT1	GPIO, SD data 1



<http://artifactnoise.com/>
<http://nonnoise.github.io/Edison/>