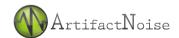
J17			
1	GP182_PWM2		GPIO capable of PWMoutput
2	- NC -		No connect.
3	- NC -		No connect.
4	VIN		7to 15V.
5	GP135	UART2_TX	GPIO, UART2 transmit output.
6	RCVR_MODE		Firmware recovery mode.
7	GP27	I2C6_SCL	GPIO,IC26 SCL output open collector.
8	GP20	I2C1_SDA	GPIO, I2C1 data open collector.
9	GP28	I2C6_SDA	GPIO, I2C6 data open collector.
10	GP111	SSP5_FS1	GPIO, SSP2 chip select 2 output.
11	GP109	SSP5_CLK	GPIO, SSP5 clock output.
12	GP115	SSP5_TXD	GPIO, UART1 clear to send input.
13	OSC_CLK_OUT_0		High speed clock output.
14	GP128	UART1_CTS	GPIO, UART1 clear to send input.

J19			
1	- NC -		No connect.
2	V_1P80		System 1.8V I/O output power.
3	GND		Ground.
4	GP44	ALS_INT_N	GPIO, ALS interrupt input.
5	GP46	ACCEL_INT_1	GPIO, accelerometer interrupt input.
6	GP48	GYRO_DRDY	GPIO, gyro data ready input
7	RESETOUT#		System resetout low.
8	GP131	UART1_TX	GPIO, UART 1 Tx output.
9	GP14	AUDIOCODEC_INT	GPIO, audio codec interrupt input.
10	GP40	SSP2_CLK	GPIO, SSP2 clock output.
11	GP43	SSP2_TXD	GPIO, SSP2 transmit data output.
12	GP77	SD_CDN	GPIO, SD card detect low input.
13	GP82	SD_DAT2	GPIO, SD data 2
14	GP83	SD_DAT3	GPIO, SD data3

J18			
1	GP13_PWM1		GPIO capable of PWMoutput.
2	GP165		GPIO
3	GPI_PWRBTN_N		Power button input.
4	MSIC_SLPCLK_2		32kHz sleep clock.
5	V_VBAT_BKUP		RTC backup battery input.
6	GP19	I2C1_SCL	GPIO,IC21 SCL output open collector.
7	GP12_PWM0		GPIO capable of PWMoutput.
8	GP183_PWM3		GPIO capable of PWMoutput.
9	- NC -		No connect.
10	GP110	SSP5_FS0	GPIO, SSP1 chip select 2 output.
11	GP114	SSP5_RXD	GPIO, SSP5 receive data input.
12	GP129	UART1_RTS	GPIO, UART1 ready to send output
13	GP130	UART1_RX	GPIO, UART1 receive data input.
14	FW_RCVR		Firmware recovery, active high on boot.

J20			
1	V_SYS		System input power.
2	V_3P30		System 3.3V output.
3	GP134	UART2_RX	UART2 Rx (input).
4	GP45	COMPASS_DRDY	GPIO, compass data ready input.
5	GP47	ACCEL_INT_2	GPIO, accelerometer interrupt input 2.
6	GP49	GYRO_INT	GPIO, gyro interrupt input.
7	GP15		GPIO.
8	GP84	SD_CLK_FB	GPIO, SD clock feedback input.
9	GP42	SSP2_RXD	GPIO, SSP2 Rx data input.
10	GP41	SSP2_FS	GPIO, SSP2 frame sync output.
11	GP78	SD_CLK	GPIO, SD clock output.
12	GP79	SD_CMD	GPIO, SD command
13	GP80	SD_DAT0	GPIO, SD data 0.
14	GP81	SD_DAT1	GP81 SD data 1.



http://artifactnoise.com/ http://nonnoise.github.io/Edison/