History of Computers

Charles Babbage - invented the Analytical engine which is the design for modern computers.

Ada Lovelace - Although programining was slightly different, she is known to be the first person to program, using the Analytical engine algorithms.

Differences in the generalities

1. Vacaum Tube 2. Transistors 3. Integrated Circuit

4. Very large Scale Integration 5. System on Chip

1. Vacuum Tube - Massive, Lots of power, heavy, low aremony

2. Transistors - Smaller, less power, lighter eg (T.Vs, radios)

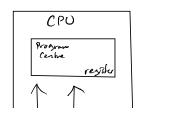
3. ICC - Smaller, fasher Cheeper.
Many transities integrated on muzicalips.

- More than one person com use at a theo.

4. VLSI - Partability

3. SOC - AU components of a computer on a single chip eg, modern CPO.

Von Neumann Model

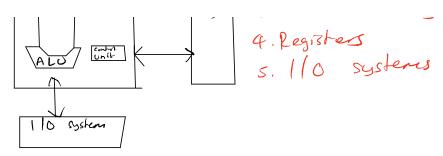


Main Mem 5 main components

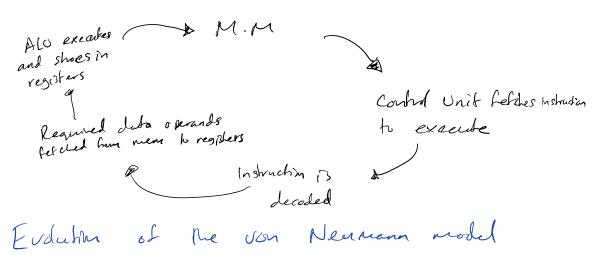
1. Conhol Unit

2. Arithmetre logic Unit

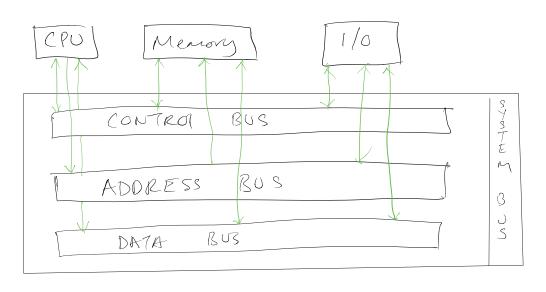
3. Man Meniory (M,M)



Datapalle von Neumann bottleneck is a single datapath between the CPU and the M.M.



Bus system. Electrically conducting path along which duty is hanstritted through devices.



- A computer bus consists of parallel conductors which may be were on probable circuit board.
- Each vive courses I bit of date. Depending on how many wives , will define how many bit device.

Data bus - Transfers data to different components of a computer. Depending of how arrang trag will define the sped.

they are Bi-directional-CPU can read and write to mening using these lines.

Address bus - Address larg is unidirectional.

Components are connected through buses.

Each component has a conque (D known as the address. Address has can be used to communicate with different components

Control bus - Transants different communicates

between different components.

eg 1. Type of operations 2. Time for which a device com use data and address long

Hardware Components

Check presentation

Seni conductors - Silvan, is Chent, can surtch between insulcher and conductor un externel Confids. Abstraction

The representations of essential information by not visualizing background info. eg, programming without knowledge of electricity flow.

Lower level into 3 holder from higher level.
Avoid 5 cluthering

Vacanum culas used to get dedroyed, were not as vetable as hansishers.

SISD - Simple inhor on simple date,

Graphies defend on the cast aways of proofs and lette.

Richards Units - predates the order of execution.

Quantum computers - Com be in more than one

Modance at a given time