

Pin Map



Legend

■ VCC	■ K0
■ GROUND	■ K1
■ INPUT	■ SIGNATURE
■ OUTPUT	

Foot Configuration

No.	Pin	Signal Type	Data Type
7-14	PA 0 - 7	Digital	Input A
27-34	PB 0 - 7	Digital	Input B
17	PC 0	Digital	Enable
18	PC 1	Digital	Clock
19	PC 2	Digital	Reset
20	PC 3	Digital	Reff
21-24	PC 4-7	Digital	OPCODE
37-40 and 1-4	PD 0 - 7	Digital	Output

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Production
SAMiCALNA Fabric
 Type
Arithmetic Logic Unit
 Family
C4B3R4W1T
 Package
TQFP/MLF
 Version
1.0

Power Information

Foot	Pin	Data Size	Voltage	I	Freq
7	PA0	1 bit	3.3 Volt	.5 A	1 MHz
8	PA1	1 bit	3.3 Volt	.5 A	1 MHz
9	PA2	1 bit	3.3 Volt	.5 A	1 MHz
10	PA3	1 bit	3.3 Volt	.5 A	1 MHz
11	PA4	1 bit	3.3 Volt	.5 A	1 MHz
12	PA5	1 bit	3.3 Volt	.5 A	1 MHz
13	PA6	1 bit	3.3 Volt	.5 A	1 MHz
14	PA7	1 bit	3.3 Volt	.5 A	1 MHz
27	PB0	1 bit	3.3 Volt	.5 A	1 MHz
28	PB1	1 bit	3.3 Volt	.5 A	1 MHz
29	PB2	1 bit	3.3 Volt	.5 A	1 MHz
30	PB3	1 bit	3.3 Volt	.5 A	1 MHz
31	PB4	1 bit	3.3 Volt	.5 A	1 MHz
32	PB5	1 bit	3.3 Volt	.5 A	1 MHz
33	PB6	1 bit	3.3 Volt	.5 A	1 MHz
34	PB7	1 bit	3.3 Volt	.5 A	1 MHz
17	PC0	1 bit	3.3 Volt	.5 A	1 MHz
18	PC1	1 bit	3.3 Volt	.5 A	1 MHz
19	PC2	1 bit	3.3 Volt	.5 A	1 MHz
20	PC3	1 bit	3.3 Volt	.5 A	1 MHz
21	PC4	1 bit	3.3 Volt	.5 A	1 MHz
22	PC5	1 bit	3.3 Volt	.5 A	1 MHz
23	PC6	1 bit	3.3 Volt	.5 A	1 MHz
24	PC7	1 bit	3.3 Volt	.5 A	1 MHz
37	PD0	1 bit	3.3 Volt	.5 A	1 MHz
38	PD1	1 bit	3.3 Volt	.5 A	1 MHz
39	PD2	1 bit	3.3 Volt	.5 A	1 MHz
40	PD3	1 bit	3.3 Volt	.5 A	1 MHz
1	PD4	1 bit	3.3 Volt	.5 A	1 MHz
2	PD5	1 bit	3.3 Volt	.5 A	1 MHz
3	PD6	1 bit	3.3 Volt	.5 A	1 MHz
4	PD7	1 bit	3.3 Volt	.5 A	1 MHz