Z-Bus

Lect_4

Zbus

- A power system network can be converted into an equivalent impedance diagram.
- This diagram forms the basis of power flow (or load flow) studies and short circuit analysis.
- Formation of bus impedance matrix (also known as Z_{bus} matrix).

$$Z_{bus} = Y_{bus}^{-1}$$

Elements Of The Bus Impedance And Admittance Matrices

- Bus impedance and admittance matrices are inverses of each other.
- Since Y_{bus} is a symmetric matrix, Z_{bus} is also a symmetric matrix.
- ▶ Voltage-current relations are given in terms of the Y_{bus} matrix for a 4 bus system

$$\begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} Y_{11} & Y_{12} & Y_{13} & Y_{14} \\ Y_{21} & Y_{22} & Y_{23} & Y_{24} \\ Y_{31} & Y_{32} & Y_{33} & Y_{34} \\ Y_{41} & Y_{42} & Y_{43} & Y_{44} \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix}$$

 Y_{11} is the admittance measured at bus-1 when buses 2, 3 and 4 are short circuited. The admittance Y_{11} is defined as the **self admittance** at bus-1

$$Y_{11} = \frac{I_1}{V_1} \bigg|_{V_2 = V_3 = V_4 = 0}$$

▶ The off diagonal elements are denoted as the **mutual admittances** . The mutual admittance between buses 1 and 2 is defined as Y_{12}

$$Y_{12} = \frac{I_1}{V_2} \Big|_{V_1 = V_3 = V_4 = 0}$$

▶ The mutual admittance Y₁₂ is obtained as the ratio of the current injected in bus-1 to the voltage of bus-2 when buses 1, 3 and 4 are short circuited.

$$V = IZ$$

$$Z = Y_{bus}$$

$$Z = Y_{bus}$$

$$V_{u} + Z_{u}$$

$$Y_{u} + Z_{u}$$

$$Y_{u} + Z_{u}$$

$$Z_{11} = \frac{V_1}{I_1} \Big|_{I_2 = I_3 = I_4 = 0}$$

$$Z_{12} = \frac{V_1}{I_2}$$
 $I_1 = I_3 = I_{4=0}$

ightharpoonup The voltage-current relation can be written in terms of the Z_{bus} matrix as

$$\begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix} = \begin{bmatrix} Z_{11} & Z_{12} & Z_{13} & Z_{14} \\ Z_{21} & Z_{22} & Z_{23} & Z_{24} \\ Z_{31} & Z_{32} & Z_{33} & Z_{34} \\ Z_{41} & Z_{42} & Z_{43} & Z_{44} \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix}$$

The driving point impedance at bus-1 is then defined as

$$Z_{11} = \frac{V_1}{I_1} \bigg|_{I_2 = I_3 = I_4 = 0}$$

▶ The transfer impedance between buses 1 and 2 can be obtained by injecting a current at bus-2 while open-circuiting buses 1, 3 and 4 as

$$Z_{12} = \frac{V_1}{I_2} \bigg|_{I_1 = I_3 = I_4 = 0}$$

▶ Z_{11} is not the reciprocal of Y_{11} , Z_{12} is not the reciprocal of Y_{12} .

Modification of Bus Impedance Matrix

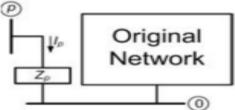
- Four possible cases by which an existing bus impedance matrix can be modified.
- Voltage-current relations in terms of the bus impedance matrix for an n-bus system are given as

$$\begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \end{bmatrix} = Z_{orig} \begin{bmatrix} I_1 \\ I_2 \\ I_3 \\ I_4 \end{bmatrix}$$

▶ The aim is to modify the matrix Z_{orig} when a new bus or line is connected to the power system.

Adding a New Bus to the Reference Bus

▶ It is assumed that a new bus p (p > n) is added to the reference bus through an impedance Z_p.

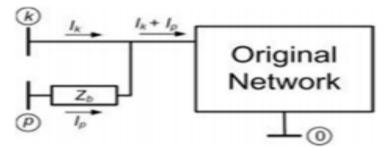


▶ This bus is only connected to the reference bus, the voltagecurrent relations the new system are

$$\begin{bmatrix} V_1 \\ \vdots \\ V_n \\ V_p \end{bmatrix} = \begin{bmatrix} & & & & 0 \\ & Z_{orig} & & \vdots \\ & & & 0 \\ 0 & \dots & 0 & Z_p \end{bmatrix} \begin{bmatrix} I_1 \\ \vdots \\ I_n \\ I_p \end{bmatrix} = Z_{new} \begin{bmatrix} I_1 \\ \vdots \\ I_n \\ I_p \end{bmatrix}$$

Adding a New Bus to an Existing Bus through an Impedance

- A bus, which has not been a part of the original network, is added to an existing bus through a transmission line with an impedance of Z_b.
- Let us assume that p (p > n) is the new bus that is connected to bus k (k < n) through Z_b .



Note from this figure that the current I_p flowing from bus p will alter the voltage of the bus k.

Adding a New Bus to an Existing Bus through an Impedance

that the current Ip flowing from bus p will alter the voltage of the bus k.

$$V_k = Z_{k1}I_1 + Z_{k2}I_2 + \dots + Z_{kk}(I_k + I_p) + \dots + Z_{kn}I_n$$

In a similar way the current Ip will also alter the voltages of all the other buses as

$$V_i = Z_{i1}I_1 + Z_{i2}I_2 + \dots + Z_{ik}(I_k + I_p) + \dots + Z_{in}I_n \quad i \neq k$$

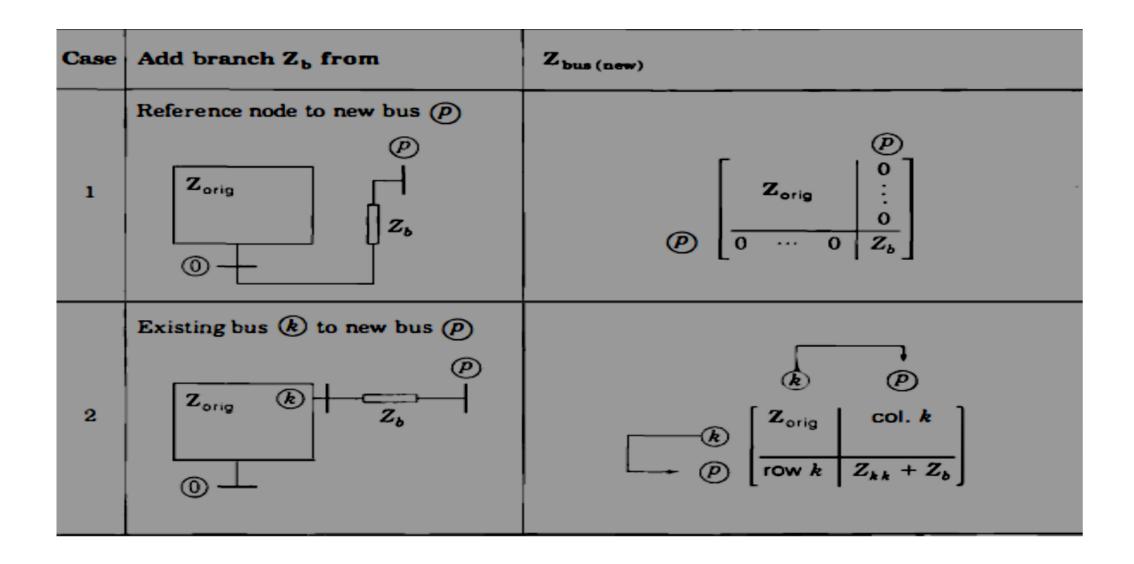
▶ Furthermore the voltage of the bus p is given by

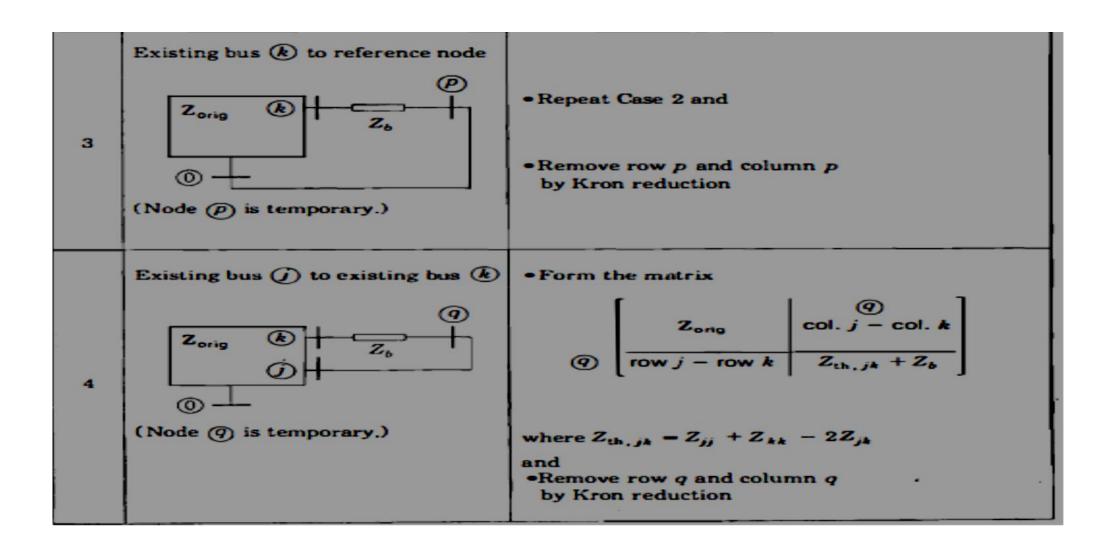
$$V_p = V_k + Z_b I_p$$

$$V_p = Z_{k1} I_1 + Z_{k2} I_2 + \dots + Z_{kk} I_k + \dots + Z_{kn} I_n + (Z_{kk} + Z_b) I_p$$

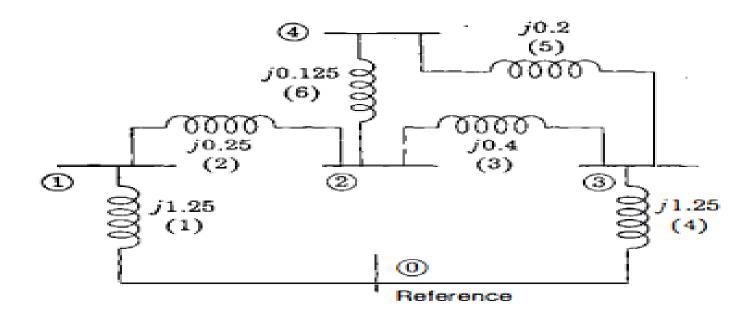
▶ Therefore the new voltage current relations are

$$\begin{bmatrix} V_1 \\ \vdots \\ V_n \\ V_p \end{bmatrix} = \begin{bmatrix} & Z_{orig} & Z_{k1} \\ & Z_{orig} & \vdots \\ & Z_{kn} & Z_{kn} \\ & & Z_{kn} & Z_{kk} + Z_b \end{bmatrix} \begin{bmatrix} I_1 \\ \vdots \\ I_n \\ I_p \end{bmatrix} = Z_{new} \begin{bmatrix} I_1 \\ \vdots \\ I_n \\ I_p \end{bmatrix}$$





Example 8.4. Determine Z_{bus} for the network shown in Fig. 8.8, where the impedances labeled 1 through 6 are shown in per unit. Preserve all buses.



Z1= (.2 new Kron Reduction.

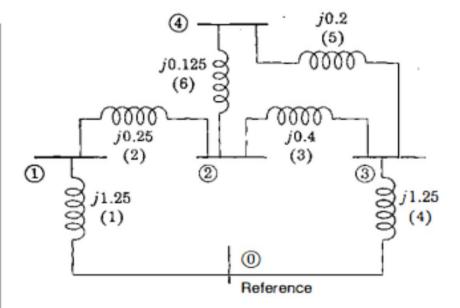
1 to 2 [add new bus] to 3 [Add new bus]. 3 to 0' [Reference bus]. 3 to 4 [Add new bus]. 4 to 2 [Add bos tope oxisting bus)

We then have the 1×1 bus impedance matrix

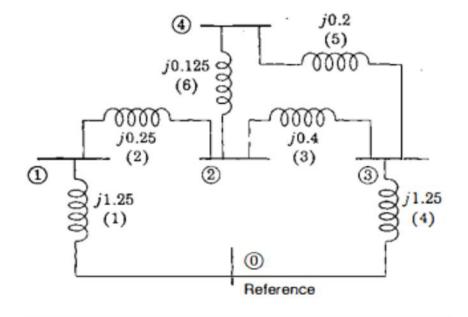
$$\mathbf{Z}_{\mathsf{bus},\,1} = \bigcirc [j1.25]$$

To establish bus 2 with its impedance to bus 1, we follow Eq. (8.32) to write

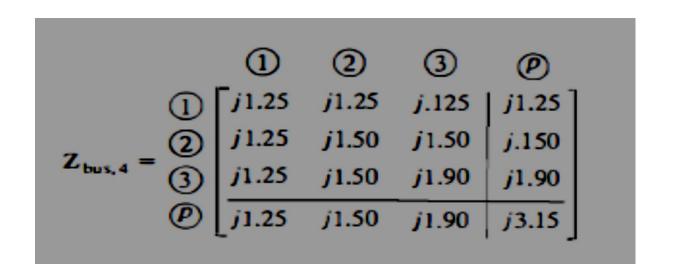
$$\mathbf{Z}_{\text{bus},2} = \begin{array}{c} \textcircled{1} & \textcircled{2} \\ \textcircled{2} \begin{bmatrix} j1.25 & j1.25 \\ j1.25 & j1.50 \end{bmatrix}$$

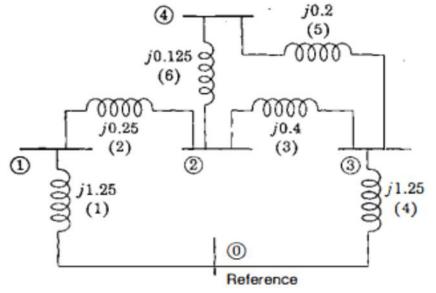


Bus (3) with the impedance connecting it to bus (2) is established by writing



Existing bus to reference bus





We now eliminate row p and column p by Kron reduction. Some of the elements of the new matrix from Eq. (8.33) are

$$Z_{11(\text{new})} = j1.25 - \frac{(j1.25)(j1.25)}{j3.15} = j0.75397$$

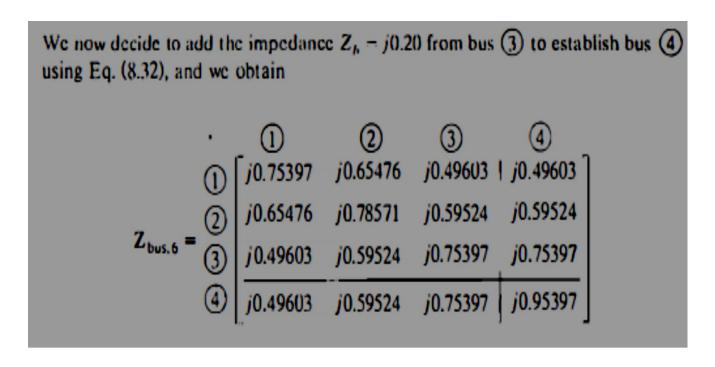
$$Z_{22(\text{new})} = j1.50 - \frac{(j1.50)(j1.50)}{j3.15} = j0.78571$$

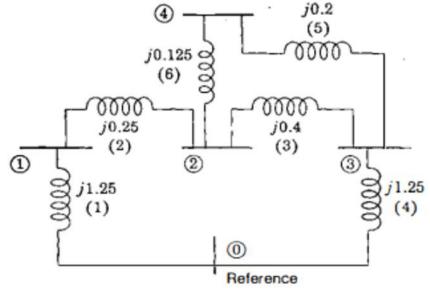
$$Z_{23(\text{new})} = Z_{32(\text{new})} = j1.50 - \frac{(j1.50)(j1.90)}{j3.15} = j0.59524$$

When all the elements are determined, we have

$$\mathbf{Z}_{\text{bus},5} = \begin{bmatrix} \mathbf{j}0.75397 & j0.65476 & j0.49603 \\ j0.65476 & j0.78571 & j0.59524 \\ j0.49603 & j0.59524 & j0.75397 \end{bmatrix}$$

To new bus 4





Bus 4 to existing bus 2

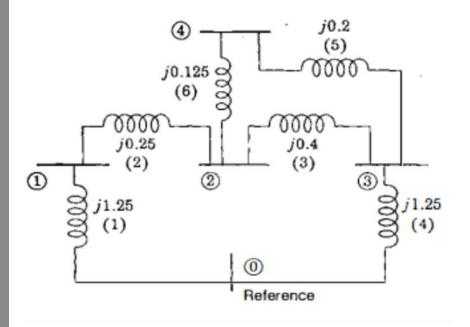
Finally, we add the impedance $Z_b = j0.125$ between buses 2 and 4. If we let j and k in Eq. (8.41) equal 2 and 4, respectively, we obtain the elements for row 5 and column 5

$$Z_{15} = Z_{12} - Z_{14} = j0.65476 - j0.49603 = j0.15873$$

 $Z_{25} = Z_{22} - Z_{24} = j0.78571 - j0.59524 = j0.19047$
 $Z_{35} = Z_{32} - Z_{34} = j0.59524 - j0.75397 = -j0.15873$
 $Z_{45} = Z_{42} - Z_{44} = j0.59524 - j0.95397 = -j0.35873$

and from Eq. (8.42)

$$Z_{55} = Z_{22} + Z_{44} - 2Z_{24} + Z_{b}$$
$$= j\{0.78571 + 0.95397 - 2(0.59524)\} + j0.125 = j0.67421$$



So, employing $Z_{bus,6}$ previously found, we write the 5 \times 5 matrix

$$Z_{\text{bus, 6}} = \begin{bmatrix} g \\ j0.15873 \\ j0.19047 \\ -j0.15873 \\ -j0.35873 \\ \hline j0.15873 \quad j0.19047 \quad -j0.15873 \quad -j0.35873 \end{bmatrix}$$

and from Eq. (8.43) we find by Kron reduction

$$\mathbf{Z}_{\text{bus}} = \begin{bmatrix} 1 & \boxed{2} & \boxed{3} & \boxed{4} \\ j0.71660 & j0.60992 & j0.53340 & j0.58049 \\ j0.60992 & j0.73190 & j0.64008 & j0.69659 \\ j0.53340 & j0.64008 & j0.71660 & j0.66951 \\ \boxed{4} & j0.58049 & j0.69659 & j0.66951 & j0.76310 \end{bmatrix}$$