

Laboratory Plan Digital Systems

Laboratories	Experiment Name	CLO
1	Introduction to lab equipment Unitrain and Implementation of basic Functions	1
2	To understand laws of Boolean Algebra	1
3	To understand De Morgan's Law and EX-OR, EX-NOR operation	1
4	To understand minimizing logical circuit techniques	1
5	To Understand Combinational Circuit and Applications	2
6	Design combinational circuit of adder using HDL language	2
7	Design Combinational Circuits using Decoders	2
8	Designing and investigation of Multiplexers / Demultiplexers	2
9	Introduction to sequential circuit and working of RS flip-flop	3
10	Understanding of JK and flip flops and designing of counter circuits	3
11	Designing of up counter and down counters	3
12	To Understand Operation of Register and Shift Register	3
13	Design of sequential parity check circuit in Verilog	3
14	Practical Implementation of a Digital System	4
15	Practical Implementation of a Digital System	4