

Digital Logic Design

Problem Set #6

Due Date: 1400/9/4



1. Draw and simulate the following circuit Using circuitverse.org tool. Remember to perform as many tests as necessary to validate the behaviour of your circuit.
 - a. Problem 2 in problem set #5

Note 1: You should provide a separate URL to your projects for each circuit.

Note 2: You should provide a screenshot for each circuit.

Note 3: You're not supposed to change details of your projects after submitting the answer

Note 4: This project must be done individually; thus, in case of any similarities between the circuits provided by the students, all of those will receive a "-200".

Good Luck!

Mosayebi

*Do not hesitate to ask your question
Via eeXBee@gmail.com*