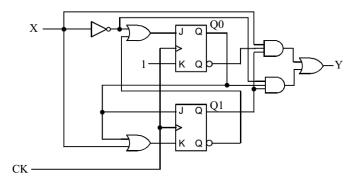
Digital Logic Design

Problem Set #9

Due Date: 1400/9/30



- 1- Given the synchronous circuit below: (50 points)
 - a) Find the state diagram
 - b) Find the state and output equations



- 2- Given the synchronous circuit below: (50 points)
 - a) Find the state diagram
 - b) Find the state and output equations

