SMZ

#### ZANZIBAR EXAMINATIONS COUNCIL

#### FORM THREE ENTRANCE EXAMINATION

051 RADIO AND TV SERVICING

TIME 2:30 HOURS

FRIDAY 1<sup>ST</sup> DECEMBER, 2017 pm

#### **INSTRUCTIONS TO CANDIDATES**

- This paper consists of THREE sections A, B and C.
- 2. Answer ALL questions in sections A and B and any three (3) questions in section C.
- 3. All answers must be written in the space provided.
- 4. Write your examination number on every page of this booklet.
- 5. Calculators and cellular phones are not allowed in the examination room.
- 6. Use a blue or black pen in writing. The diagrams must be drawn in a pencil.

		FOR EXAMINE	R'S USE ONLY	,	
Question number	Marks	Signature	Question number	Marks	Signature
1			9		
2			10		
3			11		
4			12		
5			13		
6			14		
7			15		
8					
		•	•		
TOTAL					

This paper consists of 15 printed pages.

# SECTION A: (10 Marks)

#### **Answer ALL questions in this section**

1.		e items (i) – (x) choose t n the table provided at th			nswer and write its letter below the s question.
	A 50Ω	-	a ca	pacitor.	otential difference of 100V.  After 20s time constant then total rly.
	A.	1.5J	В.	0J	
	C.	0.753	D.	15x10	-3J
	ii) Digital ir	nstruments are those whi	ch:		
	A	Have numerical readou	t.		B. Use LED or LCD display
	C	Have a circuitry of digit	al de	sign.	D. Use deflection type meter
	in	strument			
	iii) Multime	ter is used to measure			
	,	A. Resistance	В	. Curre	ent
	(	C. Voltage	D	. All of	the above.
	iv) A capaci	tor that store charge of 0	).5C a	at 10V l	nas a capacitance of:
	A.	5 Farad		В.	20 Farad
	C.	10 Farad		D.	0.05 Farad
	v) Which of	the following is a unit of	charg	je?	
	A	A/s	В.	As	
	C.	C/s	D.	V/A	

- vi) A current of 500mA is set up in a conductor for 20s. The charge which entered conductor was
  - A.  $\frac{1}{25}$ C

B. 25C

C. 10C

- D. 40C
- vii) A transformer is a device for
  - A. Changing alternating voltage
  - B. Changing direct voltage
  - C. Changing ac to dc
  - D. Increasing electrical energy.
- Viii) Donor type semiconductor is formed by adding impurity of valence
  - A. 3

B. 4

C. 5

- D. 6
- ix) A cut in voltage for Si diode is approximately
  - A. 0.2V

B. 0.6V

C. 1.1V

D. Any other.

- x) Holes may be considered as
  - A Pure conductor
- B. Intrinsic semiconductor
- C. Positive charge
- D. Negative charge.

#### **ANSWERS**

Item number	i	ii	iii	iv	٧	vi	vii	viii	ix	Х
Answer										

# SECTION B: (30 Marks) Answer ALL questions in this section.

2:	a)	Write down the two (2) popular semiconductor materials.
	b)	What is the difference between intrinsic semiconductor and extrinsic semiconductor?
3:	a)	In what unit a capacitance of a capacitor can be expressed?
	b	) Name any three (3) types of capacitors.
4:	ä	a) Write down the resistance value of an ideal diode in the region of conduction.
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	mbols of the follo		
(ii)	Step up transfor	mer.	
iii)	Tunnel diode.		

6:	State th	ne application of the following:	
		i) Ammeter	
		ii) Voltmeter	
		Ohmmeter	iii)
7:	Determine	the resistance value of the resistors with the following color codes	
	i) Green,	blue, red (with silver tolerance)	
	ii) Blue, B	rown, White, Brown, Red tolerance	
8:	List down	any three (3) workshop safety rules.	

	Candidate 5 Examination Number
Convert the following,	
i) $20k\Omega$ in to $\Omega$	
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ii) 3mA in to A	
ii) 3mA in to A	
iii) 1μH in to H	
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0: a) State ohm's law	I.

b) Use a simple circuit diagram to show the connection of an Ammeter in to an electric	С
circuit.	
11: a) List down any three (3) passive components.	
Till a) List down any times (s) passive components	
	_
b) Give two (2) examples of dielectric materials.	
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# SECTION C: (60 Marks) Answer any three (3) questions in this section.

a) Identify five (5) main parts of soldering iro	n.
b) Give two (2) examples of soldering mater	rials.
N	
) Write down a tool that can be used to	
i) Cut the wire inside the heart of the circ	cuit jumble.
) Test the presence of voltage at any point.	
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iii) Remove insulation from the wire.	

l3. Study	$\prime$ the figure 1 belo	ow carefully and the	en answer the o	questions that follow	,
	Transformer	Rectifier Figure1	Filter	→ Voltage regulato	r

b) Sketch the output waveform for each stage of fig 1.

	ntify the electronic component(s) used in each stage of figure 1.	
d) W	hat will happen if the last stage of figure 1 is removed?	
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) Idei	ntify necessary conditions for a normal rectifier diode to operate.	

14:

b)	i) Write down any two (2) advantages of a half wave rectifier.
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_	ii) Name the two (2) disadvantages of using a half wave rectifier.
- c)	A rectifier diode is connected to a 250V, 50Hz A.C supply via a step down
	transformer having turns ratio of 2:1. If $VB_E = 0.7V$ and $R_L$ is the load resistance. Use the given information to:
	i ) Draw a half wave rectifier circuit.
	ii) Determine the peak value of the rectified output.
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15: a	) State the bias condition that produce a widening depletion layer.
b)	With no incident light, a certain amount of reverse current flows in a
	photo diode. Give the name of this current.
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c)	Distinguish between rectifier and rectification.
d)	Draw the output waveform of a full wave rectifier.
e)	Two $10\mu F$ capacitors in parallel are connected in series with a $5\mu F$ capacitor. Determine the total circuit capacitance.
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