

OpenCL Overview

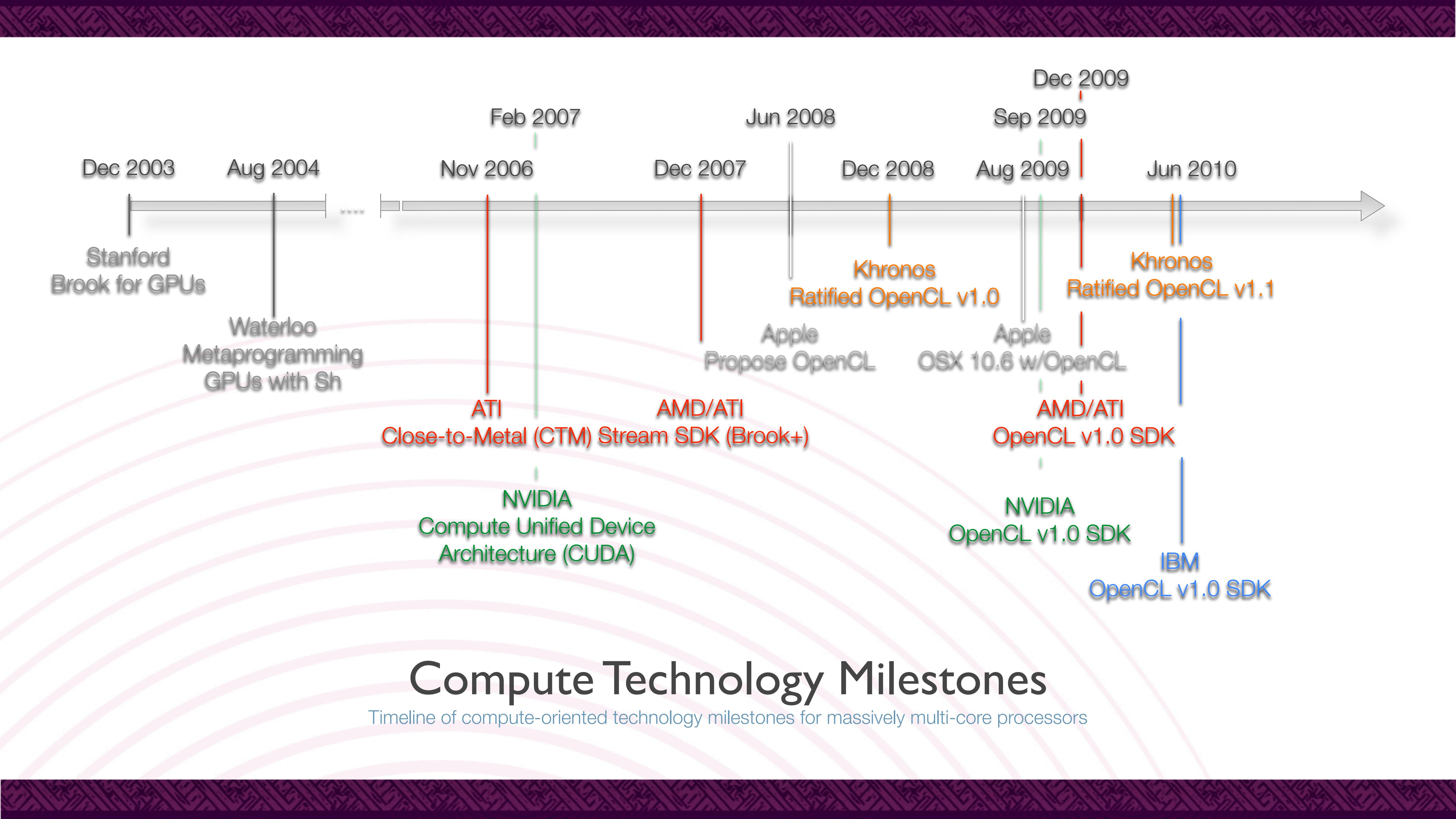
The Open Standard for Heterogenous Computing

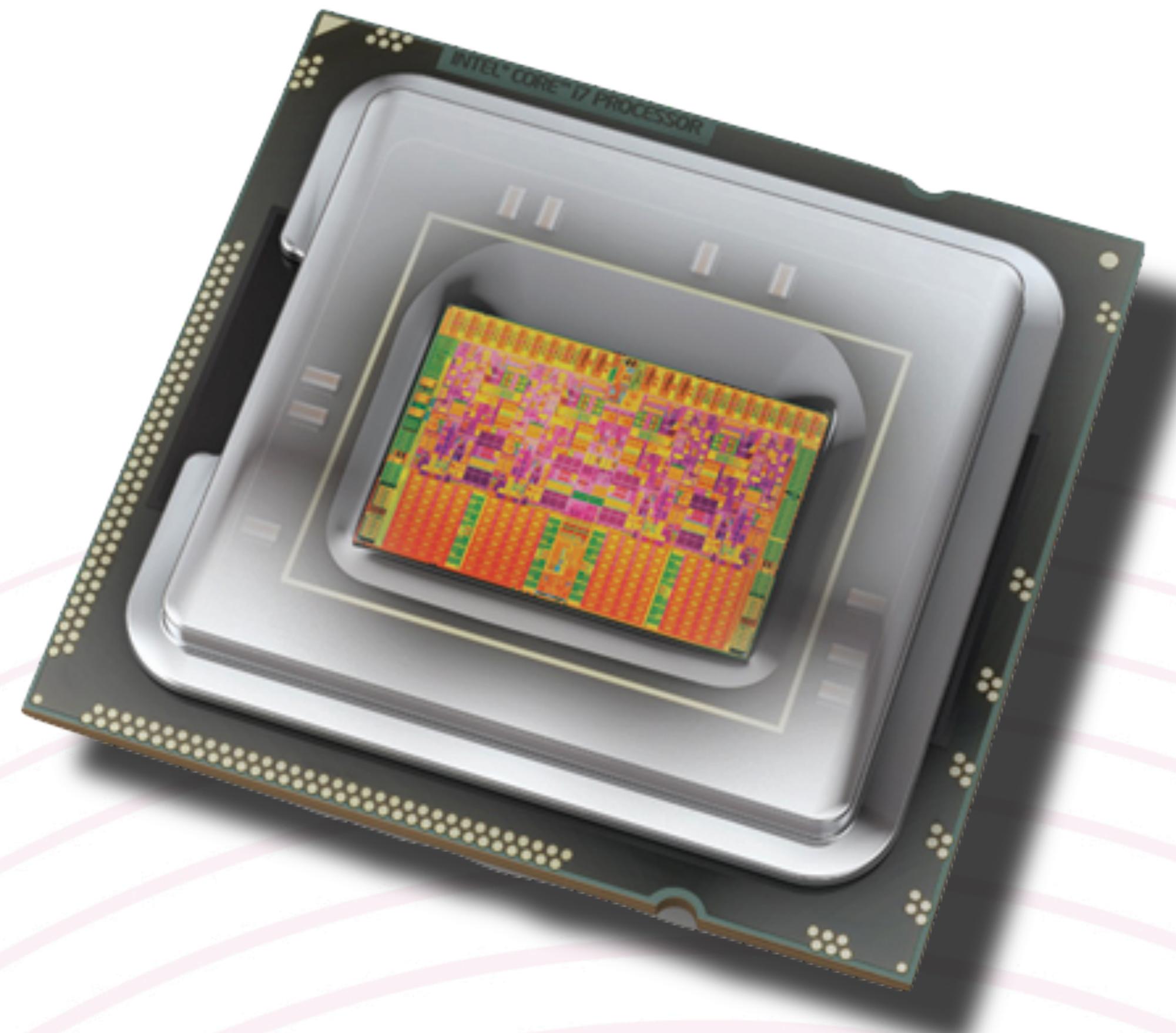
OpenCL: The Open Computing Language

The open standard for developing cross-platform, vendor agnostic, parallel programs that run on current and future multi-core processors within workstations, desktops, notebooks and embedded devices.

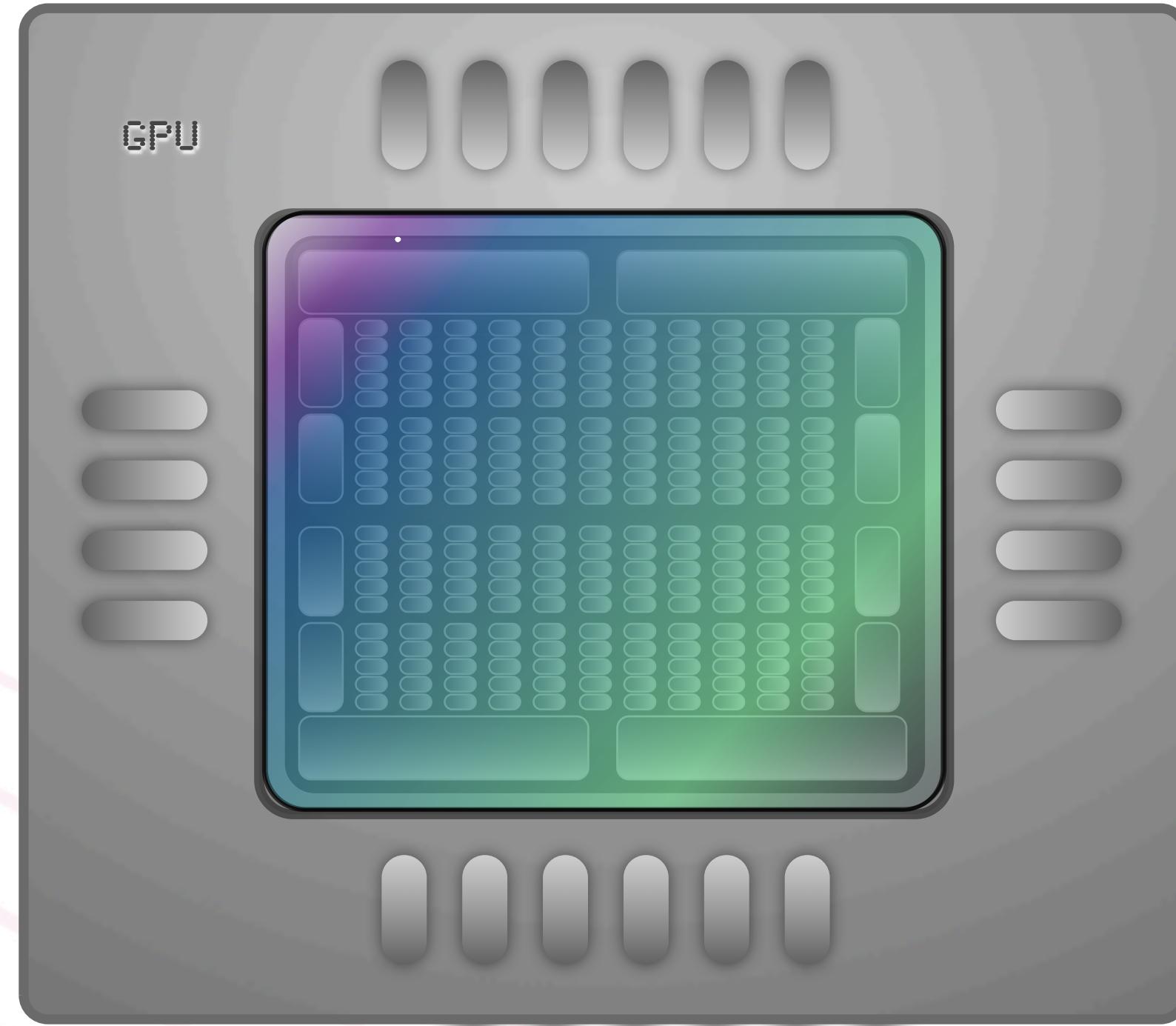
Historical Background

From whence we came and where we're heading...





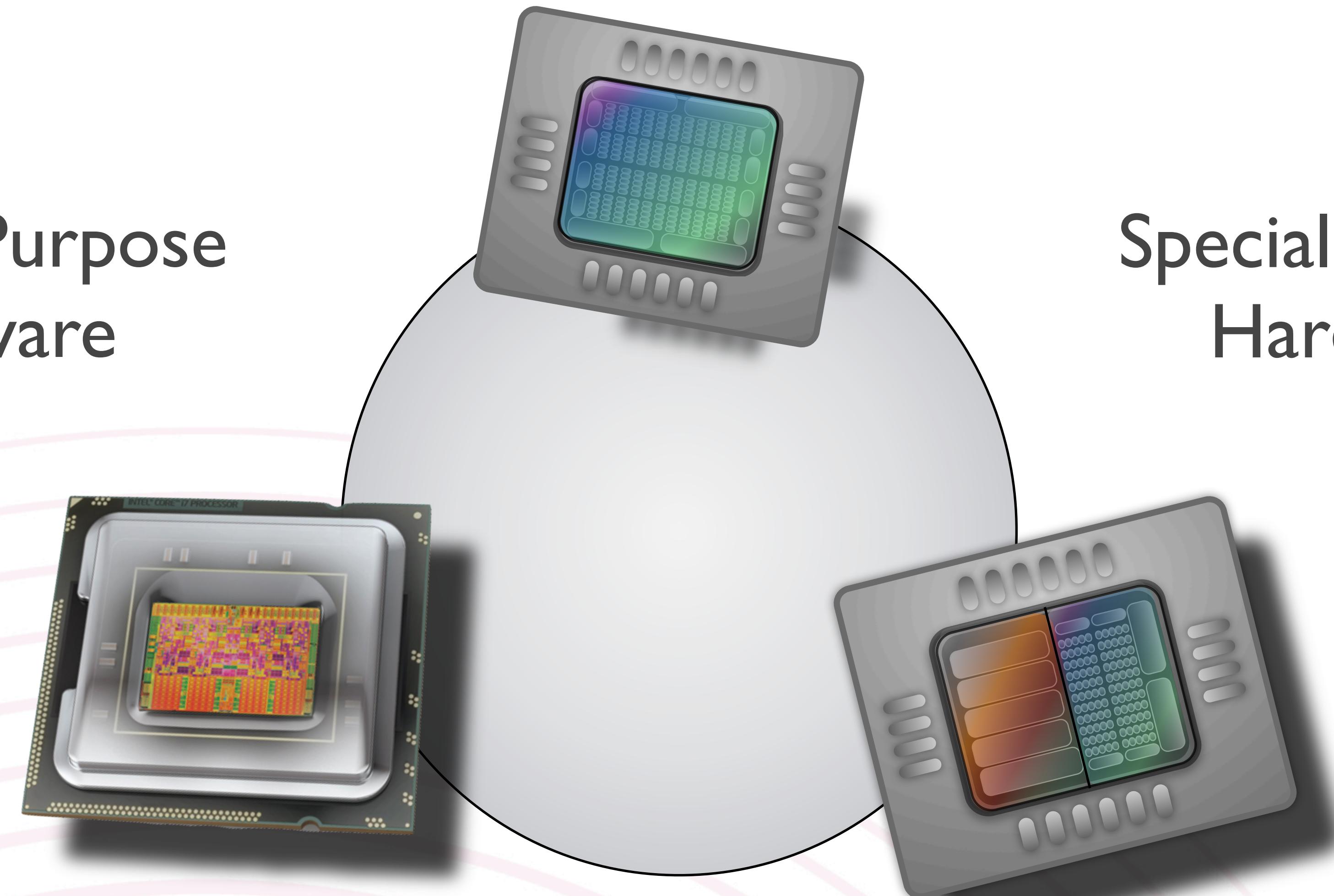
Modern Multi-Core CPUs



Massively Multi-Core GPUs

General-Purpose
Hardware

Special-Purpose
Hardware

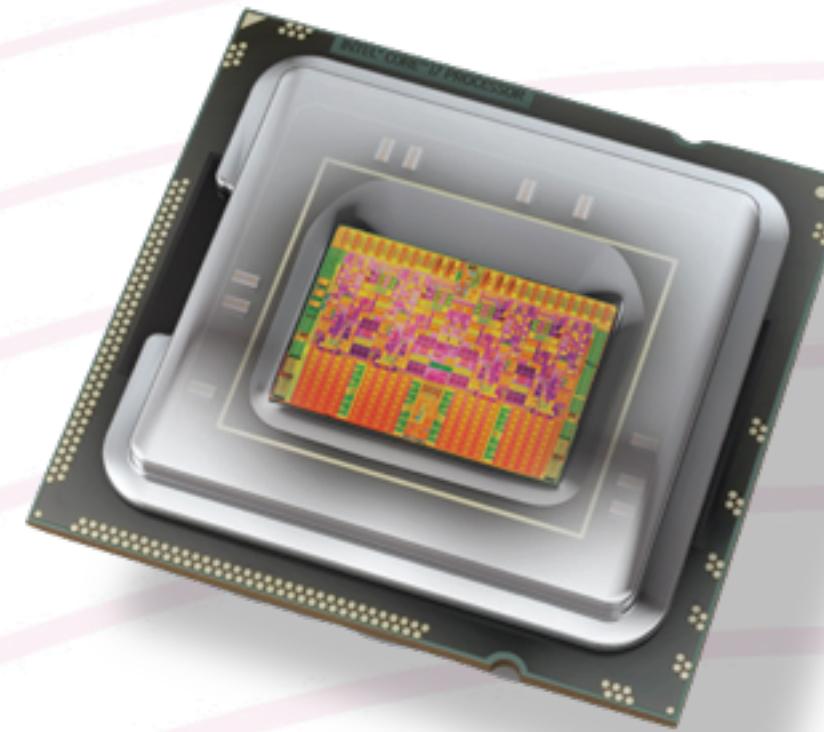


Wheel of Reincarnation

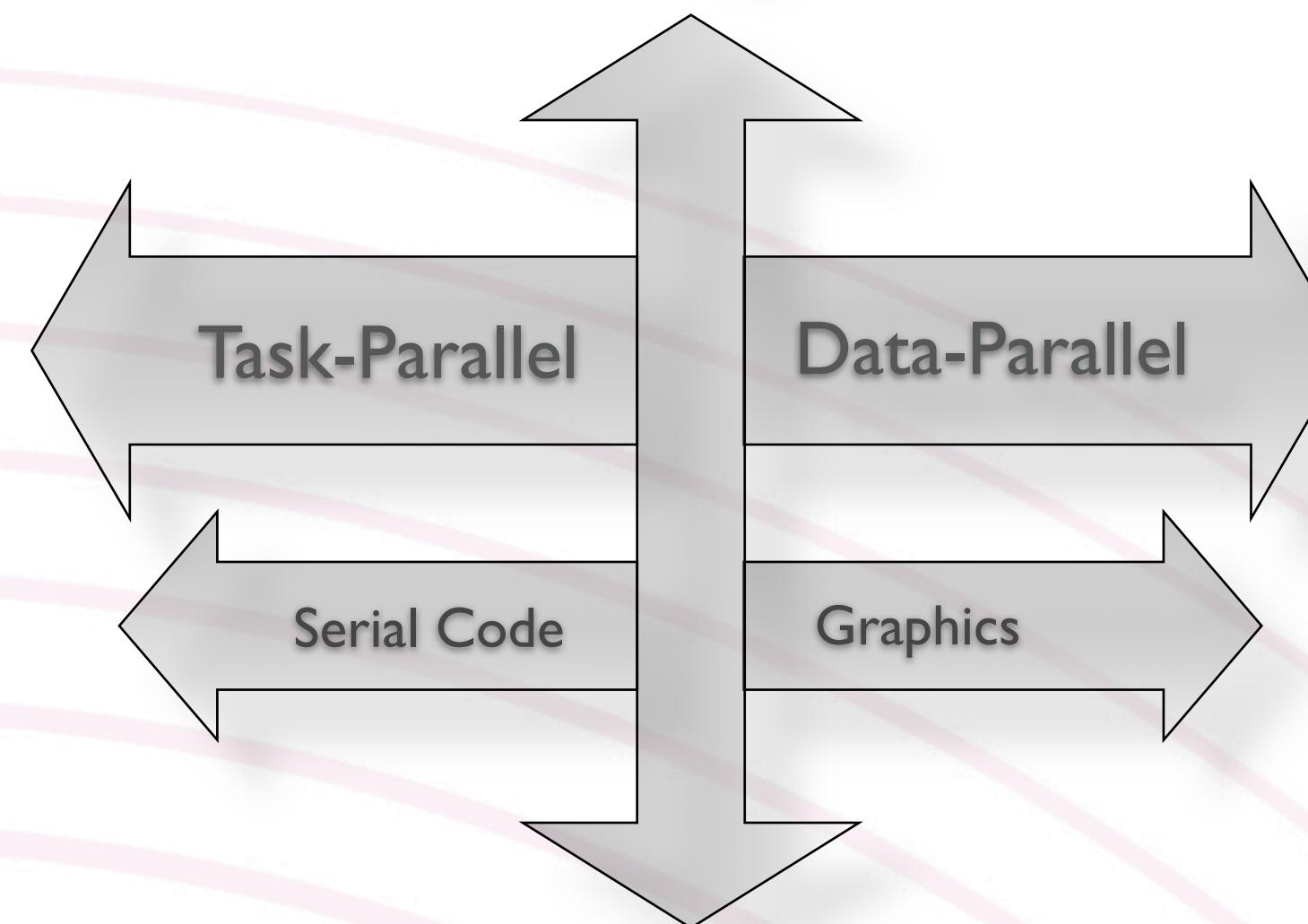
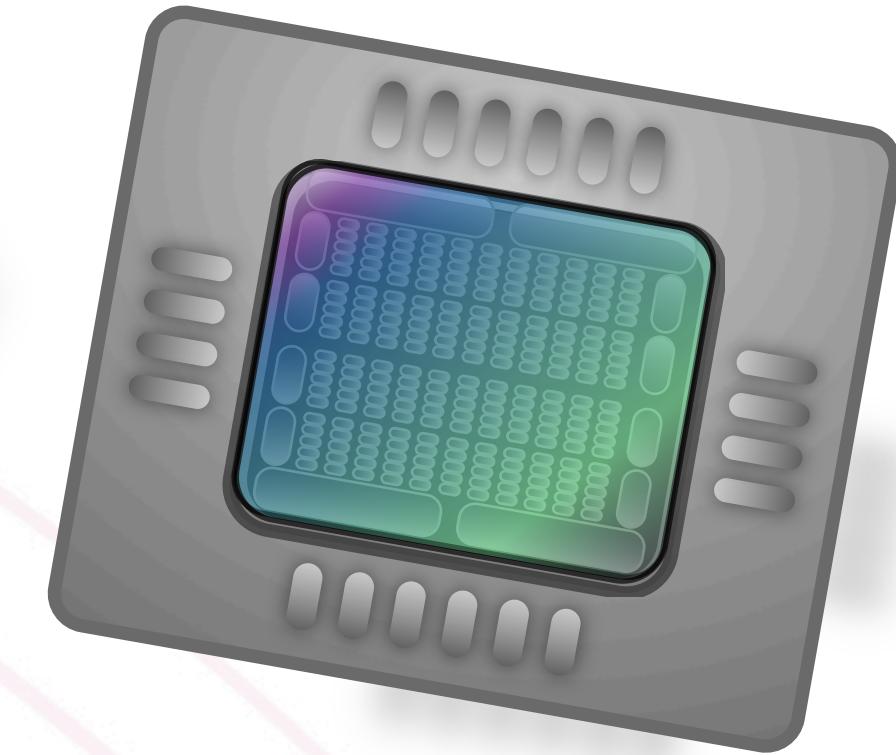
T. H. Myer and I. E. Sutherland. 1968. On the design of display processors.
Commun. ACM 11, 6 (June 1968), 410-414.

Application

Multi-Core CPUs



Massively Multi-Core GPUs



Need efficient use of all system resources to enable scalable high-performance applications

Motivation for OpenCL: Enable heterogenous computing

- Right now on current devices for real application
- Help drive the future for multi-core technology

Design Goals:

Low-level abstraction for compute

- Avoid specifics of the hardware
- Enable high performance
- Support device independence

Design Goals:

Enable all compute resources

- CPUs, GPUs, and other processors
- Data- and task- parallel compute models
- Efficient resource sharing between processors

Design Goals:

Efficient parallel programming model

- ANSI C99 based kernel language
- Minimal restrictions and language additions
- Straight-forward development path

Design Goals:

Consistent results on all platforms

- Well-defined precision requirements for all operations
- Maximal-error allowances for floating-point math
- Comprehensive conformance suite for API + language

Design Goals:

Interoperability with Graphics APIs

- Enable advanced visual computing applications
- Efficient resource sharing for graphics data
- Support graphics oriented built-in methods

Design Goals:

Drive future hardware requirements

- For both consumer-level and high-performance computing applications
- Be prepared for the next spin on the wheel of reincarnation

OpenCL: Vendor Implementations

AMD: <http://developer.amd.com/gpu/ATISDK/>
X86, Radeon, FireStream, FirePro GPUs

APPLE: <http://developer.apple.com/object/opencl.html>
All shipping desktop + notebook hardware ...

OpenCL: Vendor Implementations

NVIDIA: <http://developer.nvidia.com/object/opencl.html>

Tesla, Fermi GPUs

IBM: <http://www.alphaworks.ibm.com/tech/opencl>

Cell-BE, Power6, Power7, more coming soon...

OpenCL: Vendor Implementations

INTEL: <http://whatif.intel.com/>

Intel X86 CPUs

SNU-SAMSUNG: <http://opencl.snu.ac.kr/>

Cell-BE, ARM, DSP, more coming soon...

Anatomy of OpenCL:

Technology stack

- Language specification
- Platform specification
- Runtime specification

Khronos.org: OpenCL Quick Reference Card

[http://www.khronos.org/files/
opencl-1-1-quick-reference-card.pdf](http://www.khronos.org/files/opencl-1-1-quick-reference-card.pdf)

OpenCL API 1.1 Quick Reference Card - Page 1

OpenCL (Open Computing Language)
is a multi-vendor open standard for general-purpose parallel programming of heterogeneous systems that include CPUs, GPUs and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop computer systems and handheld devices.

[n.n] refers to the section in the API Specification available at www.khronos.org/opengl.

The OpenCL Runtime

Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (
    const cl_context context,
    const cl_device_id device,
    const cl_command_queue_properties properties,
    cl_int *errcode_ret)
properties: CL_QUEUE_PROFILING_ENABLE,
CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE
```

```
cl_int clRetainCommandQueue (
    cl_command_queue command_queue)
```

```
cl_int clReleaseCommandQueue (
    cl_command_queue command_queue)
```

```
cl_int clGetCommandQueueInfo (
    cl_command_queue command_queue,
    cl_command_queue_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
```

```
param_name: CL_QUEUE_CONTEXT,
CL_QUEUE_DEVICE,
CL_QUEUE_REFERENCE_COUNT,
CL_QUEUE_PROPERTIES
```

Querying Platform Info and Devices [4.1, 4.2]

```
cl_int clGetPlatformIds (cl_uint *platforms,
    cl_uint *num_platforms)
```

```
cl_int clGetPlatformInfo (cl_platform_id platform,
    cl_platform_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
```

```
param_name: CL_PLATFORM_PROFILE, CL_PLATFORM_NAME, VENDOR, EXTENSIONS
```

```
cl_int clGetDeviceIds (cl_platform_id platform,
    cl_device_type type, cl_int num_entries,
    cl_device_id *devices, cl_uint *num_devices)
```

```
device_type: CL_DEVICE_TYPE_CPU, GPU,
```

```
CL_DEVICE_TYPE_ACCELERATOR, DEFAULT, ALL)
```

OpenCL API 1.1 Quick Reference Card - Page 1

OpenCL (Open Computing Language) is a multi-vendor open standard for general-purpose parallel programming of heterogeneous systems that include CPUs, GPUs and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop computer systems and handheld devices.

[n.n.] refers to the section in the API Specification available at www.khronos.org/opencl.

The OpenCL Runtime

Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (cl_context context, cl_device_id device, cl_command_queue_properties properties, cl_int *errcode_ret)
```

properties: CL_QUEUE_PROFILING_ENABLE, CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE

clRetainCommandQueue (cl_command_queue command_queue)

clReleaseCommandQueue (cl_command_queue command_queue)

clGetCommandQueueInfo (cl_command_queue command_queue, cl_platform_id *platforms, cl_uint num_platforms)

clGetPlatformInfo (cl_platform_id platform, size_t param_value_size, void *param_value, size_t *param_value_size_ret)

param_name: CL_PLATFORM_PROFILE, CL_PLATFORM_NAME, VENDOR, EXTENSIONS

clGetDevices (cl_platform_id platform, cl_device_type device_type, cl_uint num_entries, cl_device_id *devices, cl_uint *num_devices)

device_type: CL_DEVICE_TYPE_CPU, GPU, CL_DEVICE_TYPE_ACCELERATOR, DEFAULT, ALL

Buffer Objects

Elements of a buffer object can be a scalar or vector data type or a user-defined structure. Elements are stored sequentially and are accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2.1]

```
cl_mem clCreateBuffer (cl_context context, cl_mem_flags flags, size_t size, void *host_ptr, cl_int *errcode_ret)
```

clCreateSubBuffer (cl_mem buffer, cl_mem_flags flags, cl_buffer_create_type buffer_create_type, const void *buffer_create_info, cl_int *errcode_ret)

flags for clCreateBuffer and clCreateSubBuffer:

- CL_MEM_READ_WRITE
- CL_MEM_WRITE_ONLY
- CL_MEM_USE_ALLOC_PTR

Read, Write, Copy Buffer Objects

clEnqueueReadBuffer

```
cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_read, const size_t t_buffer_origin[3], const size_t t_host_origin[3], const size_t t_region[3], size_t t_buffer_row_pitch, size_t t_buffer_slice_pitch, void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

clEnqueueWriteBuffer

```
cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_write, const size_t t_buffer_origin[3], const size_t t_host_origin[3], const size_t t_region[3], size_t t_buffer_row_pitch, size_t t_buffer_slice_pitch, void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

clEnqueueCopyBuffer

```
cl_command_queue command_queue, cl_mem src_buffer, cl_mem dst_buffer, size_t src_offset, size_t dst_offset, size_t t, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

clEnqueueCopyBufferRect

```
cl_command_queue command_queue, cl_mem src_buffer, cl_mem dst_buffer, const size_t t_src_origin[3], const size_t t_dst_origin[3], const size_t t_src_row_pitch, size_t t_src_slice_pitch, size_t t_dst_row_pitch, size_t t_dst_slice_pitch, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

clEnqueueWriteBuffer

```
cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_write, size_t offset, size_t t, const void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

Program Objects

Create Program Objects [5.6.1]

```
cl_program clCreateProgramWithSource (cl_context context, cl_uint count, const char **strings, const size_t *lengths, cl_int *errcode_ret)
```

clCreateProgramWithBinary (cl_context context, cl_uint num_devices, const cl_device_id *device_list, const size_t *lengths, const unsigned char **binaries, cl_int *binary_status, cl_int *errcode_ret)

clRetainProgram (cl_program program)

clReleaseProgram (cl_program program)

©2010 Khronos Group - Rev. 0610

©2010 Khronos Group - Rev. 0610

©2010 Khronos Group - Rev. 0610

The OpenCL Platform Layer

The OpenCL platform layer implements platform-specific features that allow applications to query OpenCL devices, device configuration information, and to create OpenCL contexts using one or more devices.

Contexts

```
cl_context clCreateContext (const cl_context_properties *properties, cl_uint num_devices, const cl_device_id *devices, void (CL_CALLBACK*pfn_notify) (const char *errinfo, const void *private_info, size_t cb, void *user_data, cl_int *errcode_ret))
```

properties: CL_CONTEXT_PLATFORM, CL_GL_CONTEXT_KHR, CL_GGL_SHAREGROUP_KHR, CL_EGL_DISPLAY_KHR, CL_WGL_HDC_KHR

cl_context clCreateContextFromType (const cl_context_properties *properties, cl_device_type type, void (CL_CALLBACK*pfn_notify) (const char *errinfo, const void *private_info, size_t cb, void *user_data, cl_int *errcode_ret))

cl_int clGetDeviceInfo (cl_device_id device, cl_device_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)

param_name: CL_DEVICE_VENDOR_ID, CL_DEVICE_MAX_COMPUTE_UNITS, CL_DEVICE_MAX_WORK_ITEM_DIMENSIONS, CL_DEVICE_MAX_WORK_GROUP_SIZE, CL_DEVICE_MAX_WORK_GROUP_SIZE, CL_DEVICE_MAX_WORK_GROUP_SIZE, CL_DEVICE_MAX_WORK_GROUP_SIZE, CL_DEVICE_MAX_CLOCK_FREQUENCY, CL_DEVICE_ADDRESS_BITS, CL_DEVICE_MAX_MEM_ALLOC_SIZE, CL_DEVICE_IMAGE_SUPPORT, CL_DEVICE_MAX_READ_WRITE_IMAGE_ARGS, CL_DEVICE_IMAGE2D_MAX_WIDTH_HEIGHT, CL_DEVICE_IMAGE3D_MAX_WIDTH_HEIGHT_DEPTH, CL_DEVICE_MAX_SAMPLERS, CL_DEVICE_MAX_PARAMETER_SIZE, CL_DEVICE_MAX_BASE_ALIGN, CL_DEVICE_MIN_BASE_ALIGN, CL_DEVICE_MAX_ALIGNMENT, CL_DEVICE_SINGLE_FPCONFIG, CL_DEVICE_GLOBAL_MEM_CACHE_SIZE, CL_DEVICE_GLOBAL_MEM_CACHELINE_SIZE, CL_DEVICE_GLOBAL_MEM_SIZE, CL_DEVICE_MAX_CONSTANT_BUFFER_SIZE_ARGS, CL_DEVICE_LOCAL_MEM_TYPE_SIZE, CL_DEVICE_ERROR_CORRECTION_SUPPORT, CL_DEVICE_PROFILING_TIMER_RESOLUTION, CL_DEVICE_ENDIAN_LITTLE, CL_DEVICE_AVAILABLE, CL_DEVICE_COMPILER_AVAILABLE, CL_DEVICE_EXECUTION_CAPABILITIES, CL_DEVICE_QUEUE_PROPERTIES, CL_DEVICE_VENDOR, PROFILE, VERSION, CL_PLATFORM_NAME, VENDOR, EXTENSIONS

cl_int clGetDeviceInfo (cl_platform_id platform, cl_device_type device_type, cl_uint num_entries, cl_device_id *devices, cl_uint *num_devices)

device_type: CL_DEVICE_TYPE_CPU, GPU, CL_DEVICE_TYPE_ACCELERATOR, DEFAULT, ALL

Querying Platform Info and Devices

[4.1, 4.2]

cl_int clGetPlatformInfo (cl_platform_id *platforms, cl_uint num_platforms)

cl_int clGetPlatformInfo (cl_platform_id platform, size_t param_value_size, void *param_value, size_t *param_value_size_ret)

param_name: CL_PLATFORM_PROFILE, CL_PLATFORM_NAME, VENDOR, EXTENSIONS

cl_int clGetDevices (cl_platform_id platform, cl_device_type device_type, cl_uint num_entries, cl_device_id *devices, cl_uint *num_devices)

device_type: CL_DEVICE_TYPE_CPU, GPU, CL_DEVICE_TYPE_ACCELERATOR, DEFAULT, ALL

Buffer Objects

Elements of a buffer object can be a scalar or vector data type or a user-defined structure. Elements are stored sequentially and are accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2.1]

```
cl_mem clCreateBuffer (cl_context context, cl_mem_flags flags, size_t size, void *host_ptr, cl_int *errcode_ret)
```

clCreateSubBuffer (cl_mem buffer, cl_mem_flags flags, cl_buffer_create_type buffer_create_type, const void *buffer_create_info, cl_int *errcode_ret)

flags for clCreateBuffer and clCreateSubBuffer:

- CL_MEM_READ_WRITE
- CL_MEM_WRITE_ONLY
- CL_MEM_USE_ALLOC_PTR

Read, Write, Copy Buffer Objects

clEnqueueReadBuffer

```
cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_read, const size_t t_buffer_origin[3], const size_t t_host_origin[3], const size_t t_region[3], size_t t_buffer_row_pitch, size_t t_buffer_slice_pitch, void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

clEnqueueWriteBuffer

```
cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_write, const size_t t_offset, size_t t, const void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

Program Objects

Create Program Objects [5.6.1]

```
cl_program clCreateProgramWithSource (cl_context context, cl_uint count, const char **strings, const size_t *lengths, cl_int *errcode_ret)
```

clCreateProgramWithBinary (cl_context context, cl_uint num_devices, const cl_device_id *device_list, const size_t *lengths, const unsigned char **binaries, cl_int *binary_status, cl_int *errcode_ret)

clRetainProgram (cl_program program)

clReleaseProgram (cl_program program)

(Program Objects Continue >)

OpenCL API 1.1 Quick Reference Card - Page 2

Program Objects (continued)

```
cl_int clGetProgramBuildInfo (cl_program program, cl_device_id device, cl_program_build_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)
```

param_name: CL_PROGRAM_BUILD_STATUS, OPTIONS, LOG

Unload the OpenCL Compiler [5.6.4]

```
cl_int clUnloadCompiler (void)
```

Supported Data Types

Built-in Scalar Data Types

OpenCL Type	API Type	Description
bool	--	true (1) or false (0)
char	cl_char	8-bit signed
unsigned char	cl_uchar	8-bit unsigned
short	cl_short	16-bit signed
unsigned short	cl_ushort	16-bit unsigned
int	cl_int	32-bit signed
unsigned int	cl_uint	32-bit unsigned
long	cl_long	64-bit signed
unsigned long	cl_ulong	64-bit unsigned
float	cl_float	32-bit float
half	cl_half	16-bit float (for storage only)
size_t	--	32 or 64-bit unsigned integer
ptrdiff_t	--	32 or 64-bit signed integer
intptr_t	--	signed integer
uintptr_t	--	unsigned integer
void	void	void

Built-in Vector Data Types

OpenCL Type	API Type	Description
charn	cl_chn	8-bit signed
ucharn	cl_uchrn	8-bit unsigned
shortn	cl_shorth	16-bit signed
ushortn	cl_ushorth	16-bit unsigned
intn	cl_intn	32-bit signed
uintn	cl_uin	32-bit unsigned
longn	cl_longn	64-bit signed
ulongn	cl_ulongn	64-bit unsigned
floatn	cl_floatn	32-bit float

Map Buffer Objects

[5.4.2]

void *clEnqueueMapBuffer

cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_map, const size_t t_buffer_origin[3], const size_t t_host_origin[3], const size_t t_region[3], size_t t_buffer_row_pitch, size_t t_buffer_slice_pitch, void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event, cl_int *errcode_ret)

Map Buffer Objects

[5.4.2]

cl_int clRetainMemObject (cl_mem memobj)

cl_int clReleaseMemObject (cl_mem memobj)

cl_int clSetMemObjectDestructorCallback (cl_mem memobj, void (CL_CALLBACK*pfn_notify) (cl_mem memobj, void *user_data), void *user_data)

cl_int clEnqueueUnmapMemObject (cl_command_queue command_queue, cl_mem memobj, void *mapped_ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)

Query Buffer Object

[5.4.3]

cl_int clGetMemObjectInfo (cl_mem memobj, cl_mem_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)

param_name: CL_MEMORY_TYPE, FLAGS, SIZE, HOST_PTR, CL_MEMORY_MAP_FLAGS, COUNT, CL_MEMORY_OFFSET, CL_MEMORY_CONTEXT, CL_MEMORY_ASSOCIATED_MEMOBJECT

Math Intrinsics

cl-single-precision-constant

-cl-denorms-are-zero

Warning request/suppress:

-w -Werror

Control OpenCL C language version:

-cl-std=CL1.1 // OpenCL 1.1 specification.

Query Program Objects

[5.6.5]

cl_int clGetProgramInfo (cl_program program, cl_program_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)

param_name: CL_PROGRAM_REFERENCE_COUNT, CL_PROGRAM_CONTEXT, CL_PROGRAM_DEVICES, CL_PROGRAM_SOURCE, BINARY_SIZES, BINARIES

(Program Objects Continue >)

Kernel and Event Objects

Create Kernel Objects

[5.7.1]

cl_kernel clCreateKernel (cl_program program, const char *kernel_name, cl_int *errcode_ret)

cl_int clCreateKernelsInProgram (cl_program program, cl_uint num_kernels, cl_kernel *kernels, cl_int *num_kernels_ret)

cl_int clRetainKernel (cl_kernel kernel)

cl_int clReleaseKernel (cl_kernel kernel)

Unload the OpenCL Compiler

[5.6.4]

cl_int clUnloadCompiler (void)

Event Objects

[5.9]

cl_event clCreateUserEvent (cl_context context, cl_int *errcode_ret)

cl_int clSetUserEventStatus (cl_event event, cl_execution_status)

cl_int clWaitForEvents (cl_uint num_events, const cl_event *event_list)

cl_int clRetainEvent (cl_event event)

cl_int clReleaseEvent (cl_event event)

Kernel Args. & Object Queries

[5.7.3]

cl_int clSetKernelArg (cl_kernel kernel, cl_uint arg_index, size_t arg_size, const void *arg_value)

cl_int clGetKernelInfo (cl_kernel kernel, cl_device_id device, cl_kernel_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)

param_name: CL_KERNEL_FUNCTION_NAME, CL_KERNEL_NUM_ARGS, CL_KERNEL_REFERENCE_COUNT, CL_KERNEL_COMMAND_EXECUTION_STATUS, CL_KERNEL_CONTEXT, CL_KERNEL_PROGRAM

cl_int clGetKernelWorkGroupInfo (cl_kernel kernel, cl_device_id device, cl_kernel_group_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)

param_name: CL_KERNEL_WORK_GROUP_SIZE, CL_KERNEL_COMPILE_WORK_GROUP_SIZE, CL_KERNEL_LOCAL(MEMORY), CL_KERNEL_PREFERRED(MEMORY), CL_KERNEL_MULTIPLE(MEMORY)

Out-of-order Execution of Kernels & Memory Object Commands

[5.10]

cl_int clEnqueueMarker (cl_command_queue command_queue, cl_event *event)

cl_int clEnqueueWaitForEvents (cl_command_queue command

Scalar Storage Types

Data Types for the OpenCL Framework

- `void` – incomplete type corresponding to empty set
- `cl_char` – 8bit signed two's complement integer value
- `cl_uchar` – 8bit unsigned integer
- `cl_short` – 16bit signed two's complement integer value
- `cl_ushort` – 16bit unsigned integer
- `cl_int` – 32bit signed two's complement integer value
- `cl_uint` – 32bit unsigned integer
- `cl_long` – 64bit signed two's complement integer value
- `cl_ulong` – 64bit unsigned integer value
- `cl_half` – half precision floating-point value (**IEEE 754-2008**)
- `cl_float` – full precision floating-point value (**IEEE 754**)
- `cl_double` – double precision floating-point value (**IEEE 754**)

Vector Storage Types

Data Types for the OpenCL Framework

N – Supported values of N are 2,4,8,16.

cl_charN – 8bit signed two's complement integer value
cl_ucharN – 8bit unsigned integer

cl_shortN – 16bit signed two's complement integer value
cl_ushortN – 16bit unsigned integer

cl_intN – 32bit signed two's complement integer value
cl_uintN – 32bit unsigned integer

cl_longN – 64bit signed two's complement integer value
cl_ulongN – 64bit unsigned integer value

cl_halfN – half precision floating-point value (**IEEE 754-2008**)
cl_floatN – full precision floating-point value (**IEEE 754**)
cl_doubleN – double precision floating-point value (**IEEE 754**)

Object Types

Data Types for the OpenCL Framework

<code>cl_platform_id</code>	- identifier for a specific platform
<code>cl_device_id</code>	- identifier for a specific compute device
<code>cl_context</code>	- handle for a compute context
<code>cl_command_queue</code>	- handle for a command queue (for compute device)
<code>cl_mem</code>	- handle for a memory resource (managed by the context)
<code>cl_program</code>	- handle for a program resource (library of kernels)
<code>cl_kernel</code>	- handle for a compute kernel (compiled)

All object types are opaque handles

Enables cross-platform compatibility for complex data types

All objects are reference counted and garbage collected

When reference count reaches zero, object is deallocated

Enqueue Command Methods

Conventions for the OpenCL Framework

```
cl_int clEnqueueMethod(cl_command_queue, /* command queue */,
                      ...
                      /* method specific parameters */,
                      cl_uint *           /* num of events in wait list */,
                      const cl_event *    /* event wait list */,
                      cl_event *          /* returned event */)
```

All enqueue methods return an error code

Method returns CL_SUCCESS if command was enqueued successfully

All enqueue methods support an event wait-list

Command will not get executed until all events in list are complete

All enqueue methods return an event for the command

Returned event identifies the specific command that was enqueued

Create Object Methods

Conventions for the OpenCL Framework

```
cl_object clCreateMethod(cl_context /* compute context */,  
                        ...          /* method specific parameters */,  
                        cl_int *     /* returned error code */)
```

All object creation methods return an object handle

Method returns an invalid object if creation fails

All object creation methods require a compute context

Objects and resources are managed in a context

All object creation methods optionally return an error code

Returned value identifies any errors that were encountered

Value is set to CL_SUCCESS if object was created successfully

Object Management Methods

Conventions for the OpenCL Framework

```
cl_int clRetainObject( cl_object          /* handle of object to retain */ )
```

```
cl_int clReleaseObject( cl_object          /* handle of object to release */ )
```

All retain / release object methods return an error code

Method returns CL_SUCCESS if operation was successfully

All retain methods increment the object reference count

Reference count determines life span and is set to one upon creation of object

All release methods decrement the object reference count

When reference count reaches zero, object is deallocated

Kernel Language

ANSI C99-based language

- Familiar to developers
- Some extensions & restrictions
- Includes a rich set of built-in functions
- Supports online & offline compilation

```
__kernel void square(  
    __global float* input, __global float* output)  
{  
    size_t i = get_global_id(0);  
    output[i] = input[i] * input[i];  
}
```

Declare functions with **__kernel** attribute

Defines an entry point or exported method in a program object

Use address space and usage qualifiers for memory

Address spaces and data usage must be specified for all memory objects

Built-in methods provide access to index within compute domain

Use **get_global_id** for unique work-item id, **get_group_id** for work-group, etc

Kernel Language

Well-defined precision requirements

- All math functions have accuracy requirements
- IEEE 754 floating-point rounding w/limits on max error
- Support for half and double precision

OpenCL Platform

Hardware abstraction layer

- Access a diverse set of compute resources
- Device agnostic and vendor neutral
- Cross-platform and driver independent

OpenCL Runtime

Management interface for resources

- Query, select and setup devices
- Create and manage compute contexts
- Allocate, read and write memory objects
- Execute work and synchronise devices

```
// Fill our data set with random float values
int count = 1024 * 1024;
for(i = 0; i < count; i++)
    data[i] = rand() / (float)RAND_MAX;

// Connect to a compute device, create a context and a command queue
cl_device_id device;
clGetDeviceIDs(CL_DEVICE_TYPE_GPU, 1, &device, NULL);
cl_context context = clCreateContext(0, 1, & device, NULL, NULL, NULL);
cl_command_queue queue = clCreateCommandQueue(context, device, 0, NULL);

// Create and build a program from our OpenCL-C source code
cl_program program = clCreateProgramWithSource(context, 1, (const char **)&src,
                                                NULL, NULL);
clBuildProgram(program, 0, NULL, NULL, NULL, NULL);

// Create a kernel from our program
cl_kernel kernel = clCreateKernel(program, "square", NULL);
```

```
// Allocate input and output buffers, and fill the input with data
cl_mem input = clCreateBuffer(context, CL_MEM_READ_ONLY, sizeof(float) * count,
                               NULL, NULL);

// Create an output memory buffer for our results
cl_mem output = clCreateBuffer(context, CL_MEM_WRITE_ONLY, sizeof(float) * count,
                                NULL, NULL);

// Copy our host buffer of random values to the input device buffer
clEnqueueWriteBuffer(queue, input, CL_TRUE, 0, sizeof(float) * count, data, 0,
                     NULL, NULL);

// Get the maximum number of work items supported for this kernel on this device
size_t global = count; size_t local = 0;
clGetKernelWorkGroupInfo(kernel, device, CL_KERNEL_WORK_GROUP_SIZE, sizeof(int),
                        &local, NULL);
```

```
// Set the arguments to our kernel, and enqueue it for execution
clSetKernelArg(kernel, 0, sizeof(cl_mem), &input);
clSetKernelArg(kernel, 1, sizeof(cl_mem), &output);
clSetKernelArg(kernel, 2, sizeof(unsigned int), &count);
clEnqueueNDRangeKernel(queue, kernel, 1, NULL, &global, &local, 0, NULL, NULL);

// Force the command queue to get processed, wait until all commands are complete
clFinish(queue);

// Read back the results
clEnqueueReadBuffer( queue, output, CL_TRUE, 0, sizeof(float) * count, results, 0,
                     NULL, NULL );

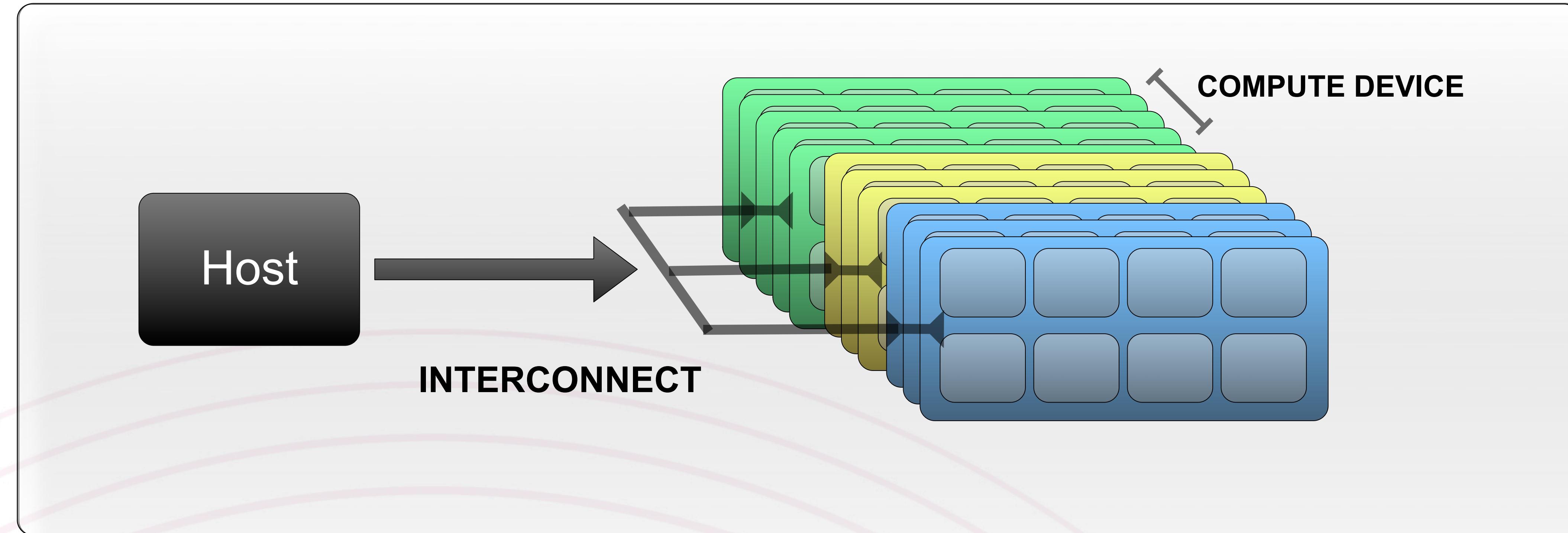
// Validate our results
int correct = 0;
for(i = 0; i < count; i++)
    correct += (results[i] == data[i] * data[i]) ? 1 : 0;

// Print a brief summary detailing the results
printf("Computed '%d/%d' correct values!\n", correct, count);
```

OpenCL Architecture

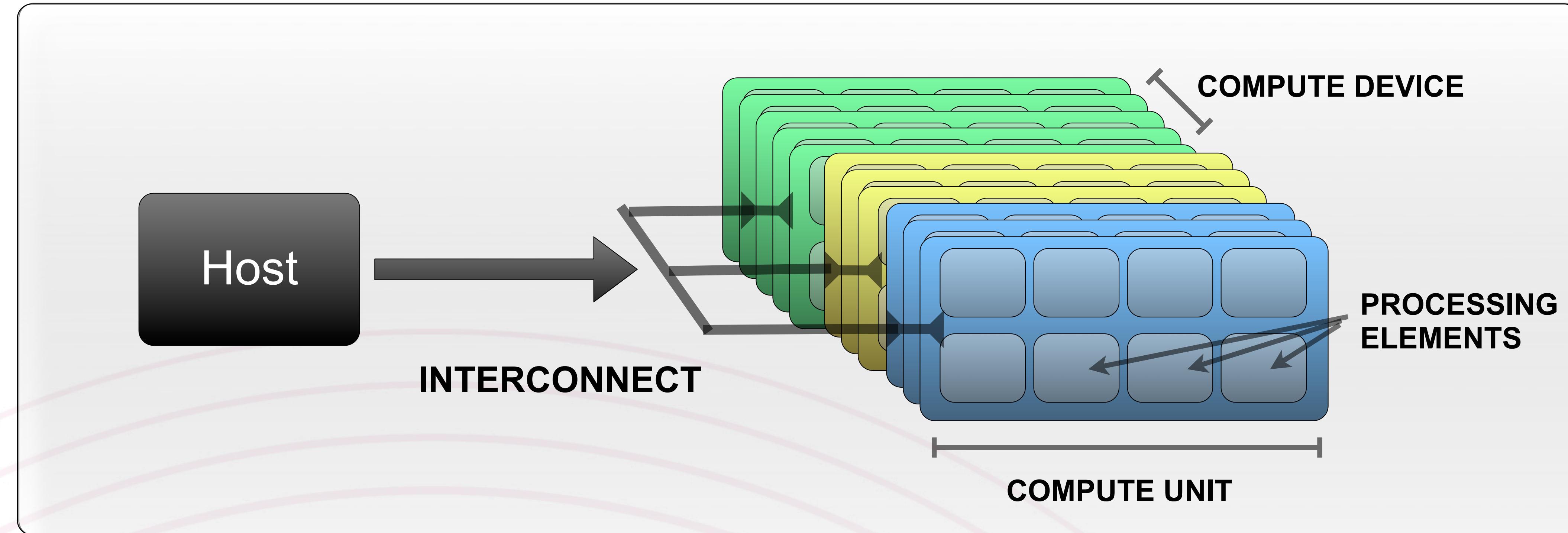
Conceptual models

- Platform model
- Execution model
- Memory model
- Programming model



Platform model encapsulates compute resources
Hierarchy of compute units logically grouped together based on locality

One host connected to one or more compute devices
Compute device could be a CPU, GPU, or other processor.



Each compute device composed of one or more units
A compute unit may be a core, multi-processor, streaming mp, etc.

Each unit has one or more processing element(s)
Processing elements execute instructions together (eg. SIMD-style)

OpenCL API 1.1 Quick Reference Card - Page 1

OpenCL (Open Computing Language) is a multi-vendor open standard for general-purpose parallel programming of heterogeneous systems that include CPUs, GPUs and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop

The OpenCL Runtime

Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (  
    cl_context context, cl_device_id device,  
    cl_command_queue_properties properties,  
    cl_int *errcode_ret)  
  
properties: CL_QUEUE_PROFILING_ENABLE,  
CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE  
  
cl_int clRetainCommandQueue (  
    cl_command_queue command_queue)  
  
cl_int clReleaseCommandQueue (  
    cl_command_queue command_queue)  
  
cl_int clGetCommandQueueInfo (  
    cl_command_queue command_queue,  
    cl_command_queue_info param_name,  
    size_t param_value_size, void *param_value,  
    size_t *param_value_size_ret)  
  
param_name: CL_QUEUE_CONTEXT,  
CL_QUEUE_DEVICE,  
CL_QUEUE_REFERENCE_COUNT,  
CL_QUEUE_PROPERTIES
```

Buffer Objects

Elements of a buffer object can be a scalar or vector, a user-defined structure. Elements are stored sequentially and are accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2]

```
cl_mem clCreateBuffer (cl_context context,  
                      cl_mem_flags flags, size_t size, void *host_ptr,  
                      cl_int *errcode_ret)  
  
cl_mem clCreateSubBuffer (cl_mem buffer,  
                        cl_mem_flags flags,  
                        cl_buffer_create_type buffer_create_type,  
                        const void *buffer_create_info, cl_int *errcode_ret)
```

Querying Platform Info and Devices [4.1, 4.2]

```
cl_int clGetPlatformIDs (cl_uint num_entries,  
                         cl_platform_id *platforms, cl_uint *num_platforms)
```

```
cl_int clGetPlatformInfo (cl_platform_id platform,  
    cl_platform_info param_name, size_t param_value_size,  
    void *param_value, size_t *param_value_size_ret)
```

param_name: CL_PLATFORM_{PROFILE, VERSION},
CL_PLATFORM_{NAME, VENDOR, EXTENSIONS}

```
cl_int clGetDeviceIDs (cl_platform_id platform,  
                      cl_device_type device_type, cl_uint num_entries,  
                      cl_device_id *devices, cl_uint *num_devices)
```

device_type: CL_DEVICE_TYPE_{CPU, GPU},
CL_DEVICE_TYPE_{ACCELERATOR, DEFAULT, ALL}

```
size_t buffer_row_pitch, size_t buffer_slice_pitch,  
size_t host_row_pitch, size_t host_slice_pitch,  
void *ptr, cl_uint num_events_in_wait_list,  
const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueWriteBufferRect (
```

cl_command_queue *command_queue*, cl_mem *buffer*,

cl_bool *blocking_write*, const size_t *buffer_origin*[3],

const size_t *host_origin*[3], const size_t *region*[3],

size_t *buffer_row_pitch*, size_t *buffer_slice_pitch*,

size_t *host_row_pitch*, size_t *host_slice_pitch*,

void **ptr*, cl_uint *num_events_in_wait_list*,

const cl_event **event_wait_list*, cl_event **event*)

```
size_t offset, size_t cb, cl_uint num_events_in_wait_list,
const cl_event *event_wait_list, cl_event *event,
cl_int *errcode_ret)
```

Map Buffer Objects [5.4.1-2]

```
cl_int clRetainMemObject (cl_mem memobj)
cl_int clReleaseMemObject (cl_mem memobj)
cl_int clSetMemObjectDestructorCallback (
    cl_mem memobj, void (CL_CALLBACK *pfn_not
        (cl_mem memobj, void *user_data),
    void *user_data)
```

OpenCL API 1.1 Quick Reference Card - Page 1

OpenCL (Open Computing Language) is a multi-vendor open standard for general-purpose parallel programming of heterogeneous systems that include CPUs, GPUs and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop computer systems and handheld devices.

[n.n.n] refers to the section in the API Specification available at www.khronos.org/opencl.

The OpenCL Runtime

Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (
    cl_context context, cl_device_id device,
    cl_command_queue_properties properties,
    cl_int *errcode_ret)
properties: CL_QUEUE_PROFILING_ENABLE,
CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE
cl_int clRetainCommandQueue (
    cl_command_queue command_queue)
cl_int clReleaseCommandQueue (
    cl_command_queue command_queue)
cl_int clGetCommandQueueInfo (
    cl_command_queue command_queue,
    cl_command_queue_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
param_name: CL_QUEUE_CONTEXT,
CL_QUEUE_DEVICE,
CL_QUEUE_REFERENCE_COUNT,
CL_QUEUE_PROPERTIES
```

Buffer Objects

Elements of a buffer object can be a scalar or vector data type or a user-defined structure. Elements are stored sequentially and are accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2.1]

```
cl_mem clCreateBuffer (cl_context context,
    cl_mem_flags flags, size_t size, void *host_ptr,
    cl_int *errcode_ret)
cl_mem clCreateSubBuffer (cl_mem buffer,
    cl_mem_flags flags,
    cl_buffer_create_type buffer_create_type,
    const void *buffer_create_info, cl_int *errcode_ret)
flags for clCreateBuffer and clCreateSubBuffer:
CL_MEM_READ_WRITE,
```

The O
The Oper
informati

Conte
cl conte
const
const
(
s
void *

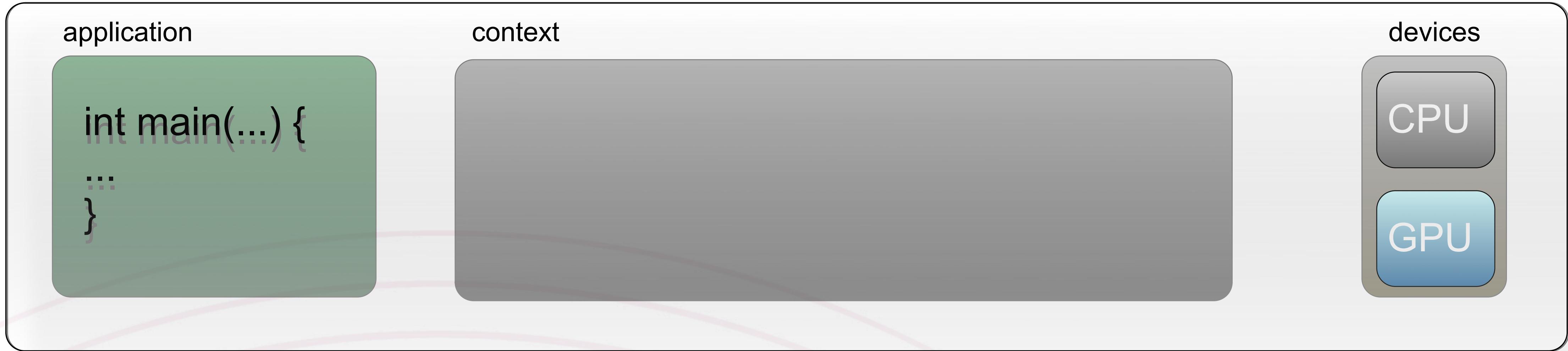
```
propert  
CL_CG  
CL_WC  
cl conte  
const  
cl dev  
(c  
vo  
void *  
propert  
cl int cl  
cl int cl  
cl int cl  
cl co  
void *  
param  
CL_CC  
Query  
cl_int cl  
cl pla  
cl_int cl  
cl pla  
void *  
param  
CL_PI  
cl_int cl  
cl de  
cl de  
device  
CL_DE
```

**cl_int clGetDeviceInfo (cl_device_id device,
cl_device_info param_name, size_t param_value_size,
void *param_value, size_t *param_value_size_ret)**

param_name: CL_DEVICE_TYPE,
CL_DEVICE_VENDOR_ID,
CL_DEVICE_MAX_COMPUTE_UNITS,
CL_DEVICE_MAX_WORK_ITEM_{DIMENSIONS, SIZES},
CL_DEVICE_MAX_WORK_GROUP_SIZE,
CL_DEVICE_{NATIVE, PREFERRED}_VECTOR_WIDTH_CHAR,
CL_DEVICE_{NATIVE, PREFERRED}_VECTOR_WIDTH_SHORT,
CL_DEVICE_{NATIVE, PREFERRED}_VECTOR_WIDTH_INT,
CL_DEVICE_{NATIVE, PREFERRED}_VECTOR_WIDTH_LONG,
CL_DEVICE_{NATIVE, PREFERRED}_VECTOR_WIDTH_FLOAT,
CL_DEVICE_{NATIVE, PREFERRED}_VECTOR_WIDTH_DOUBLE,
CL_DEVICE_{NATIVE, PREFERRED}_VECTOR_WIDTH_HALF,
CL_DEVICE_MAX_CLOCK_FREQUENCY,
CL_DEVICE_ADDRESS_BITS,
CL_DEVICE_MAX_MEM_ALLOC_SIZE,
CL_DEVICE_IMAGE_SUPPORT,
CL_DEVICE_MAX_{READ, WRITE}_IMAGE_ARGS,
CL_DEVICE_IMAGE3D_MAX_{WIDTH, HEIGHT}

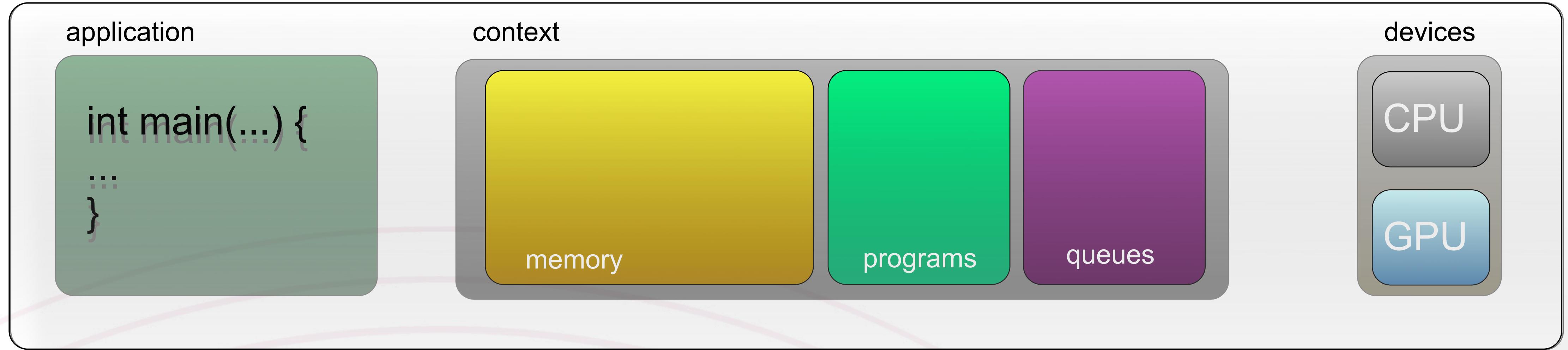


Application needs access to compute resources in system
Execution model defines the interaction between the host application and a device



Context encapsulates resources into a logical grouping

Think of this as a container of compute resources for your application



Memory, programs, and queues are owned by the context
Provides a container that localises allocations into a central store
Also, enables transparent sharing with OpenGL / OpenGL-ES / DirectX

OpenCL API 1.1 Quick Reference Card - Page 1

CPUs, GPUs and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop computer systems and handheld devices. [n.n.n] refers to the section in the API Specification available at www.khronos.org/opencl.

The OpenCL Runtime

Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (cl_context context, cl_device_id device, cl_command_queue_properties properties, cl_int *errcode_ret)  
properties: CL_QUEUE_PROFILING_ENABLE, CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE  
cl_int clRetainCommandQueue (cl_command_queue command_queue)  
cl_int clReleaseCommandQueue (cl_command_queue command_queue)  
cl_int clGetCommandQueueInfo (cl_command_queue command_queue, cl_command_queue_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)  
param_name: CL_QUEUE_CONTEXT, CL_QUEUE_DEVICE, CL_QUEUE_REFERENCE_COUNT, CL_QUEUE_PROPERTIES
```

Buffer Objects

Elements of a buffer object can be a scalar or vector data type or a user-defined structure. Elements are stored sequentially and are accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2.1]

```
cl_mem clCreateBuffer (cl_context context, cl_mem_flags flags, size_t size, void *host_ptr, cl_int *errcode_ret)  
cl_mem clCreateSubBuffer (cl_mem buffer, cl_mem_flags flags, cl_buffer_create_type buffer_create_type, const void *buffer_create_info, cl_int *errcode_ret)  
flags for clCreateBuffer and clCreateSubBuffer:  
CL_MEM_READ_WRITE, CL_MEM_WRITE_READ, CL_MEM_USE_ALLOC, COPY_HOST_PTR
```

Read, Write, Copy Buffer Objects [5.2.2]

```
cl_int clEnqueueReadBuffer (cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_read, size_t offset, size_t cb, void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)  
cl_int clEnqueueWriteBuffer (cl_command_queue command_queue, cl_mem buffer, cl_bool blocking_write, size_t offset, size_t cb, const void *ptr, cl_uint num_events_in_wait_list, const cl_event *event_wait_list, cl_event *event)
```

Program Objects

Create Program Objects [5.6.1]

```
cl_program clCreateProgramWithSource (cl_context context, cl_uint count, const char **strings, const size_t *lengths, cl_int *errcode_ret)  
cl_program clCreateProgramWithBinary (cl_context context, cl_uint num_devices, const cl_device_id *device_list, const size_t *lengths, const unsigned char **binaries, cl_int *binary_status, cl_int *errcode_ret)  
cl_int clRetainProgram (cl_program program)  
cl_int clReleaseProgram (cl_program program)
```

Contexts [4.3]

```
cl_context clCreateContext (const cl_context_properties *properties, cl_uint num_devices, const cl_device_id *devices, void (CL_CALLBACK *pfn_notify) (const char *errinfo, const void *private_info, size_t cb, void *user_data), void *user_data, cl_int *errcode_ret)
```

```
cl_int clGetPlatformInfo (cl_platform_id platform, cl_platform_info param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)
```

```
cl_int clGetDeviceInfo (cl_device_id device, cl_device_type device_type, size_t param_name_size, void *param_name, size_t param_value_size, void *param_value, size_t *param_value_size_ret)
```

```
cl_int clGetDeviceIDs (cl_device_type device_type, cl_device_id *device_id, size_t device_type_size, void *user_data, cl_int *errcode_ret)
```

The OpenCL Platform Layer

The OpenCL platform layer implements platform-specific features that allow information, and to create OpenCL contexts using one or more devices.

Contexts [4.3]

cl_context clCreateContext (

const cl_context_properties *properties, cl_uint num_devices,
const cl_device_id *devices, void (CL_CALLBACK *pfn_notify)
(const char *errinfo, const void *private_info,
size_t cb, void *user_data),
void *user_data, cl_int *errcode_ret)

properties: CL_CONTEXT_PLATFORM, CL_GL_CONTEXT_KHR,
CL_CGL_SHAREGROUP_KHR, CL_{EGL, GLX}_DISPLAY_KHR,
CL_WGL_HDC_KHR

cl_context clCreateContextFromType (

const cl_context_properties *properties,
cl_device_type device_type, void (CL_CALLBACK *pfn_notify)
(const char *errinfo, const void *private_info, size_t cb,
void *user_data),
void *user_data, cl_int *errcode_ret)

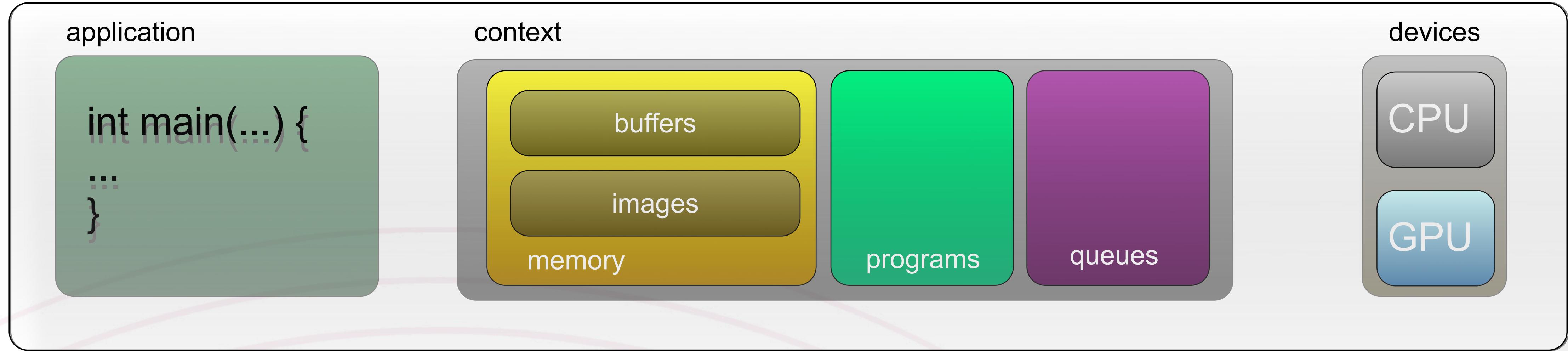
properties: See clCreateContext

cl_int clRetainContext (cl_context context)

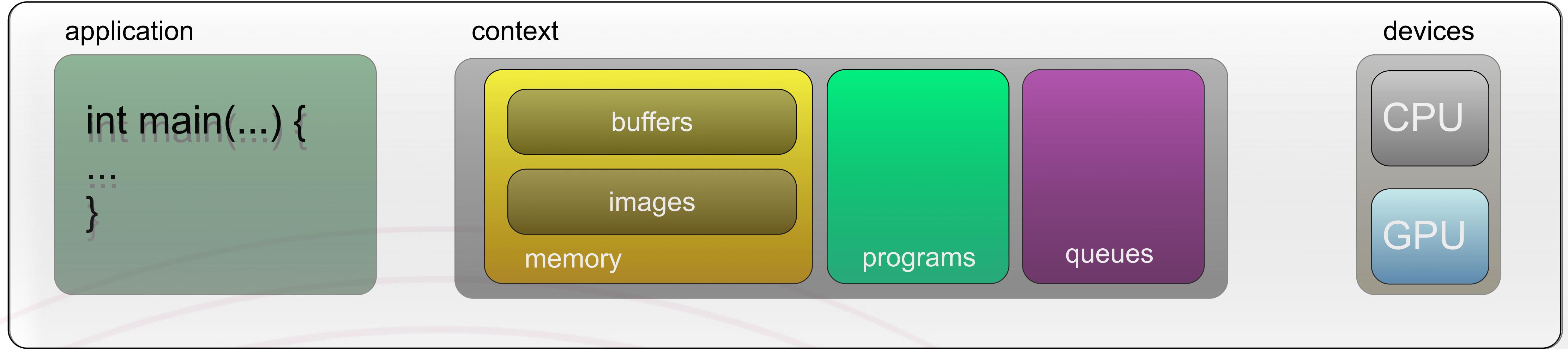
cl_int clReleaseContext (cl_context context)

cl_int clGetContextInfo (cl_context context,
cl_context_info param_name, size_t param_value_size,
void *param_value, size_t *param_value_size_ret)

©2010 Khronos Group - Rev. 0610

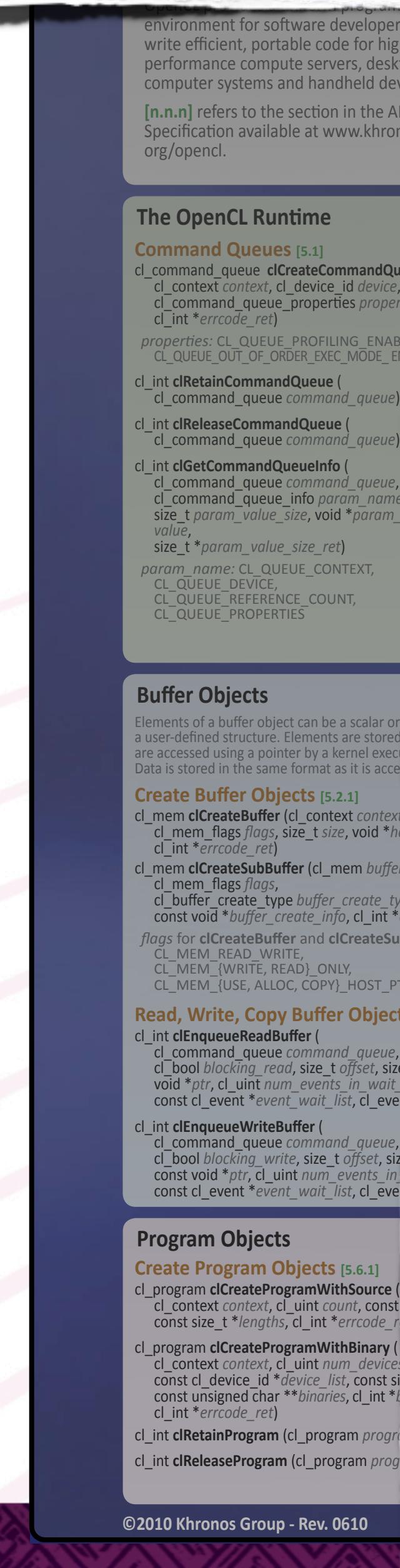


Memory allocations can be raw bytes or formatted images
Buffers are basically the same as a malloc in C99 -- contiguous allocation of bytes
Images provide a direct path to texture hardware and support sampling, filtering, etc.



Memory is accessible for all devices in context
However, synchronisation is **required** to get expected results!

OpenCL API 1.1 Quick Reference Card - Page 1



Buffer Objects

Elements of a buffer object can be a scalar or vector data type or a user-defined structure. Elements are stored sequentially and are accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2.1]

`cl_mem clCreateBuffer (cl_context context,
 cl_mem_flags flags, size_t size, void *host_ptr,
 cl_int *errcode_ret)`

`cl_mem clCreateSubBuffer (cl_mem buffer,
 cl_mem_flags flags,
 cl_buffer_create_type buffer_create_type,
 const void *buffer_create_info, cl_int *errcode_ret)`

flags for `clCreateBuffer` and `clCreateSubBuffer`:

`CL_MEM_READ_WRITE,
CL_MEM_{WRITE, READ}_ONLY,
CL_MEM_{USE, ALLOC, COPY}_HOST_PTR`

Read, Write, Copy Buffer Objects [5.2.2]

`cl_int clEnqueueReadBuffer (`
 `cl_command_queue command_queue, cl_mem buffer,
 cl_bool blocking_read, size_t offset, size_t cb,
 void *ptr, cl_uint num_events_in_wait_list,
 const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueWriteBuffer (`
 `cl_command_queue command_queue, cl_mem buffer,
 cl_bool blocking_write, size_t offset, size_t cb,
 const void *ptr, cl_uint num_events_in_wait_list,
 const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueReadBufferRect (`
 `cl_command_queue command_queue, cl_mem buffer,
 cl_bool blocking_read, const size_t buffer_origin[3],
 const size_t host_origin[3], const size_t region[3],
 size_t buffer_row_pitch, size_t buffer_slice_pitch,
 size_t host_row_pitch, size_t host_slice_pitch,
 void *ptr, cl_uint num_events_in_wait_list,
 const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueWriteBufferRect (`
 `cl_command_queue command_queue, cl_mem buffer,
 cl_bool blocking_write, const size_t buffer_origin[3],
 const size_t host_origin[3], const size_t region[3],
 size_t buffer_row_pitch, size_t buffer_slice_pitch,
 size_t host_row_pitch, size_t host_slice_pitch,
 void *ptr, cl_uint num_events_in_wait_list,
 const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueCopyBuffer (`
 `cl_command_queue command_queue,
 cl_mem src_buffer, cl_mem dst_buffer, size_t src_offset,
 size_t dst_offset, size_t cb,
 cl_uint num_events_in_wait_list,
 const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueCopyBufferRect (`
 `cl_command_queue command_queue,
 cl_mem src_buffer, cl_mem dst_buffer,
 const size_t src_origin[3], const size_t dst_origin[3],
 const size_t region[3], size_t src_row_pitch,
 size_t src_slice_pitch, size_t dst_row_pitch,
 size_t dst_slice_pitch, cl_uint num_events_in_wait_list,
 const cl_event *event_wait_list, cl_event *event)`

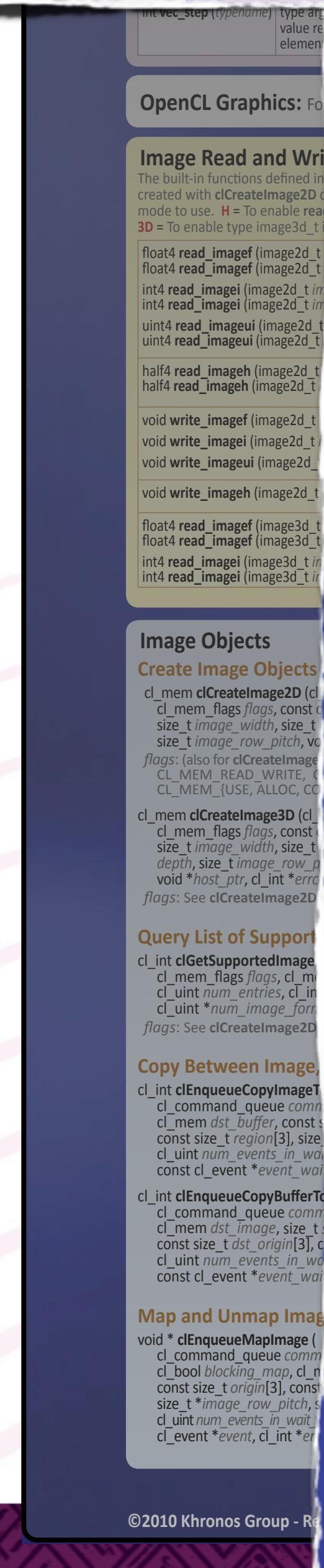


Image Objects

Create Image Objects [5.3.1]

`cl_mem clCreateImage2D (cl_context context,
cl_mem_flags flags, const cl_image_format *image_format,
size_t image_width, size_t image_height,
size_t image_row_pitch, void *host_ptr, cl_int *errcode_ret)`

flags: (also for `clCreateImage3D`, `clGetSupportedImageFormats`)
`CL_MEM_READ_WRITE`, `CL_MEM_{WRITE, READ}_ONLY`,
`CL_MEM_{USE, ALLOC, COPY}_HOST_PTR`

`cl_mem clCreateImage3D (cl_context context,
cl_mem_flags flags, const cl_image_format *image_format,
size_t image_width, size_t image_height, size_t image_depth,
size_t image_row_pitch, size_t image_slice_pitch,
void *host_ptr, cl_int *errcode_ret)`

flags: See `clCreateImage2D`

Query List of Supported Image Formats [5.3.2]

`cl_int clGetSupportedImageFormats (cl_context context,
cl_mem_flags flags, cl_mem_object_type image_type,
cl_uint num_entries, cl_image_format *image_formats,
cl_uint *num_image_formats)`

flags: See `clCreateImage2D`

Copy Between Image, Buffer Objects [5.3.4]

`cl_int clEnqueueCopyImageToBuffer (`

`cl_command_queue command_queue, cl_mem src_image,
cl_mem dst_buffer, const size_t src_origin[3],
const size_t region[3], size_t dst_offset,
cl_uint num_events_in_wait_list,
const cl_event *event wait_list, cl_event *event)`

Read, Write, Copy Image Objects [5.3.3]

`cl_int clEnqueueReadImage (`

`cl_command_queue command_queue, cl_mem image,
cl_bool blocking_read, const size_t origin[3],
const size_t region[3], size_t row_pitch,
size_t slice_pitch, void *ptr,
cl_uint num_events_in_wait_list,
const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueWriteImage (`

`cl_command_queue command_queue,
cl_mem image, cl_bool blocking_write,
const size_t origin[3], const size_t region[3],
size_t input_row_pitch, size_t input_slice_pitch,
const void *ptr, cl_uint num_events_in_wait_list,
const cl_event *event_wait_list, cl_event *event)`

`cl_int clEnqueueCopyImage (`

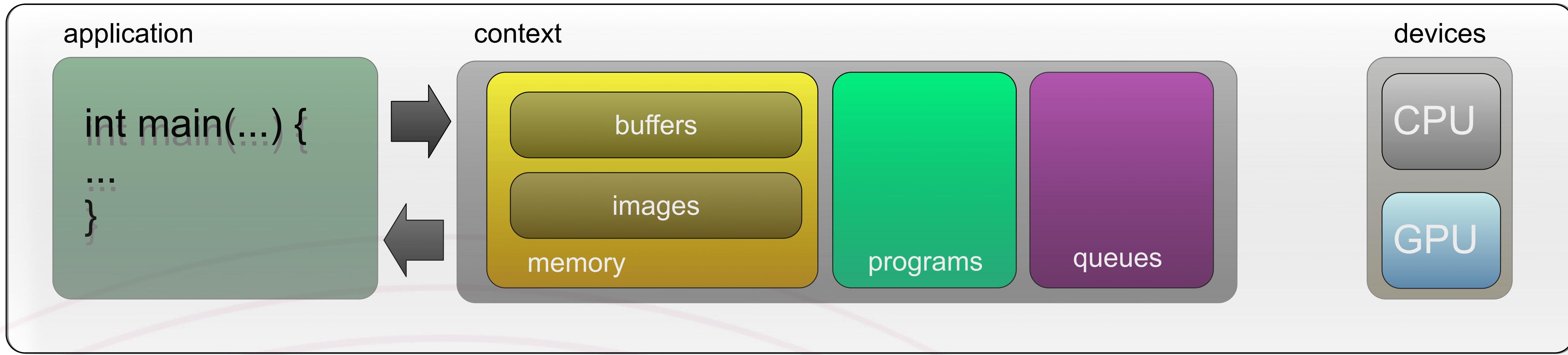
`cl_command_queue command_queue,
cl_mem src_image, cl_mem dst_image,
const size_t src_origin[3], const size_t dst_origin[3],
const size_t region[3], cl_uint num_events_in_wait_list,
const cl_event *event_wait_list, cl_event *event)`

Query Image Objects [5.3.6]

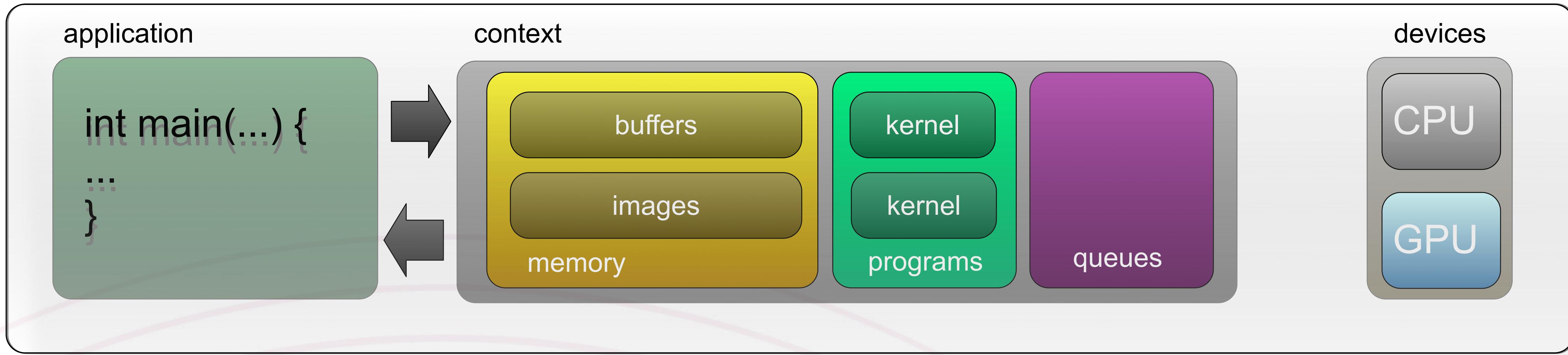
`cl_int clGetMemObjectInfo (cl_mem memobj,
cl_mem_info param_name, size_t param_value_size,
void *param_value, size_t *param_value_size_ret)`

param_name: `CL_MEM_{TYPE, FLAGS, SIZE, HOST_PTR},`
`CL_MEM_{MAP, REFERENCE}_COUNT,`
`CL_MEM_{CONTEXT, OFFSET},`
`CL_MEM_ASSOCIATED_MEMOBJECT`

`cl_int clGetImageInfo (cl_mem image`



Application uses context for moving data to/from host
All data transfers between host and device must be done explicitly!



Programs contain compiled kernel objects
Each program must be compiled for every type of device and/or instruction set

Program Objects

Create Program Objects [5.6.1]

```
cl_program clCreateProgramWithSource (
    cl_context context, cl_uint count, const char **strings,
    const size_t *lengths, cl_int *errcode_ret)
```

```
cl_program clCreateProgramWithBinary (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list, const size_t *lengths,
    const unsigned char **binaries, cl_int *binary_status,
    cl_int *errcode_ret)
```

```
cl_int clRetainProgram (cl_program program)
```

```
cl_int clReleaseProgram (cl_program program)
```

Build Program Executable [5.6.2]

```
cl_int clBuildProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, void (CL_CALLBACK*pfn_notify)
        (cl_program program, void *user_data),
    void *user_data)
```

Build Options [5.6.3]

Preprocessor: (-D processed in order listed in clBuildProgram)

-D name	-D name=definition	-I dir
---------	--------------------	--------

Optimization options:

-cl-opt-disable	-cl-strict-aliasing
-cl-mad-enable	-cl-no-signed-zeros
-cl-finite-math-only	-cl-fast-relaxed-math
-cl-unsafe-math-optimizations	

```
const cl_event *event_wait_list, cl_event *event)
cl_int clEnqueueWriteBuffer (
    cl_command_queue command_queue,
    cl_mem src_buffer, cl_mem dst_buffer,
    const size_t src_origin[3], const size_t dst_origin[3],
    const size_t region[3], size_t src_row_pitch,
    size_t src_slice_pitch, size_t dst_row_pitch,
    size_t dst_slice_pitch, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Program Objects

Create Program Objects [5.6.1]

```
cl_program clCreateProgramWithSource (
    cl_context context, cl_uint count, const char **strings,
    const size_t *lengths, cl_int *errcode_ret)
```

```
cl_program clCreateProgramWithBinary (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list, const size_t *lengths,
    const unsigned char **binaries, cl_int *binary_status,
    cl_int *errcode_ret)
```

```
cl_int clRetainProgram (cl_program program)
cl_int clReleaseProgram (cl_program program)
```

```
cl_command_queue command_queue,
    cl_mem src_buffer, cl_mem dst_buffer,
    const size_t src_origin[3], const size_t dst_origin[3],
    const size_t region[3], size_t src_row_pitch,
    size_t src_slice_pitch, size_t dst_row_pitch,
    size_t dst_slice_pitch, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Build Program Executable [5.6.2]

```
cl_int clBuildProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, void (CL_CALLBACK*pfn_notify)
        (cl_program program, void *user_data),
    void *user_data)
```

Build Options [5.6.3]

Preprocessor: (-D processed in order listed in clBuildProgram)

Optimization options:

```
cl_mem_info param_name, size_t param_value_size,
void *param_value, size_t *param_value_size_ret)
param_name: CL_MEM_{TYPE, FLAGS, SIZE, HOST_PTR},
CL_MEM_{MAP, REFERENCE}_COUNT, CL_MEM_OFFSET,
CL_MEM_CONTEXT, CL_MEM_ASSOCIATED_MEMOBJECT
```

Math Intrinsics:

-cl-single-precision-constant -cl-denorms-are-zero

Warning request/suppress:

-w -Werror

Control OpenCL C language version:

-cl-std=CL1.1 // OpenCL 1.1 specification.

Query Program Objects [5.6.5]

```
cl_int clGetProgramInfo (cl_program program,
    cl_program_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
param_name: CL_PROGRAM_{REFERENCE_COUNT},
CL_PROGRAM_{CONTEXT, NUM_DEVICES, DEVICES},
CL_PROGRAM_{SOURCE, BINARY, SIZES, BINARIES}
```

(Program Objects Continue >)

<code>size_t param_value_size, void *param_value,</code>																																																			
<code>param_name: CL_PROGRAM_BUILD_(STATUS, OPTIONS, LOG)</code>																																																			
Unload the OpenCL Compiler [5.6.4]																																																			
<code>cl_int clUnloadCompiler (void)</code>																																																			
Supported Data Types																																																			
Built-in Scalar Data Types [6.1.1]																																																			
<table border="1"><thead><tr><th>OpenCL Type</th><th>API Type</th><th>Description</th></tr></thead><tbody><tr><td>bool</td><td>--</td><td>true (1) or false (0)</td></tr><tr><td>char</td><td>cl_char</td><td>8-bit signed</td></tr><tr><td>unsigned char, uchar</td><td>cl_uchar</td><td>8-bit unsigned</td></tr><tr><td>short</td><td>cl_short</td><td>16-bit signed</td></tr><tr><td>unsigned short, ushort</td><td>cl_ushort</td><td>16-bit unsigned</td></tr><tr><td>int</td><td>cl_int</td><td>32-bit signed</td></tr><tr><td>unsigned int, uint</td><td>cl_uint</td><td>32-bit unsigned</td></tr><tr><td>long</td><td>cl_long</td><td>64-bit signed</td></tr><tr><td>unsigned long, ulong</td><td>cl_ulong</td><td>64-bit unsigned</td></tr><tr><td>float</td><td>cl_float</td><td>32-bit float</td></tr><tr><td>half</td><td>cl_half</td><td>16-bit float (for storage only)</td></tr><tr><td>size_t</td><td>--</td><td>32- or 64-bit unsigned integer</td></tr><tr><td>ptrdiff_t</td><td>--</td><td>32- or 64-bit signed integer</td></tr><tr><td>intptr_t</td><td>--</td><td>signed integer</td></tr><tr><td>uintptr_t</td><td>--</td><td>unsigned integer</td></tr><tr><td>void</td><td>void</td><td>void</td></tr></tbody></table>	OpenCL Type	API Type	Description	bool	--	true (1) or false (0)	char	cl_char	8-bit signed	unsigned char, uchar	cl_uchar	8-bit unsigned	short	cl_short	16-bit signed	unsigned short, ushort	cl_ushort	16-bit unsigned	int	cl_int	32-bit signed	unsigned int, uint	cl_uint	32-bit unsigned	long	cl_long	64-bit signed	unsigned long, ulong	cl_ulong	64-bit unsigned	float	cl_float	32-bit float	half	cl_half	16-bit float (for storage only)	size_t	--	32- or 64-bit unsigned integer	ptrdiff_t	--	32- or 64-bit signed integer	intptr_t	--	signed integer	uintptr_t	--	unsigned integer	void	void	void
OpenCL Type	API Type	Description																																																	
bool	--	true (1) or false (0)																																																	
char	cl_char	8-bit signed																																																	
unsigned char, uchar	cl_uchar	8-bit unsigned																																																	
short	cl_short	16-bit signed																																																	
unsigned short, ushort	cl_ushort	16-bit unsigned																																																	
int	cl_int	32-bit signed																																																	
unsigned int, uint	cl_uint	32-bit unsigned																																																	
long	cl_long	64-bit signed																																																	
unsigned long, ulong	cl_ulong	64-bit unsigned																																																	
float	cl_float	32-bit float																																																	
half	cl_half	16-bit float (for storage only)																																																	
size_t	--	32- or 64-bit unsigned integer																																																	
ptrdiff_t	--	32- or 64-bit signed integer																																																	
intptr_t	--	signed integer																																																	
uintptr_t	--	unsigned integer																																																	
void	void	void																																																	
Built-in Vector Data Types [6.1.2]																																																			
<table border="1"><thead><tr><th>OpenCL Type</th><th>API Type</th><th>Description</th></tr></thead><tbody><tr><td>charn</td><td>cl_charn</td><td>8-bit signed</td></tr><tr><td>ucharn</td><td>cl_ucharn</td><td>8-bit unsigned</td></tr><tr><td>shortn</td><td>cl_shortn</td><td>16-bit signed</td></tr><tr><td>ushortn</td><td>cl_ushortn</td><td>16-bit unsigned</td></tr><tr><td>intn</td><td>cl_intn</td><td>32-bit signed</td></tr><tr><td>uintn</td><td>cl_uintn</td><td>32-bit unsigned</td></tr><tr><td>longn</td><td>cl_longn</td><td>64-bit signed</td></tr><tr><td>ulongn</td><td>cl_ulongn</td><td>64-bit unsigned</td></tr><tr><td>floatn</td><td>cl_floatn</td><td>32-bit float</td></tr></tbody></table>	OpenCL Type	API Type	Description	charn	cl_charn	8-bit signed	ucharn	cl_ucharn	8-bit unsigned	shortn	cl_shortn	16-bit signed	ushortn	cl_ushortn	16-bit unsigned	intn	cl_intn	32-bit signed	uintn	cl_uintn	32-bit unsigned	longn	cl_longn	64-bit signed	ulongn	cl_ulongn	64-bit unsigned	floatn	cl_floatn	32-bit float																					
OpenCL Type	API Type	Description																																																	
charn	cl_charn	8-bit signed																																																	
ucharn	cl_ucharn	8-bit unsigned																																																	
shortn	cl_shortn	16-bit signed																																																	
ushortn	cl_ushortn	16-bit unsigned																																																	
intn	cl_intn	32-bit signed																																																	
uintn	cl_uintn	32-bit unsigned																																																	
longn	cl_longn	64-bit signed																																																	
ulongn	cl_ulongn	64-bit unsigned																																																	
floatn	cl_floatn	32-bit float																																																	
Other Built-in Data Types [6.1.3]																																																			
<table border="1"><thead><tr><th>OpenCL Type</th><th>Description</th></tr></thead><tbody><tr><td>image2d_t</td><td>2D image handle</td></tr><tr><td>image3d_t</td><td>3D image handle</td></tr><tr><td>sampler_t</td><td>sampler handle</td></tr><tr><td>event_t</td><td>event handle</td></tr></tbody></table>	OpenCL Type	Description	image2d_t	2D image handle	image3d_t	3D image handle	sampler_t	sampler handle	event_t	event handle																																									
OpenCL Type	Description																																																		
image2d_t	2D image handle																																																		
image3d_t	3D image handle																																																		
sampler_t	sampler handle																																																		
event_t	event handle																																																		
Reserved Data Types [6.1.4]																																																			
<table border="1"><thead><tr><th>OpenCL Type</th><th>Description</th></tr></thead><tbody><tr><td>booln</td><td>boolean vector</td></tr><tr><td>double, doublen</td><td>OPTIONAL 64-bit float, vector</td></tr><tr><td>halfn</td><td>16-bit, vector</td></tr><tr><td>quadn</td><td>128-bit float, vector</td></tr><tr><td>complex half, complex halfn</td><td>16-bit complex, vector</td></tr><tr><td>imaginary half, imaginary halfn</td><td>16-bit complex, vector</td></tr><tr><td>complex float, complex floatn</td><td>32-bit complex, vector</td></tr><tr><td>imaginary float, imaginary floatn</td><td>32-bit complex, vector</td></tr><tr><td>complex double, complex doublen</td><td>64-bit complex, vector</td></tr><tr><td>imaginary double, imaginary doublen</td><td>64-bit complex, vector</td></tr><tr><td>complex quad, complex quadn</td><td>128-bit complex, vector</td></tr><tr><td>imaginary quad, imaginary quadn</td><td>128-bit complex, vector</td></tr><tr><td>floatnxm</td><td>$n \times m$ matrix of 32-bit floats</td></tr><tr><td>doublenxm</td><td>$n \times m$ matrix of 64-bit floats</td></tr><tr><td>long double, long doublen</td><td>64 - 128-bit float, vector</td></tr><tr><td>long long, long longnb</td><td>128-bit signed</td></tr><tr><td>unsigned long long, ulong long,</td><td>128-bit unsigned</td></tr><tr><td>ulong longn</td><td>128-bit unsigned</td></tr></tbody></table>	OpenCL Type	Description	booln	boolean vector	double, doublen	OPTIONAL 64-bit float, vector	halfn	16-bit, vector	quadn	128-bit float, vector	complex half, complex halfn	16-bit complex, vector	imaginary half, imaginary halfn	16-bit complex, vector	complex float, complex floatn	32-bit complex, vector	imaginary float, imaginary floatn	32-bit complex, vector	complex double, complex doublen	64-bit complex, vector	imaginary double, imaginary doublen	64-bit complex, vector	complex quad, complex quadn	128-bit complex, vector	imaginary quad, imaginary quadn	128-bit complex, vector	floatnxm	$n \times m$ matrix of 32-bit floats	doublenxm	$n \times m$ matrix of 64-bit floats	long double, long doublen	64 - 128-bit float, vector	long long, long longnb	128-bit signed	unsigned long long, ulong long,	128-bit unsigned	ulong longn	128-bit unsigned													
OpenCL Type	Description																																																		
booln	boolean vector																																																		
double, doublen	OPTIONAL 64-bit float, vector																																																		
halfn	16-bit, vector																																																		
quadn	128-bit float, vector																																																		
complex half, complex halfn	16-bit complex, vector																																																		
imaginary half, imaginary halfn	16-bit complex, vector																																																		
complex float, complex floatn	32-bit complex, vector																																																		
imaginary float, imaginary floatn	32-bit complex, vector																																																		
complex double, complex doublen	64-bit complex, vector																																																		
imaginary double, imaginary doublen	64-bit complex, vector																																																		
complex quad, complex quadn	128-bit complex, vector																																																		
imaginary quad, imaginary quadn	128-bit complex, vector																																																		
floatnxm	$n \times m$ matrix of 32-bit floats																																																		
doublenxm	$n \times m$ matrix of 64-bit floats																																																		
long double, long doublen	64 - 128-bit float, vector																																																		
long long, long longnb	128-bit signed																																																		
unsigned long long, ulong long,	128-bit unsigned																																																		
ulong longn	128-bit unsigned																																																		
Conversions & Type Casting Example																																																			
<code>T a = (T)b; // Scalar to scalar, or scalar to vector T a = convert_T(b); T a = convert_T_R(b); T a = as_T(b);</code>																																																			
Operators [6.3]																																																			
These operators behave similarly as in C99 except operands may include vector types when possible																																																			
<code>+ - * % / -- ++ == != ~ ^ > < >= <= ! & ? : >> << , = op= sizeof</code>																																																			

Kernel and Event Objects

Create Kernel Objects [5.7.1]

`cl_kernel clCreateKernel (cl_program program,
const char *kernel_name, cl_int *errcode_ret)`

`cl_int clCreateKernelsInProgram (cl_program program,
cl_uint num_kernels, cl_kernel *kernels,
cl_uint *num_kernels_ret)`

`cl_int clRetainKernel (cl_kernel kernel)`

`cl_int clReleaseKernel (cl_kernel kernel)`

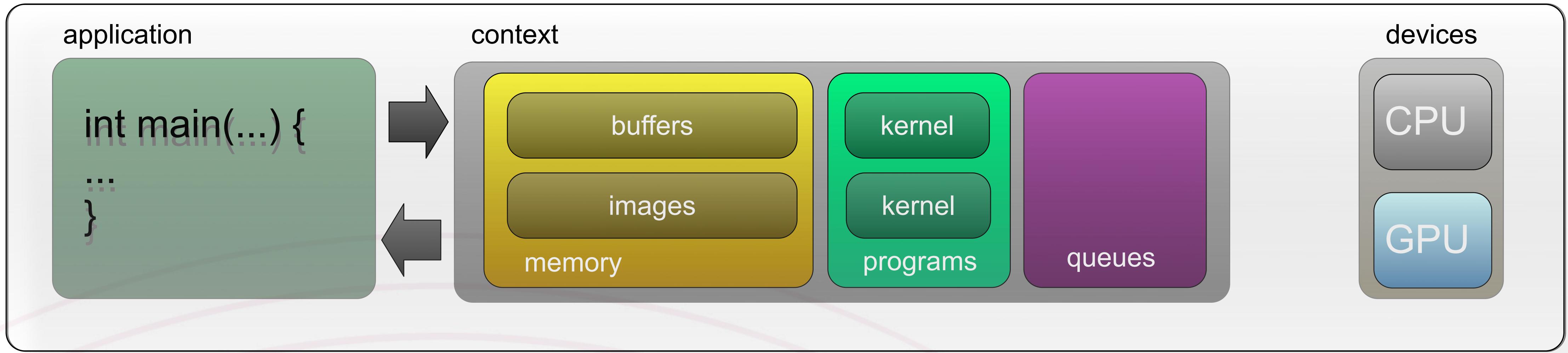
Kernel Args. & Object Queries [5.7.2, 5.7.3]

`cl_int clSetKernelArg (cl_kernel kernel, cl_uint arg_index,
size_t arg_size, const void *arg_value)`

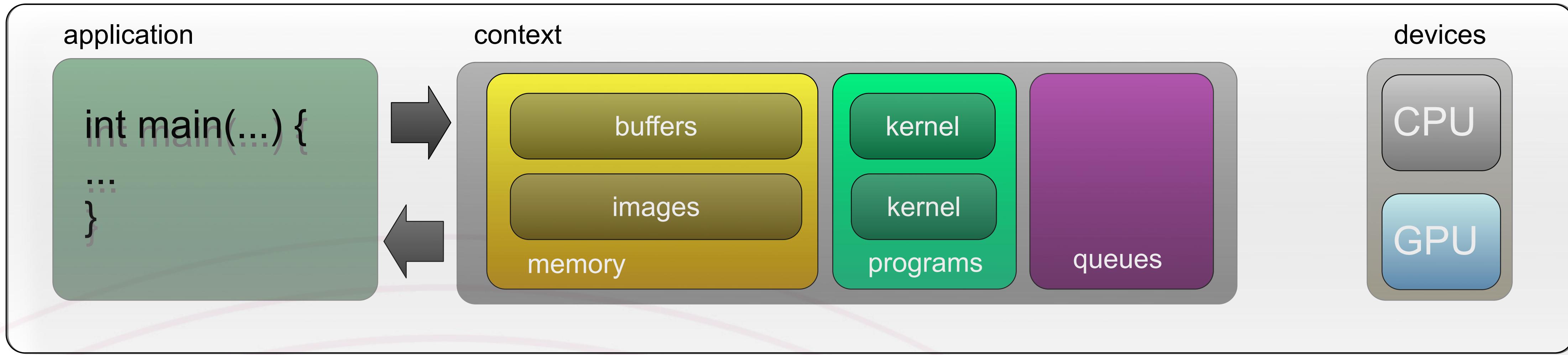
`cl_int clGetKernelInfo (cl_kernel kernel,
cl_kernel_info param_name, size_t param_value_size,
void *param_value, size_t *param_value_size_ret)`

`param_name: CL_KERNEL_FUNCTION_NAME,
CL_KERNEL_NUM_ARGS, CL_KERNEL_REFERENCE_COUNT,
CL_KERNEL_CONTEXT, CL_KERNEL_PROGRAM`

`cl_int clGetKernelWorkGroupInfo (`



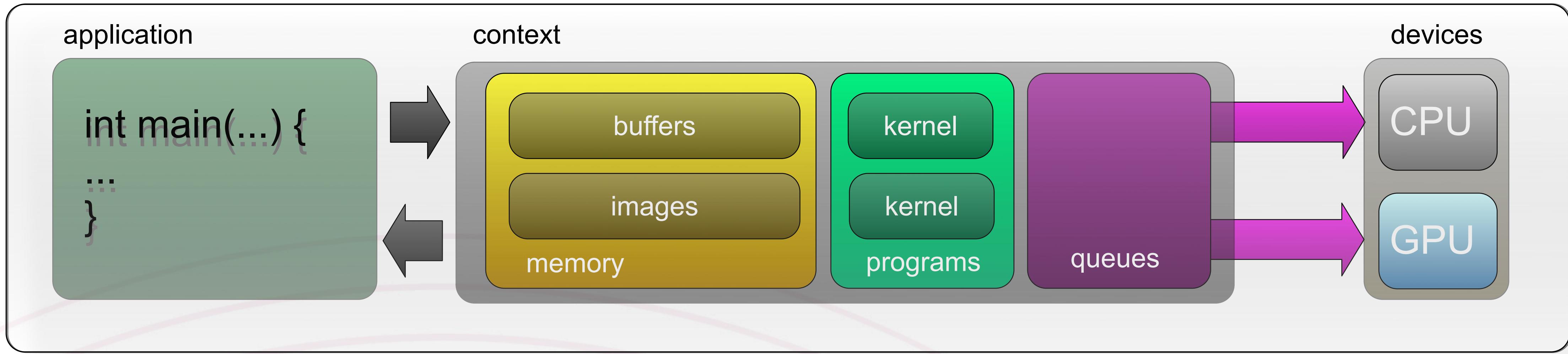
Command queues provide asynchronous execution
Enqueue commands onto a command queue to do work (sometime later)



Queues can be processed in-order or out-of-order

In-order queues are default -- commands run when preceding entries are complete

Out-of-order queues require explicit synchronisation via events and wait-lists



Queues are directly associated with a specific device
Submit commands to the associated queue to do work on a particular device

CPU, GPUs and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance computer servers, desktop computer systems and handheld devices.

[n.n] refers to the section in the API Specification available at www.khronos.org/opencl.

The OpenCL Runtime

Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (
    cl_context context, cl_device_id device,
    cl_command_queue_properties properties,
    cl_int *errcode_ret)
properties: CL_QUEUE_PROFILING_ENABLE,
CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE
```

```
cl_int clRetainCommandQueue (
    cl_command_queue command_queue)
```

```
cl_int clReleaseCommandQueue (
    cl_command_queue command_queue)
```

```
cl_int clGetCommandQueueInfo (
    cl_command_queue command_queue,
    cl_command_queue_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size)
param_name: CL_QUEUE_CONTEXT,
CL_QUEUE_DEVICE,
CL_QUEUE_REFERENCE_COUNT,
CL_QUEUE_PROPERTIES
```

Buffer Objects

Elements of a buffer object can be a scalar or vector data type or a user-defined structure. Elements are stored sequentially and are accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2.1]

```
cl_mem clCreateBuffer (cl_context context,
    cl_mem_flags flags, size_t size, void *host_ptr,
    cl_int *errcode_ret)
```

```
cl_mem clCreateSubBuffer (cl_mem buffer,
    cl_mem_flags flags,
    cl_buffer_create_type buffer_create_type,
    const void *buffer_create_info, cl_int *errcode_ret)
flags for clCreateBuffer and clCreateSubBuffer:
CL_MEM_READ_WRITE,
CL_MEM_WRITE_READ,
CL_MEM_USE_ALLOC_HOST_PTR
```

Read, Write, Copy Buffer Objects [5.2.2]

```
cl_int clEnqueueReadBuffer (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_read, size_t offset, size_t cb,
    void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueWriteBuffer (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_write, size_t offset, size_t cb,
    const void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Program Objects

Create Program Objects [5.6.1]

```
cl_program clCreateProgramWithSource (
    cl_context context, cl_uint count, const char **strings,
    const size_t *lengths, cl_int *errcode_ret)
```

```
cl_program clCreateProgramWithBinary (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list, const size_t *lengths,
    const unsigned char **binaries, cl_int *binary_status,
    cl_int *errcode_ret)
```

```
cl_int clRetainProgram (cl_program program)
```

```
cl_int clReleaseProgram (cl_program program)
```

Build Program Executable [5.6.2]

```
cl_int clBuildProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, void (CL_CALLBACK *pfn_notify) (cl_program program, void *user_data),
    void *user_data)
```

Build Options [5.6.3]

Preprocessor: (-D processed in order listed in clBuildProgram)

- D name
- D name=definition
- I dir

Optimization options:

- cl-opt-disable
- cl-mad-enable
- cl-finite-math-only
- cl-unsafe-math-optimizations
- cl-strict-aliasing
- cl-no-signed-zeros
- cl-fast-relaxed-math

The OpenCL Runtime

Command Queues [5.1]

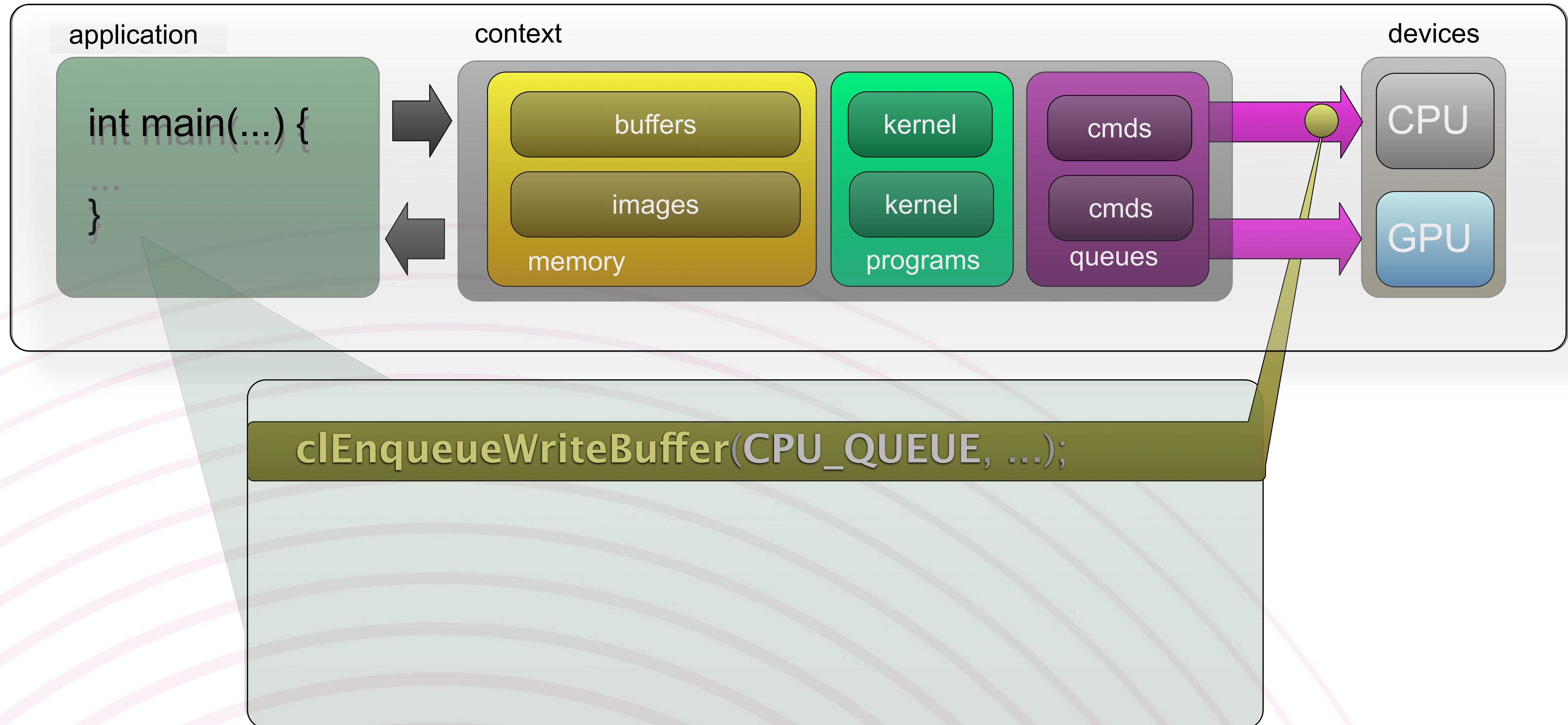
cl_command_queue clCreateCommandQueue (
cl_context context, cl_device_id device,
cl_command_queue_properties properties,
cl_int *errcode_ret)

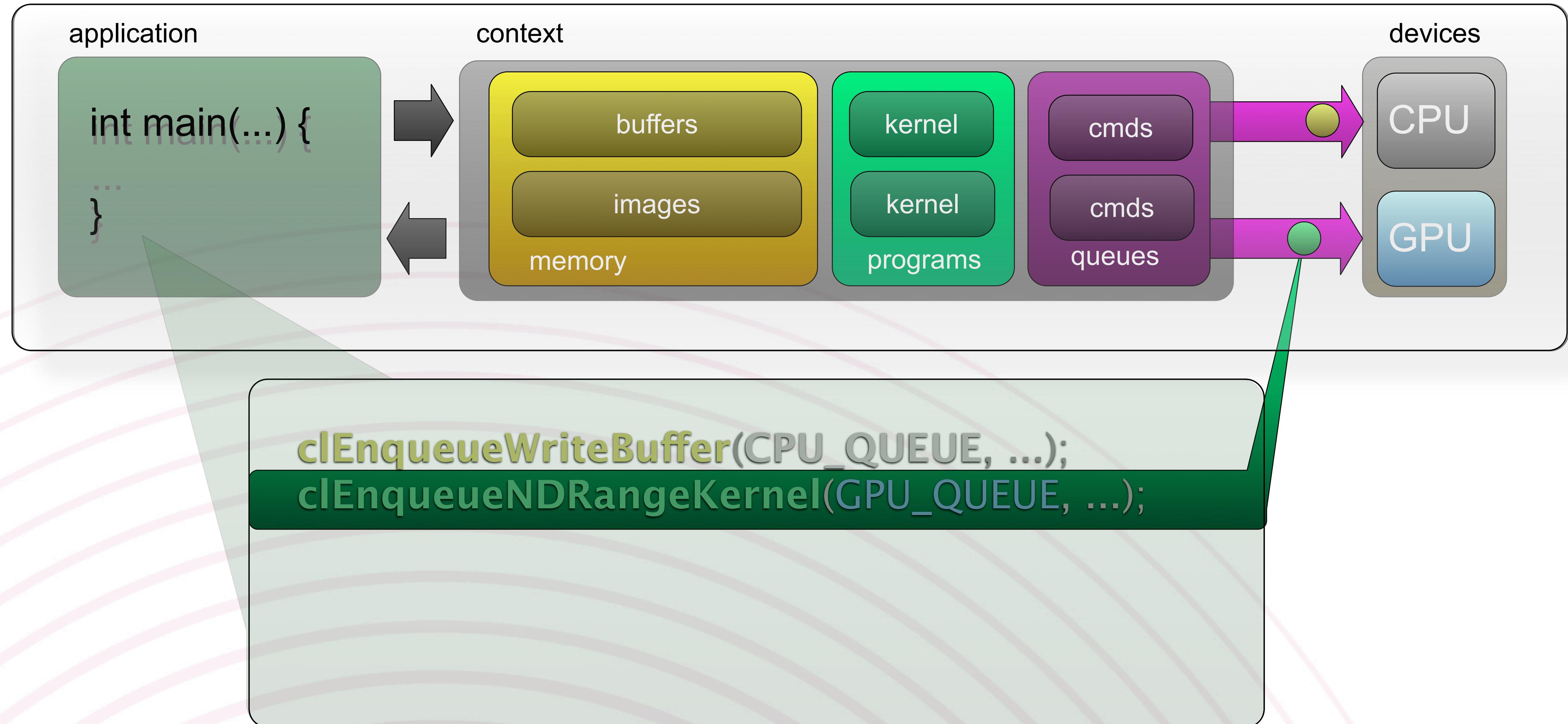
properties: CL_QUEUE_PROFILING_ENABLE,
CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE

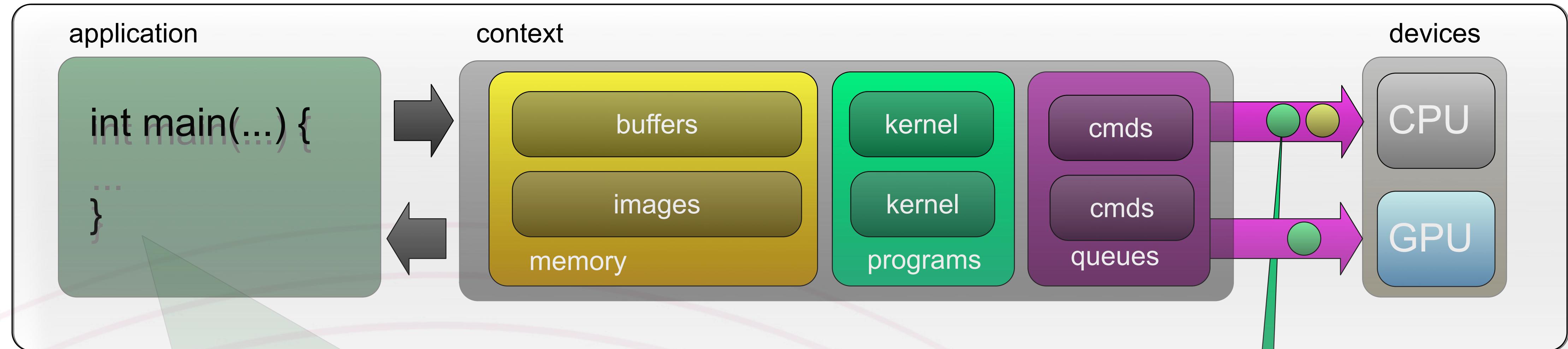
cl_int clRetainCommandQueue (
cl_command_queue command_queue)

cl_int clReleaseCommandQueue (
cl_command_queue command_queue)

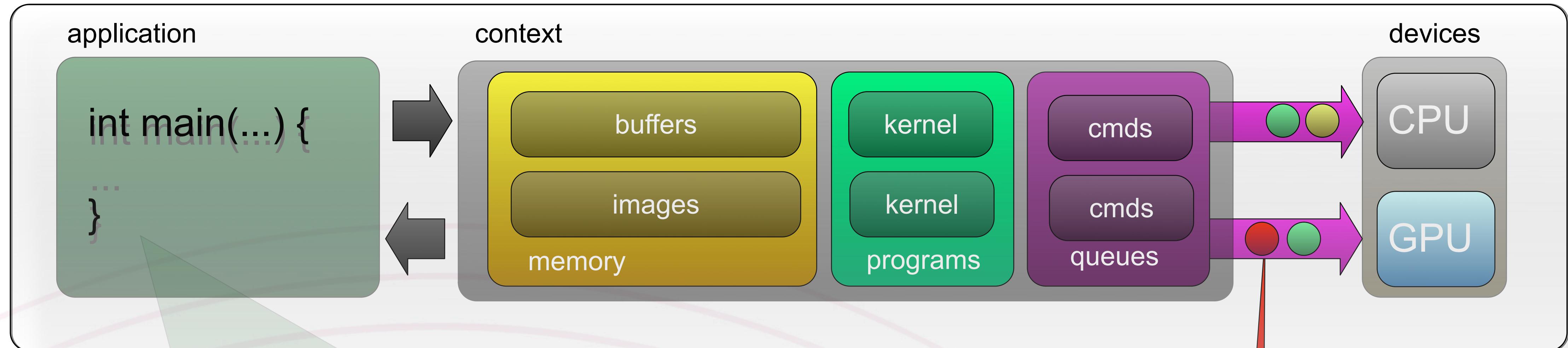
cl_int clGetCommandQueueInfo (
cl_command_queue command_queue,
cl_command_queue_info param_name,



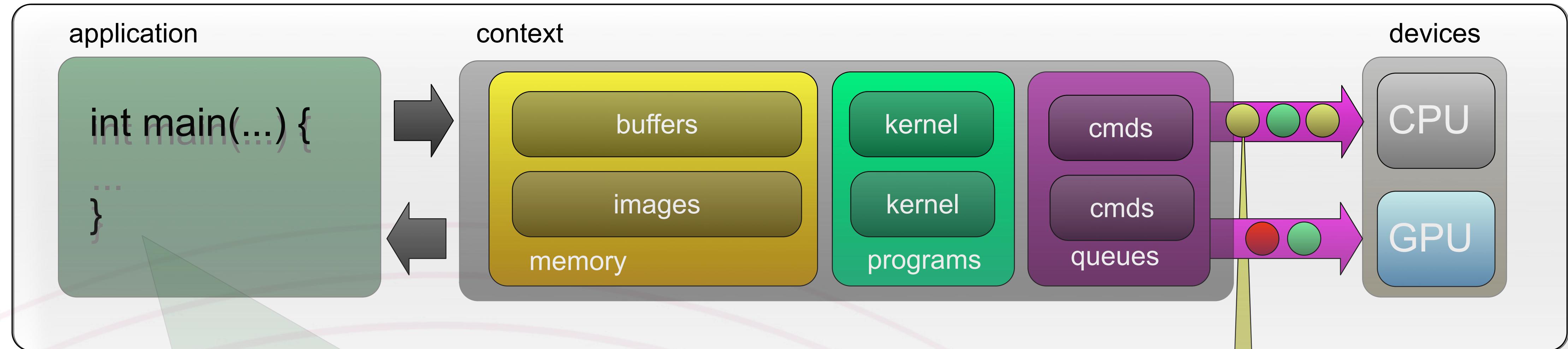




```
clEnqueueWriteBuffer(CPU_QUEUE, ...);  
clEnqueueNDRangeKernel(GPU_QUEUE, ...);  
clEnqueueNDRangeKernel(CPU_QUEUE, ...);
```



```
clEnqueueWriteBuffer(CPU_QUEUE, ...);  
clEnqueueNDRangeKernel(GPU_QUEUE, ...);  
clEnqueueNDRangeKernel(CPU_QUEUE, ...);  
clFinish(GPU_QUEUE, ...);
```



A callout box contains the following OpenCL API calls:

```
clEnqueueWriteBuffer(CPU_QUEUE, ...);  
clEnqueueNDRangeKernel(GPU_QUEUE, ...);  
clEnqueueNDRangeKernel(CPU_QUEUE, ...);  
clFinish(GPU_QUEUE, ...);  
clEnqueueWriteBuffer(CPU_QUEUE, ...);
```

Work Distribution

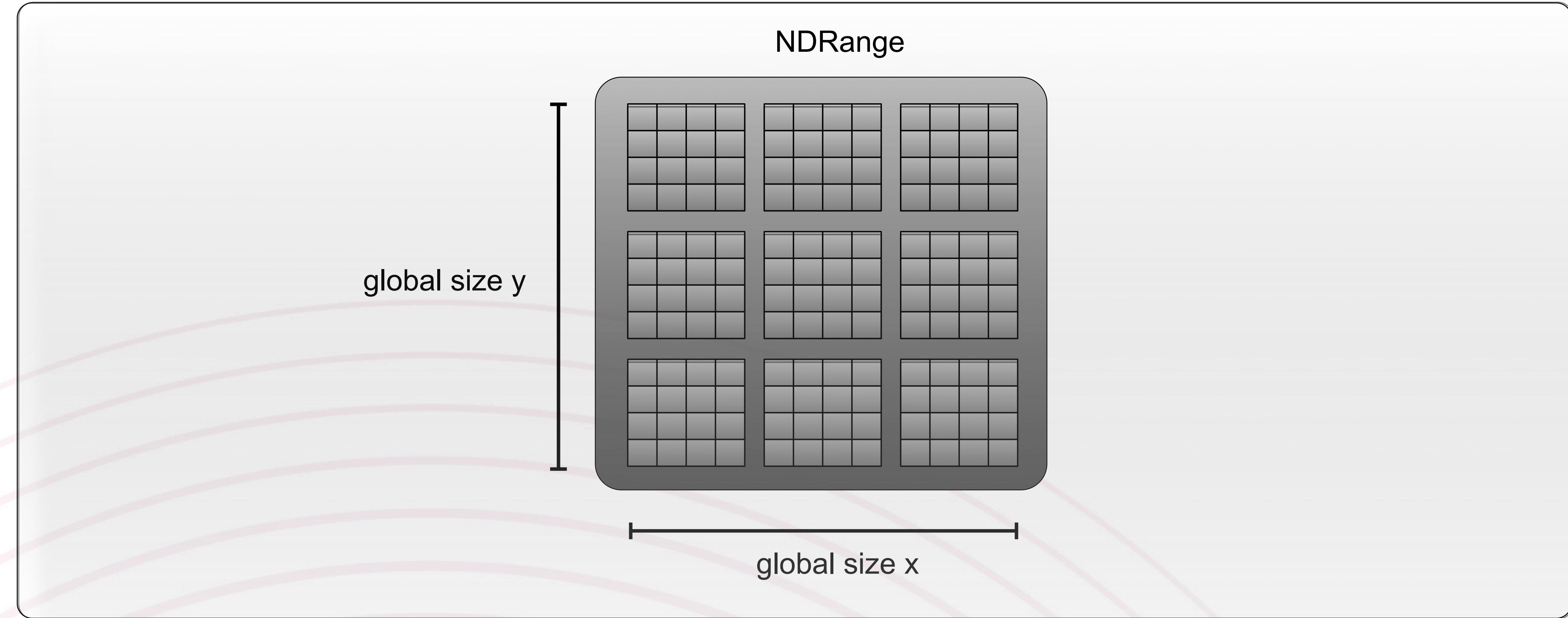
Logical groupings over a domain

- Kernels are executed across a domain of work-items
- Work-items are logically organised into work-groups
- Work-groups are executed in parallel

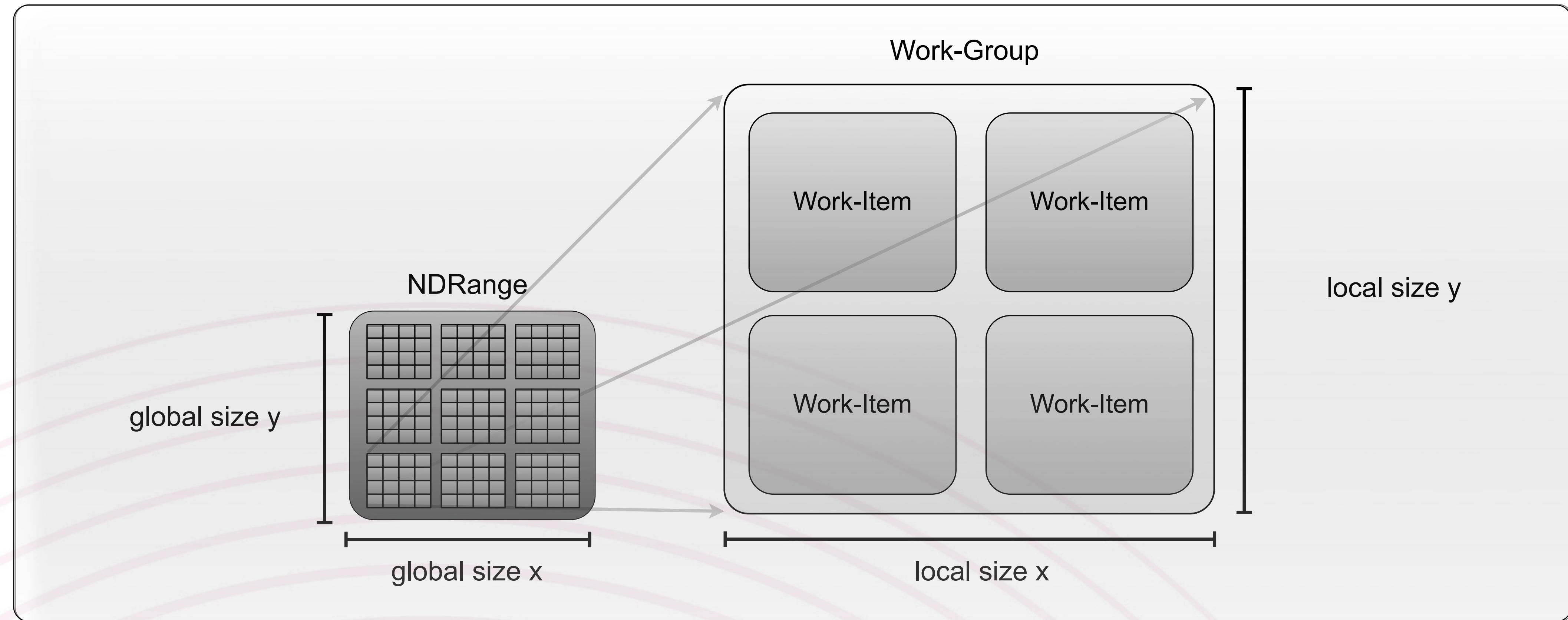
Work Distribution

Logical groupings over a domain

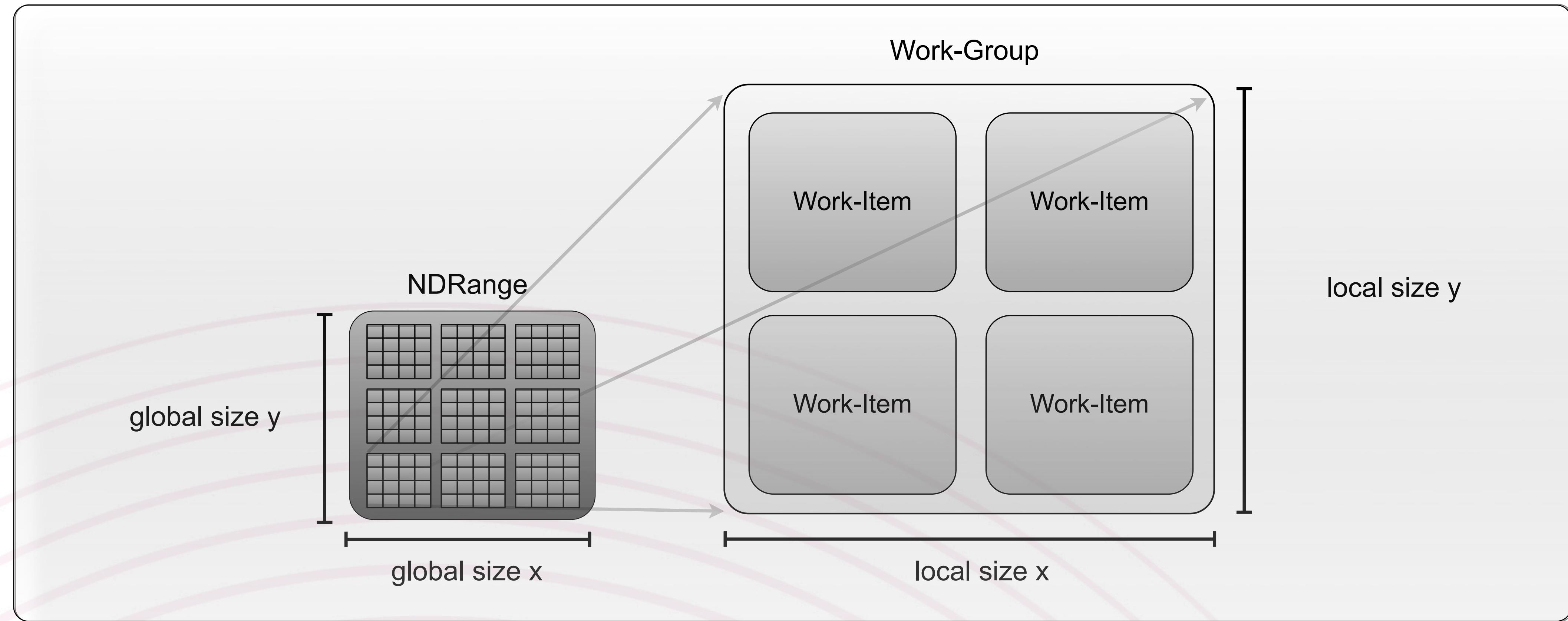
- Global dimensions define range of computation
- Local dimensions define size of work-group
- Only work-items in a work-group can communicate



Host application invokes a kernel over an index space
Index space is an N-dimensional range (where N is 1, 2, or 3)



Global range is executed over local work-groups
Each work-group has a collection of work-items with a unique id



Work-items share resources in a work-group

Mitigates communication costs and allows group-wise synchronisation

```
_kernel void square(  
    _global float* input, _global float* output)  
{  
    size_t i = get_global_id(0);  
    output[i] = input[i] * input[i];  
}
```



Built-in methods provide access to index space

Use the **get_global_id()** built-in for globally unique addresses

Use the **get_group_id()** for the logical group id spanning the ND-range

Use the **get_local_id()** for the local work-item address within a work-group

OpenCL API 1.1 Quick Reference Card - Page 3

<code>OPENCL_VERSION</code>	Integer version number
<code>_CL_VERSION_1_0</code>	Substitutes integer 100 for version 1.0
<code>_CL_VERSION_1_1</code>	Substitutes integer 110 for version 1.1
<code>_ENDIAN_LITTLE</code>	1 if device is little endian
<code>_kernel_exec(x, type)</code>	Same as: <code>_kernel_attribute_(work_group_size_hint(x, 1, 1))\n_attribute_(vec_type_hint(type))</code>
<code>_IMAGE_SUPPORT</code>	1 if images are supported
<code>_FAST_RELAXED_MATH</code>	1 if <code>-cl-fast-relaxed-math</code> optimization option is specified

<code>MAXFLOAT</code>	The values of the following symbolic constants are type float and are accurate within the precision of a single precision floating-point number.
<code>HUGE_VALF</code>	Positive float expression, evaluates to +infinity. Used as error value.

<code>INFINITY</code>	to +infinity. Used as error value. <small>OPTIONAL</small>
<code>NAN</code>	Constant float expression, positive or unsigned infinity.
<code>M_E_F</code>	Value of e
<code>M_LOG2E_F</code>	Value of log2e
<code>M_LOG10E_F</code>	Value of log10e
<code>M_LN10_F</code>	Value of log10
<code>M_PI_F</code>	Value of pi
<code>M_PI_2_F</code>	Value of pi / 2
<code>M_PI_4_F</code>	Value of pi / 4
<code>M_1_PI_F</code>	Value of 1 / pi
<code>M_2_PI_F</code>	Value of 2 / pi
<code>M_SQRTPI_F</code>	Value of 2 / sqrt(pi)
<code>M_SQRT2_F</code>	Value of sqrt(2)
<code>M_SQRT1_2_F</code>	Value of 1 / sqrt(2)

Work-Item Built-in Functions [6.11.1] D is dimension index.

<code>uint get_work_dim ()</code>	Num. of dimensions in use
<code>size_t get_global_size (uint D)</code>	Num. of global work-items
<code>size_t get_global_id (uint D)</code>	Global work-item ID value
<code>size_t get_local_size (uint D)</code>	Num. of local work-items

<code>size_t get_local_id (uint D)</code>	Local work-item ID
<code>size_t get_num_groups (uint D)</code>	Num. of work-groups
<code>size_t get_group_id (uint D)</code>	Returns the work-group ID
<code>size_t get_global_offset (uint D)</code>	Returns global offset

<small>x is type float or double (or optionally int, uint, and ulong) and Q is scalar when T is scalar. Q is qualifier _global_, _local_, or _private_. HN indicates that Half and Native variants are available by prepending "half_" or "native_" to function name. Prototypes shown in purple are half_ and native_ only. Optional extensions enable double, doublen, half, and halfn types.</small>	<small>abs(x)</small>	<small>Absolute value.</small>	<small>frexp (T x)</small>	<small>Decompose a floating-point number.</small>
	<code>T fdim (T x, T y)</code>	"Positive difference" between x and y	<code>float nan (uintn nancode)</code>	Quiet NaN
	<code>T floor (T)</code>	Round to integer toward -infinity	<code>floatn nan (uintn nancode)</code>	
	<code>T fma (T a, T b, T c)</code>	Multiply and add, then round	<code>halfn nan (ushortn nancode)</code>	
	<code>T fmax (T x, T y)</code>	Return y if x < y, otherwise it returns x	<code>doublen nan (ulongn nancode)</code>	
	<code>T fmin (T x, T y)</code>	Return y if y < x, otherwise it returns x	<code>T nextafter (T x, T y)</code>	Next representable floating-point value following x in the direction of y
	<code>T fmaxn (halfn x, halfn y)</code>		<code>T pow (T x, T y)</code>	Compute x to the power of y (x^y)
	<code>T fminn (halfn x, float y)</code>		<code>T pown (T x, intn y)</code>	Compute x^y , where y is an integer
	<code>T fmaxd (doublen x, double y)</code>		<code>T powr (T x, T y)</code>	HN Compute x^y , where x is >= 0
	<code>T fmin (halfn x, halfn y)</code>		<code>T half_recip (T x)</code>	$1/x$ (T may be float or floatn)
	<code>T fminn (floatn x, float y)</code>		<code>T native_recip (T x)</code>	
	<code>T fmax (doublen x, double y)</code>		<code>T remainder (T x, T y)</code>	Floating point remainder
	<code>T fmin (floatn x, float y)</code>		<code>T remquo (T x, T y, Q intn *quo)</code>	Floating point remainder and quotient
	<code>T fmod (T x, T y)</code>	Modulus. Returns $x - y * \text{trunc}(x/y)$	<code>T rint (T)</code>	Round to nearest even integer
	<code>T fract (T x, Q intn *iptr)</code>	Fractional value in x	<code>T rootn (T x, intn y)</code>	Compute x to the power of 1/y
	<code>T frexp (T x, Q intn *exp)</code>	Extract mantissa and exponent	<code>T round (T x)</code>	Integral value nearest to x rounding
	<code>T hypot (T x, T y)</code>	Square root of $x^2 + y^2$	<code>T rsqrt (T)</code>	Inverse square root
	<code>T intn ilogb (T x)</code>	Return exponent as an integer value	<code>T sin (T)</code>	HN Sine
	<code>T ldexp (T x, intn n)</code>	$x * 2^n$	<code>T sincos (T x, Q T *cosval)</code>	Sine and cosine of x
	<code>T ldexp (T x, int n)</code>		<code>T sinh (T)</code>	Hyperbolic sine
	<code>T lgamma (T x)</code>	Log gamma function	<code>T sinpi (T x)</code>	$\sin(\pi x)$
	<code>T lgamma_r (T x, Q intn *signp)</code>		<code>T sqrt (T)</code>	Square root
	<code>T log (T)</code>	Natural logarithm	<code>T tan (T)</code>	Tangent
	<code>T log2 (T)</code>	Base 2 logarithm	<code>T tanh (T)</code>	Hyperbolic tangent
	<code>T log10 (T)</code>	Base 10 logarithm	<code>T tanpi (T x)</code>	$\tan(\pi x)$
	<code>T log1p (T x)</code>	$\ln(1.0 + x)$	<code>T gamma (T)</code>	Gamma function
	<code>T log (T x)</code>	Exponent of x	<code>T trunc (T)</code>	Round to integer toward zero
	<code>T mad (T a, T b, T c)</code>	Approximates $a * b + c$		
	<code>T maxmag (T x, T y)</code>	Maximum magnitude of x and y		

Memory Model

Hierarchy of address spaces

- Each address space is distinct and can't be intermixed
- All data movement between each address space must be done **explicitly** (at all levels)

Memory Model

Uses relaxed consistency

- State of memory visible to a work-item is not guaranteed to be consistent with all work-items
- If consistency is needed, synchronisation is **required**

Private:

Visible only to a single work-item

Local:

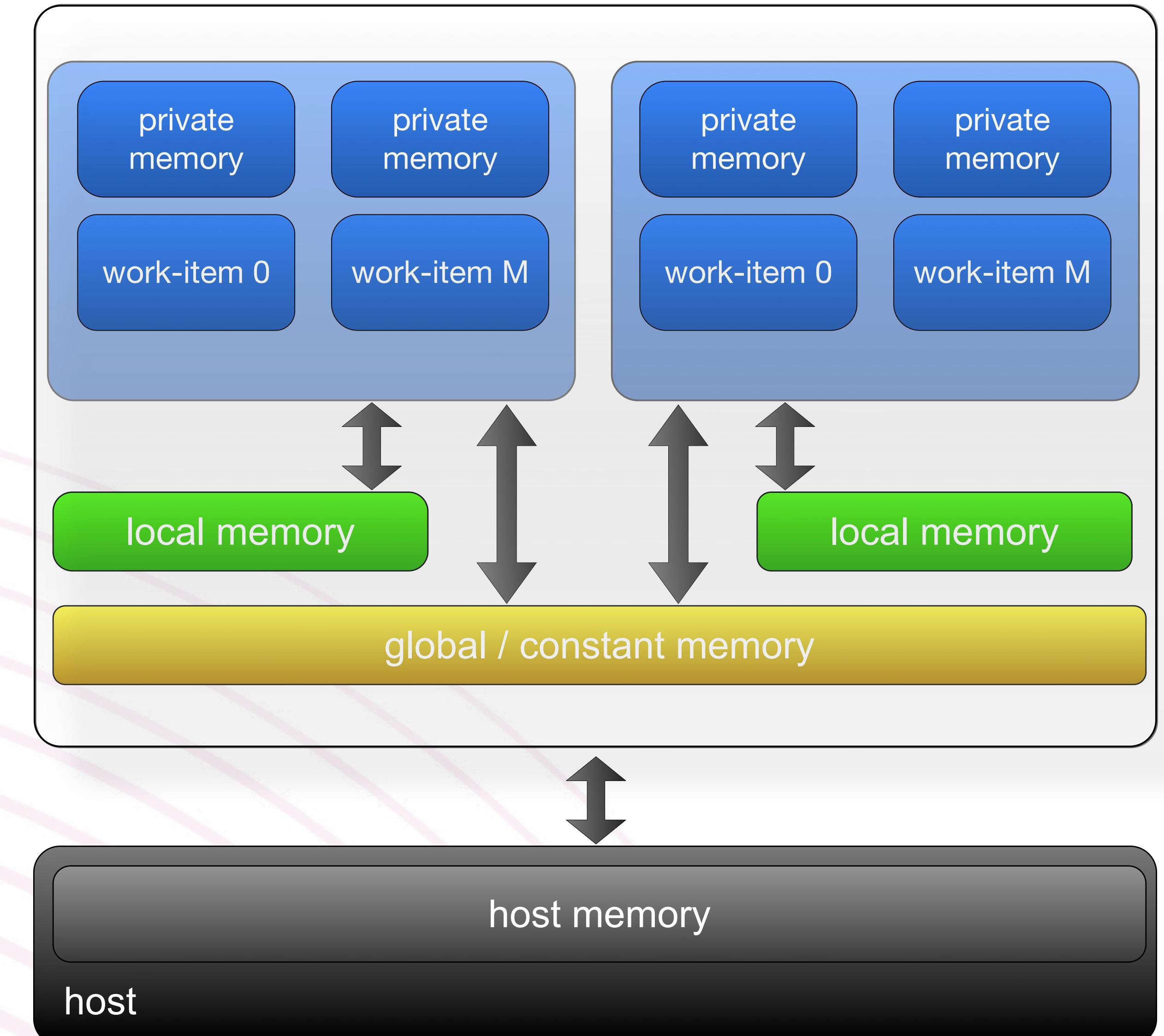
Shared within a work-group

Global/Constant::

Accessible by any work-item

Host:

Accessible only from application



Dot product	<code>float dot (float p0, float p1)</code> <code>float dot (floatn p0, floatn p1)</code> <code>double dot (double p0, double p1)</code> <code>double dot (doublen p0, doublen p1)</code> <code>half dot (half p0, half p1)</code> <code>half dot (halfn p0, halfn p1)</code>																																														
Cross product	<code>float[3,4] cross (float[3,4] p0, float[3,4] p1)</code> <code>double[3,4] cross (double[3,4] p0, double[3,4] p1)</code> <code>half[3,4] cross (half[3,4] p0, half[3,4] p1)</code>																																														
Relational Built-in Functions [6.11.6]	<table border="1"> <thead> <tr> <th></th> <th>Test for +ve or -ve infinity</th> </tr> </thead> <tbody> <tr> <td><code>int isnan (float)</code> <code>intn isnan (floatn)</code> <code>int isnan (double)</code> <code>long isnan (doublen)</code> <code>int isnan (half)</code> <code>shortn isnan (halfn)</code></td><td></td></tr> <tr> <td><code>int isequal (float x, float y)</code> <code>intn isequal (floatn x, floatn y)</code> <code>int isequal (double x, double y)</code> <code>long isequal (doublen x, doublen y)</code> <code>int isequal (half x, half y)</code> <code>shortn isequal (halfn x, halfn y)</code></td><td>Compare of $x == y$</td></tr> <tr> <td><code>int isnotequal (float x, float y)</code> <code>intn isnotequal (floatn x, floatn y)</code> <code>int isnotequal (double x, double y)</code> <code>long isnotequal (doublen x, doublen y)</code> <code>int isnotequal (half x, half y)</code> <code>shortn isnotequal (halfn x, halfn y)</code></td><td>Compare of $x != y$</td></tr> <tr> <td><code>int isgreater (float x, float y)</code> <code>intn isgreater (floatn x, floatn y)</code> <code>int isgreater (double x, double y)</code> <code>long isgreater (doublen x, doublen y)</code> <code>int isgreater (half x, half y)</code> <code>shortn isgreater (halfn x, halfn y)</code></td><td>Compare of $x > y$</td></tr> <tr> <td><code>int isgreaterequal (float x, float y)</code> <code>intn isgreaterequal (floatn x, floatn y)</code> <code>int isgreaterequal (double x, double y)</code> <code>long isgreaterequal (doublen x, doublen y)</code> <code>int isgreaterequal (half x, half y)</code> <code>shortn isgreaterequal (halfn x, halfn y)</code></td><td>Compare of $x \geq y$</td></tr> <tr> <td><code>int isless (float x, float y)</code> <code>intn isless (floatn x, floatn y)</code> <code>int isless (double x, double y)</code> <code>long isless (doublen x, doublen y)</code> <code>int isless (half x, half y)</code> <code>shortn isless (halfn x, halfn y)</code></td><td>Compare of $x < y$</td></tr> <tr> <td><code>int islessequal (float x, float y)</code> <code>intn islessequal (floatn x, floatn y)</code> <code>int islessequal (double x, double y)</code> <code>long islessequal (doublen x, doublen y)</code> <code>int islessequal (half x, half y)</code> <code>shortn islessequal (halfn x, halfn y)</code></td><td>Compare of $x \leq y$</td></tr> <tr> <td><code>int islessgreater (float x, float y)</code> <code>intn islessgreater (floatn x, floatn y)</code> <code>int islessgreater (double x, double y)</code> <code>long islessgreater (doublen x, doublen y)</code> <code>int islessgreater (half x, half y)</code> <code>shortn islessgreater (halfn x, halfn y)</code></td><td>Compare of $(x < y) (x > y)$</td></tr> <tr> <td>Test for finite value</td><td><code>int isfinite (float)</code> <code>intn isfinite (floatn)</code> <code>int isfinite (double)</code> <code>long isfinite (doublen)</code> <code>int isfinite (half)</code> <code>shortn isfinite (halfn)</code></td></tr> <tr> <td>Atomic Functions [6.11.11, 9.4]</td><td> <table border="1"> <thead> <tr> <th></th> <th></th> </tr> </thead> <tbody> <tr> <td><code>T atomic_add (Q T *p, T val)</code></td><td>Read, add, and store</td></tr> <tr> <td><code>T atomic_sub (Q T *p, T val)</code></td><td>Read, subtract, and store</td></tr> <tr> <td><code>T atomic_xchg (Q T *p, T val)</code></td><td>Read, swap, and store</td></tr> <tr> <td><code>T atomic_inc (Q T *p)</code></td><td>Read, increment, and store</td></tr> <tr> <td><code>T atomic_dec (Q T *p)</code></td><td>Read, decrement, and store</td></tr> <tr> <td><code>T atomic_cmphxg (Q T *p, T cmp, T val)</code></td><td>Read and store ($*p == c$? $*val : *p$)</td></tr> <tr> <td><code>T atomic_min (Q T *p, T val)</code></td><td>Read, store $\min(*p, val)$</td></tr> <tr> <td><code>T atomic_max (Q T *p, T val)</code></td><td>Read, store $\max(*p, val)$</td></tr> <tr> <td><code>T atomic_and (Q T *p, T val)</code></td><td>Read, store $(*p \& val)$</td></tr> <tr> <td><code>T atomic_or (Q T *p, T val)</code></td><td>Read, store $(*p val)$</td></tr> <tr> <td><code>T atomic_xor (Q T *p, T val)</code></td><td>Read, store $(*p ^ val)$</td></tr> </tbody> </table> <p>Extended 64-bit atomic functions are enabled by the following pragma; <code>extension-name</code> is one of <code>cl_khr_int64_base</code>, <code>extended_</code> atoms: <code>#pragma OPENCL EXTENSION extension-name : enable</code></p> </td></tr> </tbody> </table>		Test for +ve or -ve infinity	<code>int isnan (float)</code> <code>intn isnan (floatn)</code> <code>int isnan (double)</code> <code>long isnan (doublen)</code> <code>int isnan (half)</code> <code>shortn isnan (halfn)</code>		<code>int isequal (float x, float y)</code> <code>intn isequal (floatn x, floatn y)</code> <code>int isequal (double x, double y)</code> <code>long isequal (doublen x, doublen y)</code> <code>int isequal (half x, half y)</code> <code>shortn isequal (halfn x, halfn y)</code>	Compare of $x == y$	<code>int isnotequal (float x, float y)</code> <code>intn isnotequal (floatn x, floatn y)</code> <code>int isnotequal (double x, double y)</code> <code>long isnotequal (doublen x, doublen y)</code> <code>int isnotequal (half x, half y)</code> <code>shortn isnotequal (halfn x, halfn y)</code>	Compare of $x != y$	<code>int isgreater (float x, float y)</code> <code>intn isgreater (floatn x, floatn y)</code> <code>int isgreater (double x, double y)</code> <code>long isgreater (doublen x, doublen y)</code> <code>int isgreater (half x, half y)</code> <code>shortn isgreater (halfn x, halfn y)</code>	Compare of $x > y$	<code>int isgreaterequal (float x, float y)</code> <code>intn isgreaterequal (floatn x, floatn y)</code> <code>int isgreaterequal (double x, double y)</code> <code>long isgreaterequal (doublen x, doublen y)</code> <code>int isgreaterequal (half x, half y)</code> <code>shortn isgreaterequal (halfn x, halfn y)</code>	Compare of $x \geq y$	<code>int isless (float x, float y)</code> <code>intn isless (floatn x, floatn y)</code> <code>int isless (double x, double y)</code> <code>long isless (doublen x, doublen y)</code> <code>int isless (half x, half y)</code> <code>shortn isless (halfn x, halfn y)</code>	Compare of $x < y$	<code>int islessequal (float x, float y)</code> <code>intn islessequal (floatn x, floatn y)</code> <code>int islessequal (double x, double y)</code> <code>long islessequal (doublen x, doublen y)</code> <code>int islessequal (half x, half y)</code> <code>shortn islessequal (halfn x, halfn y)</code>	Compare of $x \leq y$	<code>int islessgreater (float x, float y)</code> <code>intn islessgreater (floatn x, floatn y)</code> <code>int islessgreater (double x, double y)</code> <code>long islessgreater (doublen x, doublen y)</code> <code>int islessgreater (half x, half y)</code> <code>shortn islessgreater (halfn x, halfn y)</code>	Compare of $(x < y) (x > y)$	Test for finite value	<code>int isfinite (float)</code> <code>intn isfinite (floatn)</code> <code>int isfinite (double)</code> <code>long isfinite (doublen)</code> <code>int isfinite (half)</code> <code>shortn isfinite (halfn)</code>	Atomic Functions [6.11.11, 9.4]	<table border="1"> <thead> <tr> <th></th> <th></th> </tr> </thead> <tbody> <tr> <td><code>T atomic_add (Q T *p, T val)</code></td><td>Read, add, and store</td></tr> <tr> <td><code>T atomic_sub (Q T *p, T val)</code></td><td>Read, subtract, and store</td></tr> <tr> <td><code>T atomic_xchg (Q T *p, T val)</code></td><td>Read, swap, and store</td></tr> <tr> <td><code>T atomic_inc (Q T *p)</code></td><td>Read, increment, and store</td></tr> <tr> <td><code>T atomic_dec (Q T *p)</code></td><td>Read, decrement, and store</td></tr> <tr> <td><code>T atomic_cmphxg (Q T *p, T cmp, T val)</code></td><td>Read and store ($*p == c$? $*val : *p$)</td></tr> <tr> <td><code>T atomic_min (Q T *p, T val)</code></td><td>Read, store $\min(*p, val)$</td></tr> <tr> <td><code>T atomic_max (Q T *p, T val)</code></td><td>Read, store $\max(*p, val)$</td></tr> <tr> <td><code>T atomic_and (Q T *p, T val)</code></td><td>Read, store $(*p \& val)$</td></tr> <tr> <td><code>T atomic_or (Q T *p, T val)</code></td><td>Read, store $(*p val)$</td></tr> <tr> <td><code>T atomic_xor (Q T *p, T val)</code></td><td>Read, store $(*p ^ val)$</td></tr> </tbody> </table> <p>Extended 64-bit atomic functions are enabled by the following pragma; <code>extension-name</code> is one of <code>cl_khr_int64_base</code>, <code>extended_</code> atoms: <code>#pragma OPENCL EXTENSION extension-name : enable</code></p>			<code>T atomic_add (Q T *p, T val)</code>	Read, add, and store	<code>T atomic_sub (Q T *p, T val)</code>	Read, subtract, and store	<code>T atomic_xchg (Q T *p, T val)</code>	Read, swap, and store	<code>T atomic_inc (Q T *p)</code>	Read, increment, and store	<code>T atomic_dec (Q T *p)</code>	Read, decrement, and store	<code>T atomic_cmphxg (Q T *p, T cmp, T val)</code>	Read and store ($*p == c$? $*val : *p$)	<code>T atomic_min (Q T *p, T val)</code>	Read, store $\min(*p, val)$	<code>T atomic_max (Q T *p, T val)</code>	Read, store $\max(*p, val)$	<code>T atomic_and (Q T *p, T val)</code>	Read, store $(*p \& val)$	<code>T atomic_or (Q T *p, T val)</code>	Read, store $(*p val)$	<code>T atomic_xor (Q T *p, T val)</code>	Read, store $(*p ^ val)$
	Test for +ve or -ve infinity																																														
<code>int isnan (float)</code> <code>intn isnan (floatn)</code> <code>int isnan (double)</code> <code>long isnan (doublen)</code> <code>int isnan (half)</code> <code>shortn isnan (halfn)</code>																																															
<code>int isequal (float x, float y)</code> <code>intn isequal (floatn x, floatn y)</code> <code>int isequal (double x, double y)</code> <code>long isequal (doublen x, doublen y)</code> <code>int isequal (half x, half y)</code> <code>shortn isequal (halfn x, halfn y)</code>	Compare of $x == y$																																														
<code>int isnotequal (float x, float y)</code> <code>intn isnotequal (floatn x, floatn y)</code> <code>int isnotequal (double x, double y)</code> <code>long isnotequal (doublen x, doublen y)</code> <code>int isnotequal (half x, half y)</code> <code>shortn isnotequal (halfn x, halfn y)</code>	Compare of $x != y$																																														
<code>int isgreater (float x, float y)</code> <code>intn isgreater (floatn x, floatn y)</code> <code>int isgreater (double x, double y)</code> <code>long isgreater (doublen x, doublen y)</code> <code>int isgreater (half x, half y)</code> <code>shortn isgreater (halfn x, halfn y)</code>	Compare of $x > y$																																														
<code>int isgreaterequal (float x, float y)</code> <code>intn isgreaterequal (floatn x, floatn y)</code> <code>int isgreaterequal (double x, double y)</code> <code>long isgreaterequal (doublen x, doublen y)</code> <code>int isgreaterequal (half x, half y)</code> <code>shortn isgreaterequal (halfn x, halfn y)</code>	Compare of $x \geq y$																																														
<code>int isless (float x, float y)</code> <code>intn isless (floatn x, floatn y)</code> <code>int isless (double x, double y)</code> <code>long isless (doublen x, doublen y)</code> <code>int isless (half x, half y)</code> <code>shortn isless (halfn x, halfn y)</code>	Compare of $x < y$																																														
<code>int islessequal (float x, float y)</code> <code>intn islessequal (floatn x, floatn y)</code> <code>int islessequal (double x, double y)</code> <code>long islessequal (doublen x, doublen y)</code> <code>int islessequal (half x, half y)</code> <code>shortn islessequal (halfn x, halfn y)</code>	Compare of $x \leq y$																																														
<code>int islessgreater (float x, float y)</code> <code>intn islessgreater (floatn x, floatn y)</code> <code>int islessgreater (double x, double y)</code> <code>long islessgreater (doublen x, doublen y)</code> <code>int islessgreater (half x, half y)</code> <code>shortn islessgreater (halfn x, halfn y)</code>	Compare of $(x < y) (x > y)$																																														
Test for finite value	<code>int isfinite (float)</code> <code>intn isfinite (floatn)</code> <code>int isfinite (double)</code> <code>long isfinite (doublen)</code> <code>int isfinite (half)</code> <code>shortn isfinite (halfn)</code>																																														
Atomic Functions [6.11.11, 9.4]	<table border="1"> <thead> <tr> <th></th> <th></th> </tr> </thead> <tbody> <tr> <td><code>T atomic_add (Q T *p, T val)</code></td><td>Read, add, and store</td></tr> <tr> <td><code>T atomic_sub (Q T *p, T val)</code></td><td>Read, subtract, and store</td></tr> <tr> <td><code>T atomic_xchg (Q T *p, T val)</code></td><td>Read, swap, and store</td></tr> <tr> <td><code>T atomic_inc (Q T *p)</code></td><td>Read, increment, and store</td></tr> <tr> <td><code>T atomic_dec (Q T *p)</code></td><td>Read, decrement, and store</td></tr> <tr> <td><code>T atomic_cmphxg (Q T *p, T cmp, T val)</code></td><td>Read and store ($*p == c$? $*val : *p$)</td></tr> <tr> <td><code>T atomic_min (Q T *p, T val)</code></td><td>Read, store $\min(*p, val)$</td></tr> <tr> <td><code>T atomic_max (Q T *p, T val)</code></td><td>Read, store $\max(*p, val)$</td></tr> <tr> <td><code>T atomic_and (Q T *p, T val)</code></td><td>Read, store $(*p \& val)$</td></tr> <tr> <td><code>T atomic_or (Q T *p, T val)</code></td><td>Read, store $(*p val)$</td></tr> <tr> <td><code>T atomic_xor (Q T *p, T val)</code></td><td>Read, store $(*p ^ val)$</td></tr> </tbody> </table> <p>Extended 64-bit atomic functions are enabled by the following pragma; <code>extension-name</code> is one of <code>cl_khr_int64_base</code>, <code>extended_</code> atoms: <code>#pragma OPENCL EXTENSION extension-name : enable</code></p>			<code>T atomic_add (Q T *p, T val)</code>	Read, add, and store	<code>T atomic_sub (Q T *p, T val)</code>	Read, subtract, and store	<code>T atomic_xchg (Q T *p, T val)</code>	Read, swap, and store	<code>T atomic_inc (Q T *p)</code>	Read, increment, and store	<code>T atomic_dec (Q T *p)</code>	Read, decrement, and store	<code>T atomic_cmphxg (Q T *p, T cmp, T val)</code>	Read and store ($*p == c$? $*val : *p$)	<code>T atomic_min (Q T *p, T val)</code>	Read, store $\min(*p, val)$	<code>T atomic_max (Q T *p, T val)</code>	Read, store $\max(*p, val)$	<code>T atomic_and (Q T *p, T val)</code>	Read, store $(*p \& val)$	<code>T atomic_or (Q T *p, T val)</code>	Read, store $(*p val)$	<code>T atomic_xor (Q T *p, T val)</code>	Read, store $(*p ^ val)$																						
<code>T atomic_add (Q T *p, T val)</code>	Read, add, and store																																														
<code>T atomic_sub (Q T *p, T val)</code>	Read, subtract, and store																																														
<code>T atomic_xchg (Q T *p, T val)</code>	Read, swap, and store																																														
<code>T atomic_inc (Q T *p)</code>	Read, increment, and store																																														
<code>T atomic_dec (Q T *p)</code>	Read, decrement, and store																																														
<code>T atomic_cmphxg (Q T *p, T cmp, T val)</code>	Read and store ($*p == c$? $*val : *p$)																																														
<code>T atomic_min (Q T *p, T val)</code>	Read, store $\min(*p, val)$																																														
<code>T atomic_max (Q T *p, T val)</code>	Read, store $\max(*p, val)$																																														
<code>T atomic_and (Q T *p, T val)</code>	Read, store $(*p \& val)$																																														
<code>T atomic_or (Q T *p, T val)</code>	Read, store $(*p val)$																																														
<code>T atomic_xor (Q T *p, T val)</code>	Read, store $(*p ^ val)$																																														

Async Copies and Prefetch Functions [6.11.10]

T is type `char`, `charn`, `uchar`, `ucharn`, `short`, `shortn`, `ushort`, `ushortn`, `int`, `intn`, `uint`, `uintn`, `long`, `longn`, `ulong`, `ulongn`, `float`, `floatn`, and optionally `halfn` `double`, `doublen`. Optional extensions enable the `halfn`, `double`, and `doublen` types.

event_t async_group_copy
 $(_\text{local } T^*dst, \text{const } _\text{global } T^*src, \text{size_t num_gentypes}, \text{event_t event})$

event_t async_group_copy
 $(_\text{global } T^*dst, \text{const } _\text{local } T^*src, \text{size_t num_gentypes}, \text{event_t event})$

event_t
async_group_strided_copy
 $(_\text{local } T^*dst, \text{const } _\text{global } T^*src, \text{size_t num_gentypes}, \text{size_t src_stride}, \text{event_t event})$

event_t
async_group_strided_copy
 $(_\text{global } T^*dst, \text{const } _\text{local }$

Copies `num_gentypes` T elements from `src` to `dst`

Copies `num_gentypes` T elements from `src` to `dst`

Atomic Functions [6.11.11, 9.4]

T is type int or unsigned int. T may also be type float for **atomic_xchg**, and type long or ulong for extended 64-bit atomic functions. Q is volatile __global or volatile __local, except Q must be volatile __global for **atomic_xchg** when T is float.

The built-in atomic functions for 32-bit values begin with **atomic_** while the extended 64-bit atomic functions begin with **atom_**. For example:

Built-in atomic function
atomic_add ()

Extended atomic function
atom_add ()

Extended 64-bit atomic functions are enabled by the following pragma; *extension-name* is one of `cl_khr_int64_{base, extended}_atomics`:

```
#pragma OPENCL EXTENSION extension-name : enable
```

$T \text{atomic_add} (Q T *p, T val)$	Read, add, and store
$T \text{atomic_sub} (Q T *p, T val)$	Read, subtract, and store
$T \text{atomic_xchg} (Q T *p, T val)$	Read, swap, and store
$T \text{atomic_inc} (Q T *p)$	Read, increment, and store
$T \text{atomic_dec} (Q T *p)$	Read, decrement, and store
$T \text{atomic_cmpxchg} (Q T *p, T cmp, T val)$	Read and store ($*p == cmp$) ? $val : *p$
$T \text{atomic_min} (Q T *p, T val)$	Read, store $\min(*p, val)$
$T \text{atomic_max} (Q T *p, T val)$	Read, store $\max(*p, val)$
$T \text{atomic_and} (Q T *p, T val)$	Read, store $(*p \& val)$
$T \text{atomic_or} (Q T *p, T val)$	Read, store $(*p val)$
$T \text{atomic_xor} (Q T *p, T val)$	Read, store $(*p ^ val)$

double dot
double dot (half)
half dot (half)
float[3,4] cr
double[3,4] cr
half[3,4] cr

Relational
 T is type float
ushort, ushort
ulong, (and
short, short
ushort, ushort
extensions:
int isequal
int isnoteq
int isequal
longn iseq
int isequal
shortn iseq
int isnoteq
int isnoteq
int isnoteq
longn isnot
int isnoteq
shortn isnot
int isgreat
int isgreat
int isgreat
longn isgre
int isgreat
shortn isgr
int isgreat
int isgreat
int isgreat
longn isgreat
int isgreat
shortn isgr
int isless
int isless
int isless
longn isles
int isless
shortn isles
int islesse
int islesse
int islesse
longn islesse
int islesse
shortn islesse
int isless
int isless
int isless
longn isles
int isless
shortn isles
int isinfinit
int isinfinit
int isinfinit
longn isinfin
int isinfinit
shortn isinfin

Atomic

T is type int or unsigned int. T may also be type float for **atomic_xchg**, and type long or ulong for extended 64-bit atomic functions. Q is volatile __global or volatile __local, except Q must be volatile __global for **atomic_xchg** when T is float.

The built-in atomic functions for 32-bit values begin with **atomic_** while the extended 64-bit atomic functions begin with **atom_**. For example:

Built-in atomic function
atomic_add ()

Extended atomic function
atom_add ()

Extended 64-bit atomic functions are enabled by the following pragma; *extension-name* is one of `cl_khr_int64_{base, extended}_atomics`:

```
#pragma OPENCL EXTENSION extension-name : enable
```

$T \text{atomic_sub} (Q T *p, T val)$	Read, subtract, and store
$T \text{atomic_xchg} (Q T *p, T val)$	Read, swap, and store
$T \text{atomic_inc} (Q T *p)$	Read, increment, and store
$T \text{atomic_dec} (Q T *p)$	Read, decrement, and store
$T \text{atomic_cmpxchg} (Q T *p, T cmp, T val)$	Read and store ($*p == cmp$) ? $val : *p$
$T \text{atomic_min} (Q T *p, T val)$	Read, store $\min(*p, val)$
$T \text{atomic_max} (Q T *p, T val)$	Read, store $\max(*p, val)$
$T \text{atomic_and} (Q T *p, T val)$	Read, store $(*p \& val)$
$T \text{atomic_or} (Q T *p, T val)$	Read, store $(*p val)$
$T \text{atomic_xor} (Q T *p, T val)$	Read, store $(*p ^ val)$

$\text{event_t sync_work_group_strided_copy} (_\text{local} T *dst, \text{const } \text{global} T *src, \text{size_t num_gentypes}, \text{size_t src_stride}, \text{event_t event})$	Copies $num_gentypes$ T elements from src to dst
$\text{event_t sync_work_group_strided_copy} (_\text{global} T *dst, \text{const } _\text{local} T *src, \text{size_t num_gentypes}, \text{size_t dst_stride}, \text{event_t event})$	
$\text{void wait_group_events} (\text{int num_events}, \text{event_t event_list})$	Wait for events that identify the sync_work_group_copy operations to complete
$\text{void prefetch} (\text{const } \text{global} T *p, \text{size_t num_gentypes})$	Prefetch $num_gentypes * \text{sizeof}(T)$ bytes into the global cache

double dot (double p_0 , double p_1) double dot (double n p_0 , double n p_1) half dot (half p_0 , half p_1) half dot (half n p_0 , half n p_1) float{3,4} cross (float{3,4} p_0 , float{3,4} p_1) double{3,4} cross (double{3,4} p_0 , double{3,4} p_1) half{3,4} cross (half{3,4} p_0 , half{3,4} p_1)		float distance (float n p_0 , half n p_1) float length (float p) float length (float n p) double length (double p) double length (double n p) half length (half p) half length (half n p)
Relational Built-in Functions [6.11.6]		
T is type float, float n , char, uchar, uchar n , short, short n , ushort, ushort n , int, int n , uint, uint n , long, long n , ulong, or ulong n (and optionally double, double n). S is type char, uchar, short, short n , int, int n , long, or long n . U is type uchar, uchar n , ushort, ushort n , uint, uint n , ulong, or ulong n . Optional extensions enable double, double n , and half n types.		int isnf (float) intn isnf (float n) int isnf (double) longn isnf (double n) int isnf (half) shortn isnf (half n)
int isequal (float x , float y) intn isequal (float n x , float n y) int isequal (double x , double y) longn isequal (double n x , double n y) int isequal (half x , half y) shortn isequal (half n x , half n y)	Compare of $x == y$	int isnan (float) intn isnan (float n) int isnan (double) longn isnan (double n) int isnan (half) shortn isnan (half n)
int isnotequal (float x , float y) intn isnotequal (float n x , float n y) int isnotequal (double x , double y) longn isnotequal (double n x , double n y) int isnotequal (half x , half y) shortn isnotequal (half n x , half n y)	Compare of $x != y$	int isnormal (float) intn isnormal (float n) int isnormal (double) longn isnormal (double n) int isnormal (half) shortn isnormal (half n)
int isgreater (float x , float y) intn isgreater (float n x , float n y) int isgreater (double x , double y) longn isgreater (double n x , double n y) int isgreater (half x , half y) shortn isgreater (half n x , half n y)	Compare of $x > y$	int isordered (float x , float y) intn isordered (float n x , float n y) int isordered (double x , double y) longn isordered (double n x , double n y) int isordered (half x , half y) shortn isordered (half n x , half n y)
int isgreaterequal (float x , float y) intn isgreaterequal (float n x , float n y) int isgreaterequal (double x , double y) longn isgreaterequal (double n x , double n y) int isgreaterequal (half x , half y) shortn isgreaterequal (half n x , half n y)	Compare of $x \geq y$	int isunordered (float x , float y) intn isunordered (float n x , float n y) int isunordered (double x , double y) longn isunordered (double n x , double n y) int isunordered (half x , half y) shortn isunordered (half n x , half n y)
int isless (float x , float y) intn isless (float n x , float n y) int isless (double x , double y) longn isless (double n x , double n y) int isless (half x , half y) shortn isless (half n x , half n y)	Compare of $x < y$	int signbit (float) intn signbit (float n) int signbit (double) longn signbit (double n) int signbit (half) shortn signbit (half n)
int islessequal (float x , float y) intn islessequal (float n x , float n y) int islessequal (double x , double y) longn islessequal (double n x , double n y) int islessequal (half x , half y) shortn islessequal (half n x , half n y)	Compare of $x \leq y$	int any (S x) int all (S x)
int islessgreater (float x , float y) intn islessgreater (float n x , float n y) int islessgreater (double x , double y) longn islessgreater (double n x , double n y) int islessgreater (half x , half y) shortn islessgreater (half n x , half n y)	Compare of $(x < y) (x > y)$	T bitselect (T a , T b , T c) halfn bitselect (half n a , half n b , half n c) doublen bitselect (double n a , double n b , double n c)
int isfinite (float) intn isfinite (float n) int isfinite (double) longn isfinite (double n) int isfinite (half) shortn isfinite (half n)	Test for finite value	T select (T a , T b , S c) T select (T a , T b , U c) doublen select (double n a , double n b , loc n) doublen select (double n a , double n b , val n) halfn select (half n a , half n b , short n) halfn select (half n a , half n b , ushort n)
Atomic Functions [6.11.11, 9.4]		
T is type int or unsigned int. T may also be type float for atomic_xchg, and type long or ulong for extended 64-bit atomic functions. Q is volatile_global or volatile_local, except Q must be volatile_global for atomic_xchg when T is float.		T atomic_add (Q T * p , T val) T atomic_sub (Q T * p , T val) T atomic_xchg (Q T * p , T val) T atomic_inc (Q T * p) T atomic_dec (Q T * p) T atomic_cmpxch (Q T * p , T cmp, T val) T atomic_min (Q T * p , T val) T atomic_max (Q T * p , T val) T atomic_and (Q T * p , T val) T atomic_or (Q T * p , T val) T atomic_xor (Q T * p , T val)
The built-in atomic functions for 32-bit values begin with atomic_, while the extended 64-bit atomic functions begin with atom_. For example:		
Built-in atomic function atomic_add ()	Extended atomic function atom_add ()	
Extended 64-bit atomic functions are enabled by the following pragma; extension-name is one of cl_khr_int64_base, extended_atomics: #pragma OPENCL EXTENSION extension-name : enable		
©2010 Khronos Group - Rev. 0610		

Vector Data Load/Store Functions [6.11.7]

Q is an Address Space Qualifier listed in 6.5 unless otherwise noted. R defaults to the current rounding mode, or is one of the Rounding Modes listed in 6.2.3.2. T is type char, uchar, short, ushort, int, uint, long, ulong, half, or float (or optionally double). Tn refers to the vector form of type T . **Optional extensions enable the double, doublen, half, and halfn types.**

Tn vloadn (size_t offset, const Q T * p)

Read vector data from memory

void vstoren (Tn data, size_t offset, Q T * p)

Write vector data to memory
(Q in this function cannot be `_constant`)

float vload_half (size_t offset, const Q half * p)

Read a half from memory

floatn vload_halfn (size_t offset, const Q half * p)

Read multiple halves from memory

void vstore_half (float data, size_t offset, Q half * p)

Write a half to memory

<code>LINE_</code>	Integer line number
<code>_OPENCL_VERSION_</code>	Integer version number
<code>_CL_VERSION_1_0_</code>	Substitutes integer 100 for version 1.0
<code>_CL_VERSION_1_1_</code>	Substitutes integer 110 for version 1.1
<code>_ENDIAN_LITTLE_</code>	1 if device is little endian
<code>_kernel_exec(X, type)</code>	Same as: <code>_kernel_attribute_(work_group_size_hint(x, 1))_\attribute_((vec_type_hint(type)))</code>
<code>_IMAGE_SUPPORT_</code>	1 if images are supported
<code>_FAST_RELAXED_MATH_</code>	1 if -cl-fast-relaxed-math optimization option is specified
Math Constants	
<code>MAXFLOAT</code>	Value non-precise point
<code>HUGE_VALF</code>	Positive expression to +in error
Work-Item Built-in Functions [6.11.1] <small>D is dimension index.</small>	
<code>uint get_work_dim()</code>	Num. of dimensions in use
<code>size_t get_global_size(uint D)</code>	Num. of global work-items
<code>size_t get_global_id(uint D)</code>	Global work-item ID value
<code>size_t get_local_size(uint D)</code>	Num. of local work-items
Integer Built-in Functions [6.11.3]	
<small>T is type char, uchar, ucharn, short, shortn, ushort, ushortn, int, intr, uint, uintn, long, longn, ulong, or ulongn. U is the unsigned version of T. S is the scalar version of T.</small>	
<code>U abs(T x)</code>	$ x $
<code>U abs_diff(T x, T y)</code>	$ x-y $ without modulo overflow
<code>T add_sat(T x, T y)</code>	$x+y$ and saturates the result
<code>T hadd(T x, T y)</code>	$(x+y) \gg 1$ without mod. overflow
<code>T rhadd(T x, T y)</code>	$(x+y+1) \gg 1$
<code>T clz(T x)</code>	Number of leading 0-bits in x
<code>T clamp(T x, T min, T max)</code>	$\min(\max(x, \text{minval}), \text{maxval})$
<code>T clamp(T x, S min, S max)</code>	
<code>T mad_hi(T a, T b, T c)</code>	$a * b + c$
<code>T mad_sat(T a, T b, T c)</code>	$a * b + c$ and saturates the result
<code>T max(T x, T y)</code>	y if $x < y$, otherwise it returns x
<code>T max(T x, S y)</code>	y if $y < x$, otherwise it returns x
<code>T min(T x, T y)</code>	y if $y < x$, otherwise it returns x
<code>T min(T x, S y)</code>	y if $y < x$, otherwise it returns x
<code>T mul_hi(T x, T y)</code>	high half of the product of x and y
<code>T rotate(T v, T l)</code>	$\text{result}[l] = v[l] \ll l$
Math Built-in Functions [6.11.2]	
<small>T is type float or floatn (or optionally double, doublen, or halfn). intn, uintn, and ulongn must be scalar when T is scalar. Q is qualified _global_, _local_, or _private_. HN indicates that Half and Native variants are available by prepending "half_" or "native_" to function name. Prototypes shown in purple are half_ and native_ only. Optional extensions enable double, doublen, half, and halfn types.</small>	
<code>T acos(T)</code>	Arc cosine
<code>T acosh(T)</code>	Inverse hyperbolic cosine
<code>T acospi(T x)</code>	$\text{acos}(x) / \pi$
<code>T asin(T)</code>	Arc sine
<code>T asinh(T)</code>	Inverse hyperbolic sine
<code>T asinpi(T x)</code>	$\text{asin}(x) / \pi$
<code>T atan(T y_over_x)</code>	Arc tangent
<code>T atan2(T y, T x)</code>	Arc tangent of y/x
<code>T atanh(T)</code>	Hyperbolic arc tangent
<code>T atanpi(T x)</code>	$\text{atan}(x) / \pi$
<code>T atan2pi(T x, T y)</code>	$\text{atan2}(x, y) / \pi$
<code>T cbrt(T)</code>	Cube root
<code>T ceil(T)</code>	Round to integer toward +infinity
<code>T copysign(T x, T y)</code>	x with sign changed to sign of y
<code>T cos(T)</code>	<small>HN</small> Cosine
<code>T cosh(T)</code>	Hyperbolic consine
<code>T cospi(T x)</code>	$\cos(\pi x)$
<code>T half_divide(T x, T y)</code>	x/y
<code>T native_divide(T x, T y)</code>	(T may be float or floatn)
<code>T erfc(T)</code>	Complementary error function
<code>T erf(T)</code>	Calculates error function of T
<code>T exp(T x)</code>	<small>HN</small> Exponential base e
<code>T exp2(T)</code>	<small>HN</small> Exponential base 2
<code>T exp10(T)</code>	<small>HN</small> Exponential base 10
<code>T expm1(T x)</code>	
<code>T fabs(T)</code>	
<code>T fdim(T x, T y)</code>	
<code>T floor(T)</code>	
<code>T fma(T a, T b, T c)</code>	
<code>T fmax(T x, T y)</code>	$\text{halfn}_fmax(\text{halfn } x, \text{half } y)$
<code>T fmin(T x, T y)</code>	$\text{halfn}_fmin(\text{halfn } x, \text{half } y)$
<code>T fmod(T x, T y)</code>	
<code>T fract(T x, Q T *iptr)</code>	
<code>T frexp(T x, Q intn *exp)</code>	
<code>T hypot(T x, T y)</code>	
<code>T ilogb(T x)</code>	
<code>T ldexp(T x, intn n)</code>	
<code>T ldexp(T x, int n)</code>	
<code>T lgamma(T x)</code>	
<code>T lgamma_r(T x, Q intn *signp)</code>	
<code>T log(T)</code>	<small>HN</small>
<code>T log2(T)</code>	<small>HN</small>
<code>T log10(T)</code>	<small>HN</small>
<code>T log1p(T x)</code>	
<code>T logb(T x)</code>	
<code>T mad(T a, T b, T c)</code>	
<code>T maxmag(T x, T y)</code>	

Common Built-in Functions [6.11.4]

T is type float or floatn (or optionally double, doublen, or halfn). Optional extensions enable double, doublen, and halfn types.

$T \text{clamp}(T x, T \text{min}, T \text{max})$

$\text{floatn} \text{clamp}(\text{floatn } x, \text{float } \text{min}, \text{float } \text{max})$

$\text{doublen} \text{clamp}(\text{doublen } x, \text{double } \text{min}, \text{double } \text{max})$

$\text{halfn} \text{clamp}(\text{halfn } x, \text{half } \text{min}, \text{half } \text{max})$

Clamp x to range given by min , max

$T \text{degrees}(T \text{radians})$

radians to degrees

$T \text{max}(T x, T y)$

$\text{floatn} \text{max}(\text{floatn } x, \text{float } y)$

$\text{doublen} \text{max}(\text{doublen } x, \text{double } y)$

$\text{halfn} \text{max}(\text{halfn } x, \text{half } y)$

Max of x and y

$T \text{min}(T x, T y)$

$\text{floatn} \text{min}(\text{floatn } x, \text{float } y)$

$\text{doublen} \text{min}(\text{doublen } x, \text{double } y)$

$\text{halfn} \text{min}(\text{halfn } x, \text{half } y)$

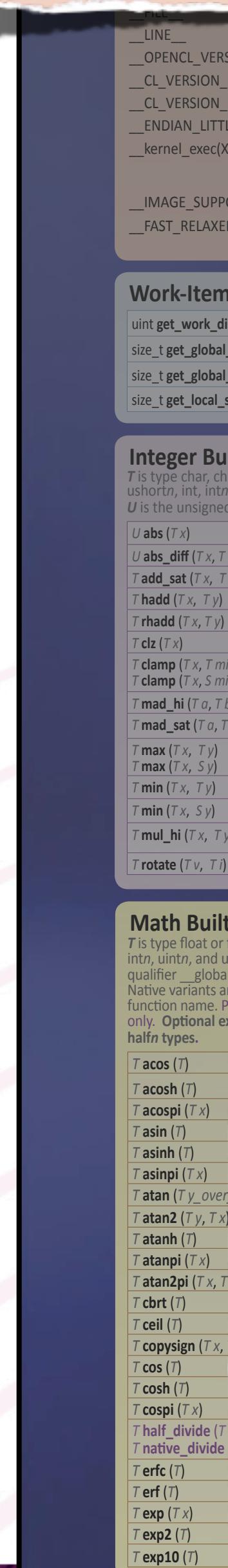
Min of x and y

$T \text{mix}(T x, T y, T a)$

$\text{floatn} \text{mix}(\text{floatn } x, \text{float } y, \text{float } a)$

$\text{doublen} \text{mix}(\text{doublen } x, \text{double } y, \text{double } a)$

Linear blend of x and y



Integer Built-in Functions [6.11.3]

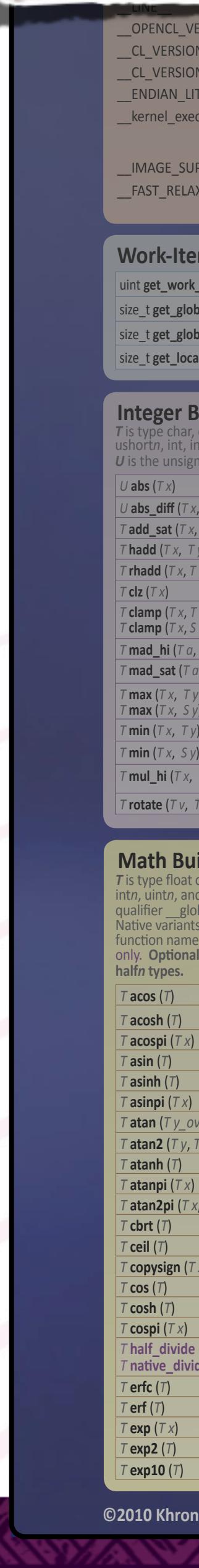
T is type `char`, `charn`, `uchar`, `ucharn`, `short`, `shortn`, `ushort`, `ushortn`, `int`, `intn`, `uint`, `uintn`, `long`, `longn`, `ulong`, or `ulongn`.
 U is the unsigned version of T . S is the scalar version of T .

$U \text{abs}(Tx)$	$ x $
$U \text{abs_diff}(Tx, Ty)$	$ x - y $ without modulo overflow
$T \text{add_sat}(Tx, Ty)$	$x + y$ and saturates the result
$T \text{hadd}(Tx, Ty)$	$(x + y) \gg 1$ without mod. overflow
$T \text{rhadd}(Tx, Ty)$	$(x + y + 1) \gg 1$
$T \text{clz}(Tx)$	Number of leading 0-bits in x
$T \text{clamp}(Tx, T \text{min}, T \text{max})$	$\min(\max(x, \text{minval}), \text{maxval})$
$T \text{clamp}(Tx, S \text{min}, S \text{max})$	
$T \text{mad_hi}(Ta, Tb, Tc)$	$\text{mul_hi}(a, b) + c$
$T \text{mad_sat}(Ta, Tb, Tc)$	$a * b + c$ and saturates the result
$T \text{max}(Tx, Ty)$	y if $x < y$, otherwise it returns x
$T \text{max}(Tx, Sy)$	
$T \text{min}(Tx, Ty)$	y if $y < x$, otherwise it returns x
$T \text{min}(Tx, Sy)$	y if $y < x$, otherwise it returns x
$T \text{mul_hi}(Tx, Ty)$	high half of the product of x and y
$T \text{rotate}(Tv, Ti)$	$\text{result}[idx] = v[idx] \ll i[idx]$

$T \text{sub_sat}(Tx, Ty)$	$x - y$ and saturates the result
For <code>upsample</code> , scalar types are permitted for the vector types below.	
$\text{shortn upsample}(\text{charn } hi, \text{ucharn } lo)$	$\text{result}[i] = ((\text{short})hi[i] \ll 8) lo[i]$
$\text{ushortn upsample}(\text{ucharn } hi, \text{ucharn } lo)$	$\text{result}[i] = ((\text{ushort})hi[i] \ll 8) lo[i]$
$\text{intn upsample}(\text{shortn } hi, \text{ushortn } lo)$	$\text{result}[i] = ((\text{int})hi[i] \ll 16) lo[i]$
$\text{uintn upsample}(\text{ushortn } hi, \text{ushortn } lo)$	$\text{result}[i] = ((\text{uint})hi[i] \ll 16) lo[i]$
$\text{longn upsample}(\text{intn } hi, \text{uintn } lo)$	$\text{result}[i] = ((\text{long})hi[i] \ll 32) lo[i]$
$\text{ulongn upsample}(\text{uintn } hi, \text{uintn } lo)$	$\text{result}[i] = ((\text{ulong})hi[i] \ll 32) lo[i]$

The following fast integer functions optimize the performance of kernels. In these functions, T is type `int`, `int2`, `int3`, `int4`, `int8`, `int16`, `uint`, `uint2`, `uint4`, `uint8` or `uint16`.

$T \text{mad24}(Ta, Tb, Tc)$	Multiply 24-bit int. values a , b , add 32-bit int. result to 32-bit int. c
$T \text{mul24}(Ta, Tb)$	Multiply 24-bit int. values a and b



Math Built-in Functions [6.11.2]

T is type float or floatn (or optionally double, doublen, or halfn). intn, uintn, and ulongn must be scalar when T is scalar. Q is qualifier __global, __local, or __private. **HN** indicates that Half and Native variants are available by prepending "half_" or "native_" to function name. Prototypes shown in purple are half_ and native_ only. Optional extensions enable double, doublen, half, and halfn types.

$T \text{acos}(T)$	Arc cosine
$T \text{acosh}(T)$	Inverse hyperbolic cosine
$T \text{acospi}(Tx)$	$\text{acos}(x) / \pi$
$T \text{asin}(T)$	Arc sine
$T \text{asinh}(T)$	Inverse hyperbolic sine
$T \text{asinpi}(Tx)$	$\text{asin}(x) / \pi$
$T \text{atan}(Ty_over_x)$	Arc tangent
$T \text{atan2}(Ty, Tx)$	Arc tangent of y / x
$T \text{atanh}(T)$	Hyperbolic arc tangent
$T \text{atanpi}(Tx)$	$\text{atan}(x) / \pi$
$T \text{atan2pi}(Tx, Ty)$	$\text{atan2}(x, y) / \pi$
$T \text{cbrt}(T)$	Cube root
$T \text{ceil}(T)$	Round to integer toward + infinity
$T \text{copysign}(Tx, Ty)$	x with sign changed to sign of y
$T \text{cos}(T)$ HN	Cosine
$T \text{cosh}(T)$	Hyperbolic consine
$T \text{cospi}(Tx)$	$\cos(\pi x)$
$T \text{half_divide}(Tx, Ty)$	x / y
$T \text{native_divide}(Tx, Ty)$	(T may be float or floatn)

$T \text{expm1}(Tx)$	$e^x - 1.0$
$T \text{fabs}(T)$	Absolute value
$T \text{fdim}(Tx, Ty)$	"Positive difference" between x and y
$T \text{floor}(T)$	Round to integer toward - infinity
$T \text{fma}(Ta, Tb, Tc)$	Multiply and add, then round
$T \text{fmax}(Tx, Ty)$ halfn fmax(halfn x, half y) floatn fmax(floatn x, float y) doublen fmax(doublen x, double y)	Return y if $x < y$, otherwise it returns x
$T \text{fmin}(Tx, Ty)$ halfn fmin(halfn x, half y) floatn fmin(floatn x, float y) doublen fmin(doublen x, double y)	Return y if $y < x$, otherwise it returns x
$T \text{fmod}(Tx, Ty)$	Modulus. Returns $x - y * \text{trunc}(x/y)$
$T \text{fract}(Tx, Q T *iptr)$	Fractional value in x
$T \text{frexp}(Tx, Q \text{intn} *exp)$	Extract mantissa and exponent
$T \text{hypot}(Tx, Ty)$	Square root of $x^2 + y^2$
$\text{intn} \text{ilogb}(Tx)$	Return exponent as an integer value
$T \text{ldexp}(Tx, \text{intn } n)$	$x * 2^n$
$T \text{lgamma}(Tx)$ $T \text{lgamma_r}(Tx, Q \text{intn} *signp)$	Log gamma function
$T \text{log}(T)$ HN	Natural logarithm
$T \text{log2}(T)$ HN	Base 2 logarithm

$T \text{minmag}(Tx, Ty)$
$T \text{modf}(Tx, Q T *iptr)$
float nan (uintn nan)
floatn nan (uintn nan)
halfn nan (ushortn nan)
doublen nan (ulongn nan)
$T \text{nextafter}(Tx, Ty)$
$T \text{pow}(Tx, Ty)$
$T \text{pown}(Tx, \text{intn } y)$
$T \text{powr}(Tx, Ty)$
$T \text{half_recip}(Tx)$
$T \text{native_recip}(Tx)$
$T \text{remainder}(Tx, T)$
$T \text{remquo}(Tx, Ty, Q \text{intn} *quo)$
$T \text{rint}(T)$
$T \text{rootn}(Tx, \text{intn } y)$
$T \text{round}(Tx)$
$T \text{rsqrt}(T)$
$T \text{sin}(T)$
$T \text{sincos}(Tx, Q T *c)$
$T \text{sinh}(T)$
$T \text{sinpi}(Tx)$
$T \text{sqrt}(T)$

OpenCL API 1.1 Quick Reference Card - Page 4

float dot (float <i>p0</i> , float <i>p1</i>) float dot (float <i>n</i> <i>p0</i> , float <i>n</i> <i>p1</i>) double dot (double <i>p0</i> , double <i>p1</i>) double dot (doublen <i>p0</i> , doublen <i>p1</i>) half dot (half <i>p0</i> , half <i>p1</i>) half dot (halfn <i>p0</i> , halfn <i>p1</i>) float{3,4} cross (float{3,4} <i>p0</i> , float{3,4} <i>p1</i>) double{3,4} cross (double{3,4} <i>p0</i> , double{3,4} <i>p1</i>) half{3,4} cross (half{3,4} <i>p0</i> , half{3,4} <i>p1</i>)	Dot product	double distance (doublen <i>p0</i> , doublen <i>p1</i>) half distance (half <i>p0</i> , half <i>p1</i>) halfn distance (halfn <i>p0</i> , halfn <i>p1</i>) float length (float <i>p</i>) float length (float <i>n</i> <i>p</i>) double length (double <i>p</i>) double length (doublen <i>p</i>) half length (half <i>p</i>) half length (halfn <i>p</i>) float{3,4} cross (float{3,4} <i>p0</i> , float{3,4} <i>p1</i>) double{3,4} cross (double{3,4} <i>p0</i> , double{3,4} <i>p1</i>) half{3,4} cross (half{3,4} <i>p0</i> , half{3,4} <i>p1</i>)	Vector length	doublen normalize (doublen <i>p</i>) half normalize (half <i>p</i>) halfn normalize (halfn <i>p</i>) float fast_distance (float <i>p0</i> , float <i>p1</i>) float fast_distance (float <i>n</i> <i>p0</i> , float <i>n</i> <i>p1</i>) float fast_length (float <i>p</i>) float fast_length (float <i>n</i> <i>p</i>) float fast_normalize (float <i>p</i>) float fast_normalize (float <i>n</i> <i>p</i>)
--	-------------	---	---------------	--

Geometric Built-in Functions [6.11.5]

Vector types may have 2, 3, or 4 components. Optional extensions enable double, doublen, and halfn types.

float dot (float <i>p0</i> , float <i>p1</i>) float dot (float <i>n</i> <i>p0</i> , float <i>n</i> <i>p1</i>) double dot (double <i>p0</i> , double <i>p1</i>) double dot (doublen <i>p0</i> , doublen <i>p1</i>) half dot (half <i>p0</i> , half <i>p1</i>) half dot (halfn <i>p0</i> , halfn <i>p1</i>)	Dot product
--	-------------

float{3,4} cross (float{3,4} <i>p0</i> , float{3,4} <i>p1</i>) double{3,4} cross (double{3,4} <i>p0</i> , double{3,4} <i>p1</i>) half{3,4} cross (half{3,4} <i>p0</i> , half{3,4} <i>p1</i>)	Cross product
--	---------------

float **distance** (float *p0*, float *p1*)

float **distance** (float*n* *p0*, float*n* *p1*)

double **distance** (double *p0*, double *p1*)

double **distance** (doublen *p0*, doublen *p1*)

half **distance** (half *p0*, half *p1*)

halfn **distance** (halfn *p0*, halfn *p1*)

float **length** (float *p*)

float **length** (float*n* *p*)

double **length** (double *p*)

double **length** (doublen *p*)

half **length** (half *p*)

halfn **length** (halfn *p*)

Vector distance

Vector length

float **normalize** (float *p*)

float*n* **normalize** (float*n* *p*)

double **normalize** (double *p*)

doublen **normalize** (doublen *p*)

half **normalize** (half *p*)

halfn **normalize** (halfn *p*)

float **fast_distance** (float *p0*, float *p1*)

float **fast_distance** (float*n* *p0*, float*n* *p1*)

float **fast_length** (float *p*)

float **fast_length** (float*n* *p*)

float **fast_normalize** (float *p*)

float*n* **fast_normalize** (float*n* *p*)

int islessgreater (float <i>x</i> , float <i>y</i>) int islessgreater (float <i>n</i> <i>x</i> , float <i>n</i> <i>y</i>) int islessgreater (double <i>x</i> , double <i>y</i>) long islessgreater (double <i>n</i> <i>x</i> , double <i>n</i> <i>y</i>) int islessgreater (half <i>x</i> , half <i>y</i>) shortn islessgreater (halfn <i>x</i> , halfn <i>y</i>)	Compare if $(x < y) \mid\mid (x > y)$
int isfinite (float) int isfinite (float <i>n</i>) int isfinite (double) long isfinite (double) int isfinite (half) shortn isfinite (halfn)	Test for finite value

T bitselect (<i>T a</i> , <i>T b</i> , <i>T c</i>) halfn bitselect (halfn <i>a</i> , halfn <i>b</i> , halfn <i>c</i>) doublen bitselect (doublen <i>a</i> , doublen <i>b</i> , doublen <i>c</i>)	Components of <i>a</i> are set; else 0
T select (<i>T a</i> , <i>T b</i> , <i>S c</i>) T select (<i>T a</i> , <i>T b</i> , <i>U c</i>) doublen select (doublen, doublen, long) doublen select (doublen, doublen, ulongn) halfn select (halfn, halfn, shortn) halfn select (halfn, halfn, ushortn)	Each bit of result is corresponding bit of <i>a</i> if corresponding bit of <i>c</i> is 0 For each component of a vector type, result[i] = if MSB of <i>c</i> [i] is set ? <i>b</i> [i] : <i>a</i> [i] For scalar type, result = <i>c</i> ? <i>b</i> : <i>a</i>

size_t <i>offset</i> , <i>C halfn</i> * <i>p</i>)	
Async Copies and Prefetch Functions [6.11.10]	

T is type char, uchar, ucharn, short, shortn, ushort, ushortn, int, intn, uint, uintn, long, longn, ulong, ulongn, float, floatn, and optionally halfn, double, doublen. Optional extensions enable the halfn, double, and doublen types.

event_t **async_group_copy**

(*local T***dst*, const *global T***src*, size_t *num_gentypes*, event_t *event*)

event_t **async_group_copy**

(*global T***dst*, const *local T***src*, size_t *num_gentypes*, event_t *event*)

event_t **async_group_strided_copy**

(*local T***dst*, const *global T***src*, size_t *num_gentypes*, size_t *src_stride*, event_t *event*)

event_t **async_group_strided_copy**

(*global T***dst*, const *local T***src*, size_t *num_gentypes*, size_t *dst_stride*, event_t *event*)

void **wait_group_events**

(int *num_events*, event_t **event_list*)

void **prefetch** (*const global T***p*, size_t *num_gentypes*)

Prefetch *num_gentypes* * *sizeof(T)* bytes into the global cache

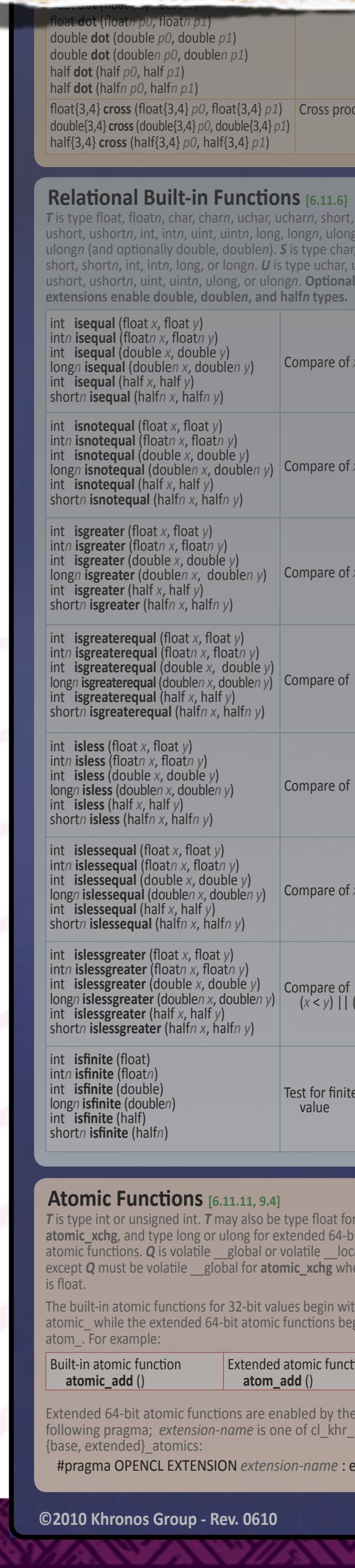
Atomic Functions [6.11.11, 9.4]

T is type int or unsigned int. *T* may also be type float for atomic_xchg, and type long or ulong for extended 64-bit atomic functions. *Q* is volatile_global or volatile_local, except *Q* must be volatile_global for atomic_xchg when *T* is float.

The built-in atomic functions for 32-bit values begin with atomic_, while the extended 64-bit atomic functions begin with atom_. For example:

Built-in atomic function atomic_add ()	Extended atomic function atom_add ()
--	--

Extended 64-bit atomic functions are enabled by the following pragma; extension-name is one of cl_khr_int64_{base, extended}_atomics:
#pragma OPENCL EXTENSION extension-name : enable



Relational Built-in Functions [6.11.6]

T is type `float`, `floatn`, `char`, `charn`, `uchar`, `ucharn`, `short`, `shortn`, `ushort`, `ushortn`, `int`, `intn`, `uint`, `uintn`, `long`, `longn`, `ulong`, or `ulongn` (and optionally `double`, `doublen`). *S* is type `char`, `charn`, `short`, `shortn`, `int`, `intn`, `long`, or `longn`. *U* is type `uchar`, `ucharn`, `ushort`, `ushortn`, `uint`, `uintn`, `ulong`, or `ulongn`. **Optional extensions enable `double`, `doublen`, and `halfn` types.**

<code>int isequal (float x, float y)</code>	Compare of $x == y$
<code>intn isequal (floatn x, floatn y)</code>	
<code>int isequal (double x, double y)</code>	
<code>longn isequal (doublen x, doublen y)</code>	
<code>int isgreater (half x, half y)</code>	
<code>shortn isgreater (halfn x, halfn y)</code>	
<code>int islesseq (float x, float y)</code>	
<code>intn islesseq (floatn x, floatn y)</code>	
<code>int islesseq (double x, double y)</code>	
<code>longn islesseq (doublen x, doublen y)</code>	
<code>int islesseq (half x, half y)</code>	
<code>shortn islesseq (halfn x, halfn y)</code>	

<code>int isnotequal (float x, float y)</code>	Compare of $x \neq y$
<code>intn isnotequal (floatn x, floatn y)</code>	
<code>int isnotequal (double x, double y)</code>	
<code>longn isnotequal (doublen x, doublen y)</code>	
<code>int isnotequal (half x, half y)</code>	
<code>shortn isnotequal (halfn x, halfn y)</code>	
<code>int islessgreater (float x, float y)</code>	
<code>intn islessgreater (floatn x, floatn y)</code>	
<code>int islessgreater (double x, double y)</code>	
<code>longn islessgreater (doublen x, doublen y)</code>	
<code>int islessgreater (half x, half y)</code>	
<code>shortn islessgreater (halfn x, halfn y)</code>	
<code>int isnan (float)</code>	Test for finite value
<code>intn isnan (floatn)</code>	
<code>int isnan (double)</code>	
<code>longn isnan (doublen)</code>	
<code>int isnan (half)</code>	
<code>shortn isnan (halfn)</code>	

<code>int isgreater (float x, float y)</code>	Compare of $x > y$
<code>intn isgreater (floatn x, floatn y)</code>	
<code>int isgreater (double x, double y)</code>	
<code>longn isgreater (doublen x, doublen y)</code>	
<code>int isordered (float x, float y)</code>	
<code>intn isordered (floatn x, floatn y)</code>	
<code>int isordered (double x, double y)</code>	
<code>longn isordered (doublen x, doublen y)</code>	

<code>int isinf (float)</code>	
<code>intn isinf (floatn)</code>	
<code>int isinf (double)</code>	
<code>longn isinf (doublen)</code>	
<code>int isinf (half)</code>	
<code>shortn isinf (halfn)</code>	

<code>int isnan (float)</code>	
<code>intn isnan (floatn)</code>	
<code>int isnan (double)</code>	
<code>longn isnan (doublen)</code>	
<code>int isnan (half)</code>	
<code>shortn isnan (halfn)</code>	

<code>int isnormal (float)</code>	
<code>intn isnormal (floatn)</code>	
<code>int isnormal (double)</code>	
<code>longn isnormal (doublen)</code>	
<code>int isnormal (half)</code>	
<code>shortn isnormal (halfn)</code>	

<code>int isordered (float x, float y)</code>	
<code>intn isordered (floatn x, floatn y)</code>	
<code>int isordered (double x, double y)</code>	
<code>longn isordered (doublen x, doublen y)</code>	

OpenCL API 1.1 Quick Reference Card - Page 5

Image Read and Write Built-in Functions [6.11.13, 9.5, 9.6.8]

The built-in functions defined in this section can only be used with image memory objects created with `clCreateImage2D` or `clCreateImage3D`. `sampler` specifies the addressing and filtering mode to use. **H** = To enable `read_imageh` and `write_imageh`, enable extension `cl_khr_fp16`. **3D** = To enable type `image3d_t` in `write_image{f, i, ui}`, enable extension `cl_khr_3d_image_writes`.

`float4 read_imagef (image2d_t image, sampler_t sampler, int2 coord)`
`float4 read_imagef (image2d_t image, sampler_t sampler, float2 coord)`

`int4 read_imagei (image2d_t image, sampler_t sampler, int2 coord)`
`int4 read_imagei (image2d_t image, sampler_t sampler, float2 coord)`

`uint4 read_imageui (image2d_t image, sampler_t sampler, int2 coord)`
`uint4 read_imageui (image2d_t image, sampler_t sampler, float2 coord)`

`half4 read_imageh (image2d_t image, sampler_t sampler, int2 coord) H`
`half4 read_imageh (image2d_t image, sampler_t sampler, float2 coord) H`

`void write_imagef (image2d_t image, int2 coord, float4 color)`

`void write_imagei (image2d_t image, int2 coord, int4 color)`

`void write_imageui (image2d_t image, int2 coord, uint4 color)`

`void write_imageh (image2d_t image, int2 coord, half4 color) H`

`float4 read_imagef (image3d_t image, sampler_t sampler, int4 coord)`
`float4 read_imagef (image3d_t image, sampler_t sampler, float4 coord)`

`int4 read_imagei (image3d_t image, sampler_t sampler, int4 coord)`
`int4 read_imagei (image3d_t image, sampler_t sampler, float4 coord)`

Read an element from a 2D image

Write `color` value to `(x, y)` location specified by `coord` in the 2D image

Read an element from a 3D image

`uint4 read_imageui (image3d_t image, sampler_t sampler, int4 coord)`
`uint4 read_imageui (image3d_t image, sampler_t sampler, float4 coord)`

`int get_image_width (image2d_t image)`
`int get_image_width (image3d_t image)`

`int get_image_height (image2d_t image)`
`int get_image_height (image3d_t image)`

`int get_image_depth (image3d_t image)`

`int get_image_channel_data_type (image2d_t image)`
`int get_image_channel_data_type (image3d_t image)`

`int get_image_channel_order (image2d_t image)`
`int get_image_channel_order (image3d_t image)`

`int2 get_image_dim (image2d_t image)`

`int4 get_image_dim (image3d_t image)`

Use this pragma to enable type `image3d_t` in `write_image{f, i, ui}`:

`#pragma OPENCL EXTENSION cl_khr_3d_image_writes : enable`

`void write_imagef (image3d_t image, int4 coord, float4 color)`

`void write_imagei (image3d_t image, int4 coord, int4 color)`

`void write_imageui (image3d_t image, int4 coord, uint4 color)`

3D

3D

3D

Map and Unmap Image Objects [5.3.5]

```
void * clEnqueueMapImage (cl_command_queue command_queue, cl_mem image,  
                           cl_bool blocking_map, cl_map_flags map_flags,  
                           const size_t origin[3], const size_t region[3],  
                           size_t *image_row_pitch, size_t *image_slice_pitch,  
                           cl_uint num_events_in_wait_list, const cl_event *event_wait_list,  
                           cl_event *event, cl_int *errcode_ret)
```

Access Qualifiers [6.6]

Apply to image `image2d_t` and `image3d_t` types to declare if the image memory object is being read or written by a kernel. The default qualifier is `__read_only`.

`__read_only, read_only`
`__write_only, write_only`

clGetSamplerInfo [6.6]

```
cl_int clGetSamplerInfo (cl_sampler sampler,  
                        cl_sampler_info param_name,  
                        size_t param_value_size, void *param_value,  
                        size_t *param_value_size_ret)  
  
param_name: CL_SAMPLER_REFERENCE_COUNT,  
CL_SAMPLER_CONTEXT, FILTER_MODE,  
CL_SAMPLER_ADDRESSING_MODE,  
CL_SAMPLER_NORMALIZED_COORDS
```

OpenCL Course:

14:15-18:00 Room 308B-C

- Development Tips
- Graphics Interop
- Physical Simulations
- Visualisation Applications



Questions?

Derek Gerstmann
University of Western Australia
<http://local.wasp.uwa.edu.au/~derek>