Flow Control Instructions

Label

- Labels are needed in situations where one instruction refers to another
- Labels end with a colon
- Label usually placed on a line by themselves
- They refer to the instruction that follows

Conditional Jumps

- If the condition for the jump is true, the next instruction to be executed is the one at destination_label
- If the condition is false, the instruction immediately following the jump is done next
- Syntax

  Jump_instruction destination_label
- Range
  - The destination label must precede or follow the jump instruction no more than 126 bytes

Conditional Jump

- If the conditions for the jump instruction that is the combination of status flag settings are true, the CPU adjusts the IP to point to the destination label so that the instruction at this label will be executed next
- If the jump condition is false, then IP is not altered
- There are three categories of conditional jumps-
  - Signed conditional jumps
  - Unsigned conditional jumps
  - Single-flag jumps

Signed Conditional Jump

Symbol	Description	Condition for Jumps
JG/JNLE	Jump if greater than	ZF=0 and SF=OF
	Jump if not less than or equal to	
JGE/JNL	Jump if greater than or equal to	SF=OF
	Jump if not less than	
JL/JNGE	Jump if less than	SF<>OF
	Jump if not greater than or equal	
JLE/JNG	Jump if less than or equal	ZF=1 or SF<>OF
	Jump if not greater than	

Unsigned Conditional Jump

Symbol	Description	Condition for Jumps
JA/JNBE	Jump if above	CF=0 and ZF=0
	Jump if not below or equal	
JAE/JNB	Jump if above or equal	CF=0
	Jump if not below	
JB/JNAE	Jump if below	CF=1
	Jump if not above or equal	
JBE/JNA	Jump if below or equal	CF=1 or ZF=1
	Jump if not above	

Single-Flag Conditional Jump

Symbol	Description	Condition for Jumps
JE/JZ	Jump if equal	ZF=1
	Jump if equal to or zero	
JNE/JNZ	Jump if not equal	ZF=0
	Jump if not zero	
JC	Jump if carry	CF=1
JNC	Jump if no carry	CF=0
JO	Jump if overflow	OF=1
JNO	Jump if no overflow	OF=0
JS	Jump if sign negative	SF=1
JNS	Jump if nonnegative sign	SF=0
JP/JPE	Jump if parity even	PF=1
JNP/JPO	Jump if parity odd	PF=0

The CMP Instruction

- The jump condition is often provided by the CMP(compare) instruction
- Syntax

  CMP destination,source
- Does the compare by subtracting the source from the destination
- The result is not stored
- Only the flags are affected
- The operands of CMP may not both be memory locations
- Destination operand may not be a constant

Example

- Suppose AX and BX contain signed numbers. Write some code to put the biggest one in CX
- Solution

```
MOV CX,AX
CMP BX,CX
JLE NEXT
MOV CX,BX
NEXT:
```

The JMP instruction

- The JMP instruction causes an unconditional jump
- JMP can be used to get around the range restriction of a conditional jump
- Syntax

```
JMP destination_label
```

High-level Language Branching Structures

- IF-THEN
  - Syntax

    IF condition is true

        THEN

            execute true branch statements

    END_IF

Example of IF-THEN

- Replace the number in AX by its absolute value

Pseudocode Algorithm	**Assembly Code**
IF AX<0 THEN replace AX by –AX END_IF	CMP AX,0 JNL END_IF NEG AX END_IF:

High-level Language Branching Structures

- IF-THEN-ELSE
  - Syntax

```
IF condition is true
    THEN
        execute true branch statements
    ELSE
        execute false branch statements
END_IF
```

Example of IF-THEN-ELSE

- Suppose AL and BL contain extended ASCII characters. Display the one that comes first in the character sequence.

Pseudocode Algorithm	**Assembly Code**

IF AL<=BL
THEN
display the character in AL
ELSE
display the character in BL
END_IF
	MOV AH,2
	CMP AL,BL
	JNBE ELSE_
	MOV DL,AL
	JMP DISPLAY
	ELSE_:
	MOV DL,BL
	DISPLAY:
	INT 21H
	END_IF:

High-level Language Branching Structures

- CASE
  - It is a multiway branch structure that tests a register, variable or expression for particular values or range of values
  - Syntax

```
CASE expression
    values_1: statements_1
    values_2: statements_2
     .
     .
    values_n: statements_n
END_CASE
```

Example of CASE

- If AX contains a negative number, put -1 in BX; if AX contains 0,put 0 in BX; if AX contains a positive number, put 1 in BX

Pseudocode Algorithm	**Assembly Code**

CASE AX
  <0: put -1 in BX
  =0: put 0 in BX
  >0: put 1 in BX
END_CASE
	CMP AX,0
	JL NEGATIVE
	JE ZERO
	JG POSITIVE
	NEGATIVE:
	MOV BX,-1
	JMP END_CASE
	ZERO:
	MOV BX,0
	JMP END_CASE
	POSITIVE:
	MOV BX,1
	END_CASE:

High-level Language Branching Structures with Compound Conditions

- AND
  - An AND condition is true if and only if condition_1 and condition_2 are both true
  - Syntax

    condition_1 AND condition_2

High-level Language Branching Structures with Compound Conditions

- OR
  - An OR condition is true if at least one of condition between condition_1 and condition_2 are true
  - Syntax

    condition_1 OR condition_2