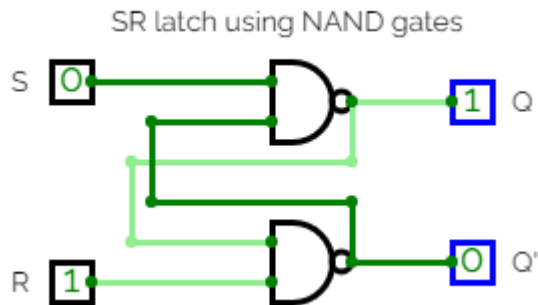
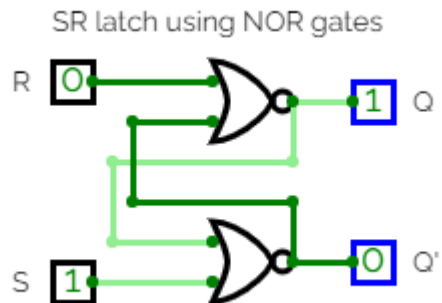


**Assignment-9**  
**Sambhav Kaushik | SK10 | 22220CMP023**

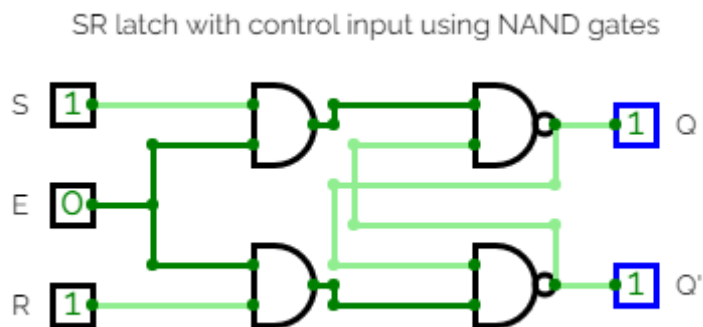
- **Problem 1:** Design a SR latch using NAND gates.



- **Problem 2:** Design SR latch using NOR gates.



- **problem 3:** Design SR latch with control input using NAND gates.



- **problem 4:** Design D flip flop using NAND gate.

D flip flop using NAND gate

