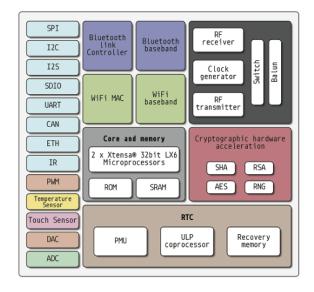
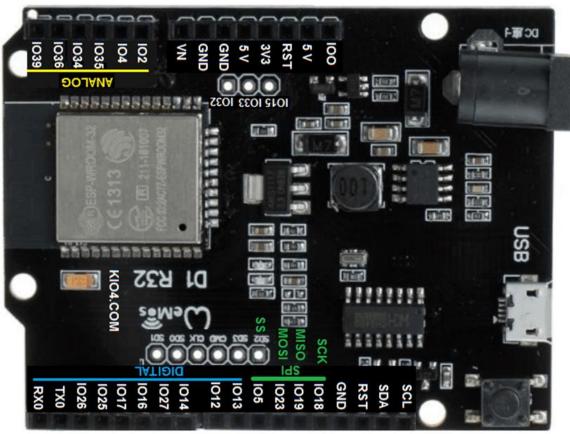
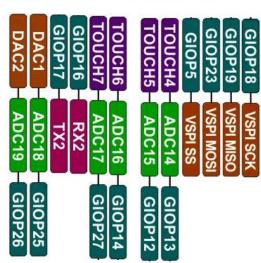


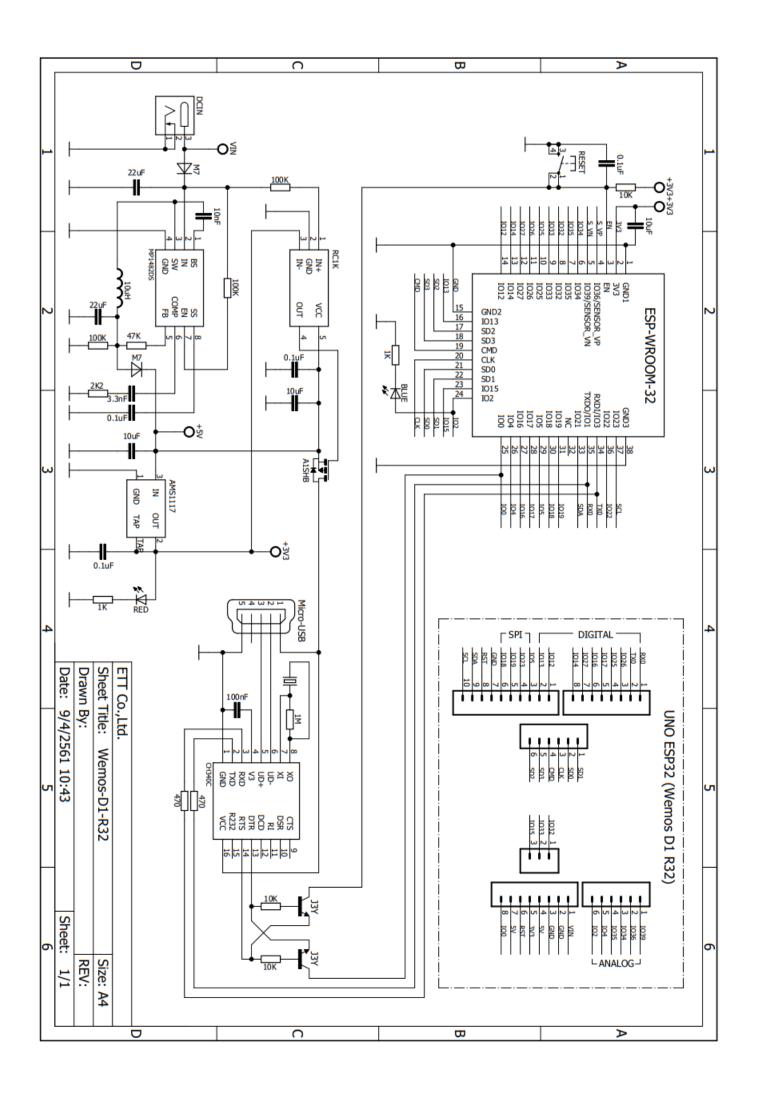
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PIN HEADER 1 – power supply pins, grounding and reset				
Position	Pin Name	Function		
1	VIN	DC Power In from Jack Connector		
2	GND	Ground		
3	GND	Ground		
4	5V	5V Power Out from DC/DC Regulator / 5V DC In from USB Connector		
5	3V3	3V3 DC Power Out, Main Power Supply of ESP32-WROOM-32 Module		
6	RST/EN	Chip-reset, active LOW / Chip-enable, active HIGH		
7	5V	5V Power Out from DC/DC Regulator / 5V DC In from USB Connector		
8	100	GPIO0, TOUCH1, ADC2_CH1, CLK_OUT1		

PIN HEADER 2 – analog input pins (ADCs, Touch)					
Position	Pin Name	Function			
1	1039	GPIO39, SENSOR_VN, ADC1_CH3, ADC_H, RTC_GPIO3			
2	1036	GPIO36, SENSOR_VP, ADC_H, ADC1_CH0, RTC_GPIO0			
3	1034	GPIO34, ADC1_CH6, RTC_GPIO4			
4	1035	GPIO35, ADC1_CH7, RTC_GPIO5			
5	1004	GPIO4, ADC2_CH0, TOUCH0, RTC_GPIO10, HSPIHD, HS2_DATA1, SD_DATA1, EMAC_TX_ER			
6	1002	GPIO2, ADC2_CH2, TOUCH2, RTC_GPIO12, HSPIWP, HS2_DATA0, SD_DATA0			

PIN HEADER 3 – digital I/O pins, analog output pins (DACs) and UART interface				
Position	Pin Name	Function		
1	RX0	GPIO3, <b>U0RXD</b> , CLK_OUT2		
2	TX0	GPIO1, <b>U0TXD</b> , CLK_OUT3, EMAC_RXD2		
3	1026	GPIO26, DAC_2, ADC2_CH9, RTC_GPIO7, EMAC_RXD1		
4	1025	GPIO25, DAC_1, ADC2_CH8, RTC_GPIO6, EMAC_RXD0		
5	IO17	GPIO17, HS1_DATA5, <mark>U2TXD</mark> , EMAC_CLK_OUT_180		
6	IO16	GPIO16, HS1_DATA4, <b>U2RXD</b> , EMAC_CLK_OUT		
7	1027	GPIO27, ADC2_CH7, TOUCH7, RTC_GPIO17, EMAC_RX_DV		
8	IO14	GPIO14, ADC2 CH6, TOUCH6, RTC GPIO16, MTMS, HSPICLK, HS2 CLK, SD CLK, EMAC TXD2		

PIN HEADER 4 – digital I/O pins, SPI interface and I2C interface				
Position	Pin Name	Function		
1	IO12	GPIO12, ADC2_CH5, TOUCH5, RTC_GPIO15, MTDI, HSPIQ,HS2_DATA2, SD_DATA2, EMAC_TXD3		
2	IO13	GPIO13, ADC2_CH4, TOUCH4, RTC_GPIO14, MTCK, HSPID, HS2_DATA3, SD_DATA3, EMAC_RX_ER		
3	1005	GPIO5, VSPICSO, HS1_DATA6, EMAC_RX_CLK		
4	1023	GPIO23, VSPID, HS1_STROBE		
5	IO19	GPIO19, VSPIQ, UOCTS, EMAC_TXD0		
6	IO18	GPIO18, VSPICLK, HS1_DATA7		
7	GND	Ground		
8	RST/EN	Chip-reset, active LOW / Chip-enable, active HIGH		
9	SDA	GPIO21, VSPIHD, EMAC_TX_EN		
10	SCL	GPIO22, VSPIWP, UORTS, EMAC_TXD1		