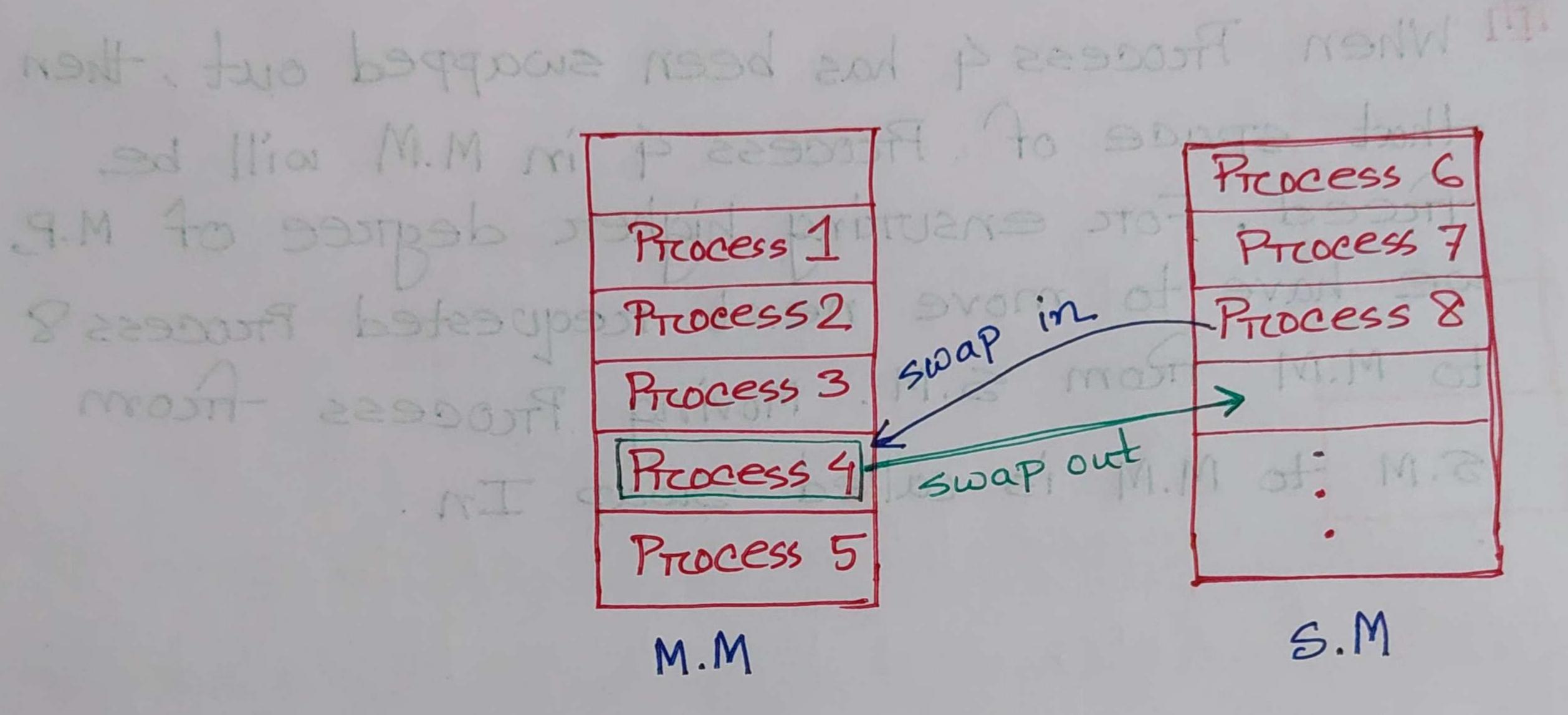
Swapping Island

Up space in RAM for process that needs.



- Degree of multiprogramming refers to the number of programs/processes that can be con-currently loaded into M.M and tready to execute.
- > Higher Degree of multi-P. > higher number of processes resides in M.M waiting for CPU execution.
- => Higher Degree of M.P ensures proper CPU
 resource utilization.

Fraces 4 has completed its execution on one goes fore I/o operation, then Process 4 must be moved fore M.M to S.M which is called swap out

When Process 4 has been swapped out, then that space of Process 4 in M.M. will be freed. Forcensuring higher degree of M.P., we have to move next requested Process 8 to M.M. from S.M. Moving Process from 5.M. to M.M. is called swap In.

M.a

M.M

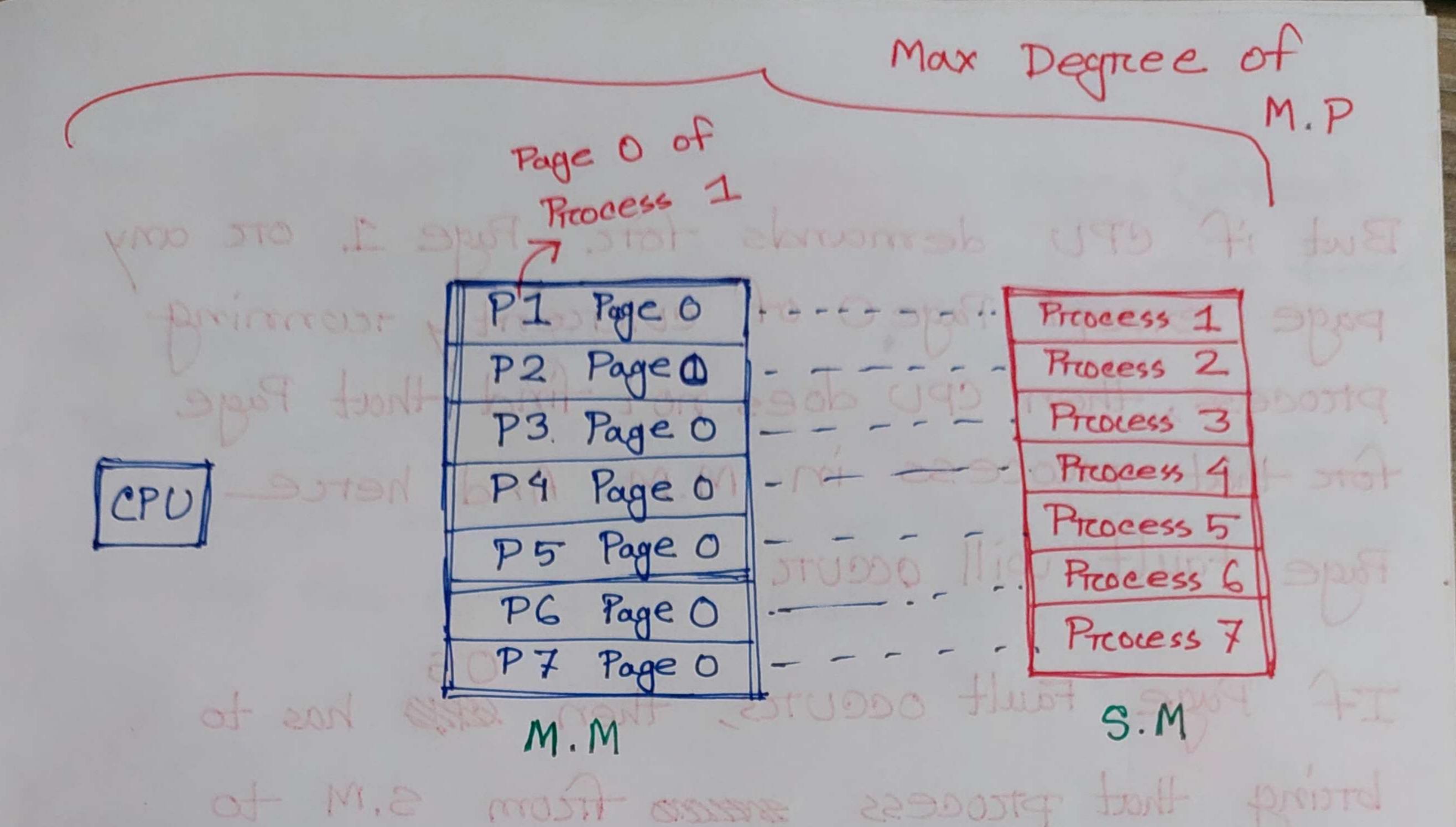
Degree of multiprogramming rectors to the number of programs processes that can be con-concretly loaded into M.M. and recordy to execute tigher Degree of multi-P. I higher number of processes resides in M.M. waiting for CPO executing tigher Degree of M.P. ensures proper CPO executing tigher Degree of M.P. ensures proper CPO executing tigher Degree of M.P. ensures proper CPO executions resource of tigher of M.P. ensures proper CPO

In Viretual Memorry

VM preorides the preogrammere an illusion of a largere M.M than physically available.

VM gives an illusion that a process whose size is larger than the physical size of M.M can also be executed in the system.

The CPU. Illusion of infinite of any numbers of processes of any sizes can be executed in the CPU.



As size of M.M (RAM) is limited, we can not strong all process resides in S.M. But if we want to maximize the degree of multi-programming, we have to storce as many as possible processes in the main memory. We can solve this by storing some portion of almost all processes in the Main Memory from S.M.

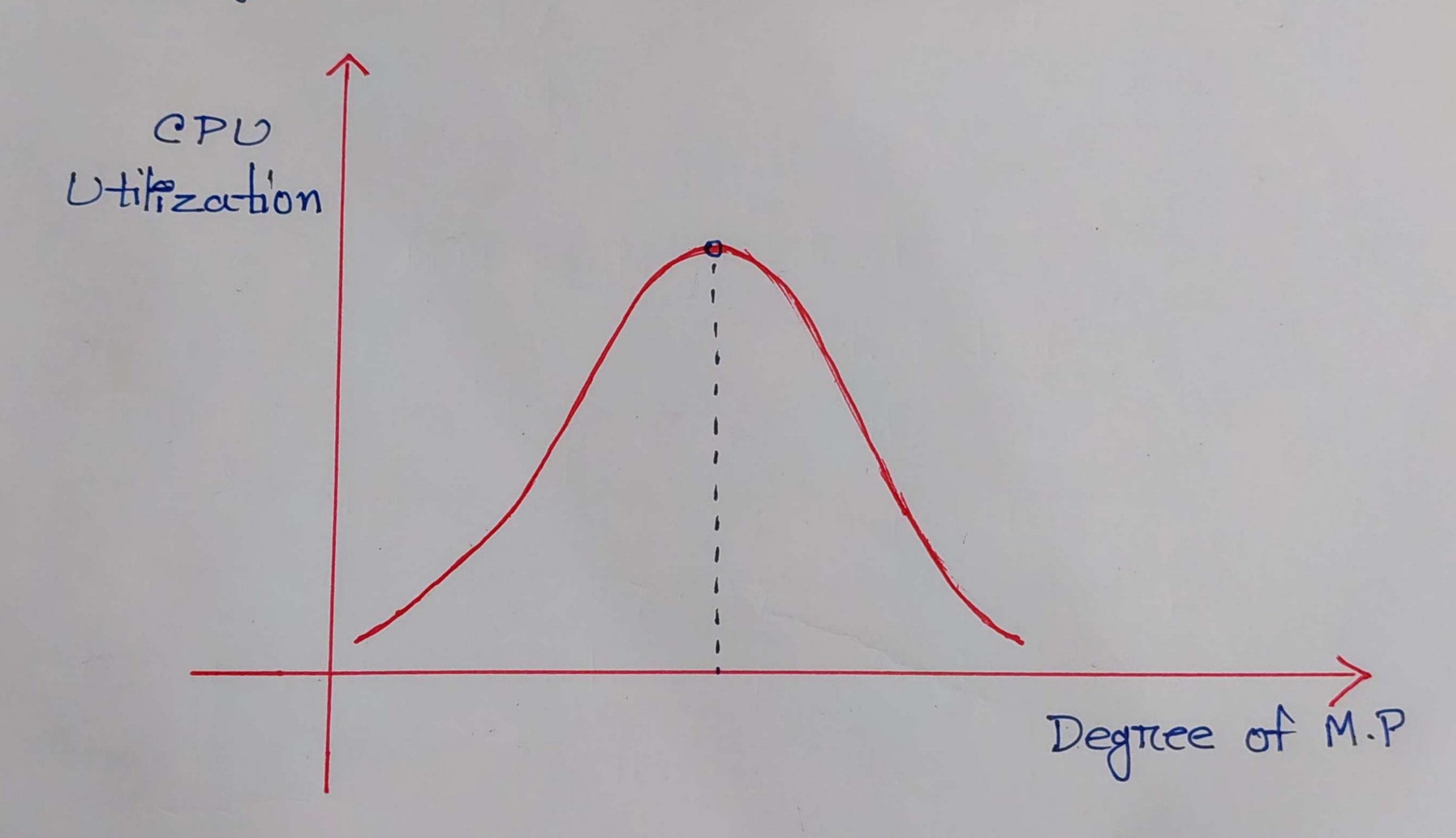
Force example, we can storce Page o of all Processes in M.M. from S.M. 50, CPU feels like all Processes are in M.M. It can call any waiting Processes force execution.

But if CPU demands forc Page 1 orc any Page except Page o of contrently rooming Ptrocess, then CPU does not find that Page tore that preocess in M.M. And herce Page Fault will occur If Page Fault occurres, then was to bring that process menon from S.M to M.M. This process takes a lot of time in terms of CPU, and this time is called Page Fault Serctvice Time Move to store an amount on store of sylvin

TO SOMPSOT WOM

Now if number of reguests for pages (without page 0) increases, 0s must spend more times to bring that page from S.M to M.M.

That means morre page fault will occur and OS spends morre times (page Fault Service Time). During this time, CPU seats idle.



o fi 1
Invalid bit
Invalid

o means
invalid
invalid
invalid
found
found
found
page

P.T

M.M

If CPU wants from e for Page 1 then CPU finds 0 in the valid/invalid bit porction of page 1. So CPU does not find that page and a Page Fault occurs. When Page Fault happens, it generates an intercupt called Trap.

When trap is generated, system control will be switched from user to OS.

OS checks the authenticity of the user who requested fore that page. That means OS checks whether the users is peremitted to access that page.